

General Description

The MAX7376 silicon oscillator with reset, is a replacement for ceramic resonators, crystals, crystal oscillator modules, and discrete reset circuits. The MAX7376 provides the primary clock source and reset function for microcontrollers in 3V, 3.3V, and 5V applications. The MAX7376 features a factory-programmed high-speed oscillator and a microprocessor (µP) power-on-reset (POR) supervisor.

Unlike typical crystal and ceramic resonator oscillator circuits, the MAX7376 is resistant to vibration and EMI. The high-output-drive current and absence of highimpedance nodes make the oscillator less susceptible to dirty or humid operating conditions. With a wide operating temperature range as standard, the MAX7376 is a good choice for demanding home appliance, industrial, and automotive environments.

The MAX7376 is available in factory-programmed frequencies from 32.768kHz to 10MHz. See Table 2 for standard frequencies and contact the factory for custom frequencies.

The MAX7376 is available in a 5-pin SOT23 package. Refer to the MAX7382 data sheet for frequencies ≥10MHz. The MAX7376 standard operating temperature range is -40°C to +125°C. See the Applications Information section for the extended operating temperature range.

Applications

White Goods

Automotive

Consumer Products

Appliances and Controls

Handheld Products

Portable Equipment

Microcontroller (µC) Systems

Typical Application Circuit appears at end of data sheet.

Features

- ♦ 2.7V to 5.5V Operation
- Accurate Oscillator: 600kHz to 10MHz or 32kHz
- Reset Output Holds the μC in Reset for 100μs After Clock Startup
- ♦ ±10mA Clock Output Drive Capability
- ♦ 2% Initial Accuracy
- **♦** ±50ppm/°C Temperature Coefficient
- ♦ 50% Duty Cycle
- ♦ 5ns Output Rise and Fall Time
- ◆ Low Jitter: 160ps_{P-P}) at 8MHz (No PLL)
- ♦ 3mA Operating Current (8MHz)
- ♦ 13µA Operating Current (32kHz)
- -40°C to +125°C Temperature Range

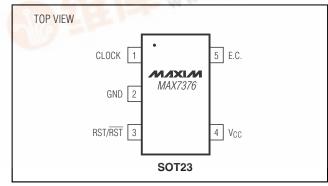
Ordering Information

PART	TEMP RANGE	PIN- PACKAGE	RESET OUTPUT	
MAX7376CT	-40°C to +125°C	5 SOT23-5	Open drain	
MAX7376AT	-40°C to +125°C	5 SOT23-5	Active high push-pull	
MAX7376BT	-40°C to +125°C	5 SOT23-5	Active low push-pull	

Standard version is shown in bold.

The first letter after the part number designates the reset output option. Insert the letter corresponding to the desired reset threshold level from Table 1 in the next position. Insert the twoletter code from Table 2 in the remaining two positions for the desired frequency range. See Table 3 for standard part numbers.

Pin Configuration



ABSOLUTE MAXIMUM RATINGS

V _{CC} to GND0.3V to +6V	Operating Temperature Range40°C to +135°C
All Other Pins to GND0.3V to (V _{CC} + 0.3V)	Junction Temperature+150°C
CLOCK Current±10mA	Storage Temperature Range60°C to +150°C
Continuous Power Dissipation (T _A = +70°C)	Lead Temperature (soldering, 10s)+300°C
5-Pin SOT23	
(derate 7.1mW/°C above +70°C)571mW (U5 - 2)	

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

ELECTRICAL CHARACTERISTICS

 $(V_{CC} = 2.7V \text{ to } 5.5V, T_A = -40^{\circ}\text{C} \text{ to } +125^{\circ}\text{C}, \text{ unless otherwise noted.}$ Typical values are at $V_{CC} = 5V$ and $T_A = +25^{\circ}\text{C}.)$ (Note 1)

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS	
Operating Supply Voltage	Vcc		2.7		5.5	V	
Operation County Course	1	fCLOCK = 8MHz, no load		3	5	mA	
Operating Supply Current	Icc	fCLOCK = 32.768kHz, no load		13	25	μΑ	
CLOCK OUTPUT							
Minimum Operating Voltage				1.65		V	
Outrout High Valtage	V	V _{CC} = 4.5V, I _{SOURCE} = 9mA	V _C C - 0.4				
Output High Voltage	Voh	V _{CC} = 2.7V, I _{SOURCE} = 2.5mA	V _C C - 0.4			V	
Output Law Valtage	1/0:	V _{CC} = 4.5V, I _{SINK} = 20mA	0.4		V		
Output Low Voltage	V _{OL}	V _{CC} = 2.7V, I _{SINK} = 10mA			0.4]	
Initial Frequency Accuracy	fcLock	$V_{CC} = 5V$, $T_A = +25^{\circ}C$ (Note 2)			+2	%	
Initial Frequency Accuracy	ICLOCK	$V_{CC} = 2.7V \text{ to } 5.5V, T_A = +25^{\circ}C$	-4		+4	70	
Frequency Temperature Sensitivity		(Note 3)		±50	±325	ppm/°C	
Initial Frequency Accuracy	for oor	V _{CC} = 5V, T _A = +25°C (Note 2)	32.440	32.768	33.096	Ld I=	
(32.768kHz)	fclock	$V_{CC} = 2.7V \text{ to } 5.5V, T_A = +25^{\circ}C$ 31.785			33.751	kHz	
Frequency Temperature Sensitivity		32.768kHz (Note 3)		±50	±325	ppm/°C	
CLOCK Output Duty Cycle			43	50	57	%	
CLOCK Output Jitter		Observation of 8MHz output for 20s using a 500MHz oscilloscope		160		ps _{P-P}	
CLOCK Output Rise Time	t _R	10% to 90%		5		ns	
CLOCK Output Fall Time	t⊨	90% to 10%		5		ns	

ELECTRICAL CHARACTERISTICS (continued)

 $(V_{CC} = 2.7 \text{V to } 5.5 \text{V}, T_A = -40 ^{\circ} \text{C to } +125 ^{\circ} \text{C}, \text{ unless otherwise noted.}$ Typical values are at $V_{CC} = 5 \text{V}$ and $T_A = +25 ^{\circ} \text{C}.)$ (Note 1)

			• •				
PARAMETER	SYMBOL		CONDITIONS	MIN	TYP	MAX	UNITS
RST/RST OUTPUT							
Reset Threshold	V	V vicin v	T _A = +25°C	V _{TH} - 2%		V _{TH} + 2%	V
	V _{TH+}	V _{CC} rising	$T_A = -40^{\circ}C \text{ to } +125^{\circ}C$	V _{TH} - 5%		V _{TH} + 5%	
Reset Hysteresis	V _{TH} YS	V _H YST = {(V _{TH+}) - (V _{TH-})}/(V _{TH-}) x 100%			2.0		%
POR Delay		V _{CC} rising from 0V to (V _{TH} + 200mV) in 1µs			100		μs
Output High Voltage	Vari	$V_{CC} = 4.5V, I_{S}$	SOURCE = 9mA	V _C C - 0.4	1		V
	Voh	V _{CC} = 2.7V, I _{SOURCE} = 2.5mA		VCC - 0.4	1		'
Output Low Voltage	\/-·	V _{CC} = 4.5V, I _{SINK} = 20mA				0.4	V
	V _{OL}	$V_{CC} = 2.7V$, $I_{SINK} = 10$ mA				0.4	V
Leakage Current	ILEAK	V _{CC} = 5.5V, V _{RST} = 5.5V, open drain				1	μΑ

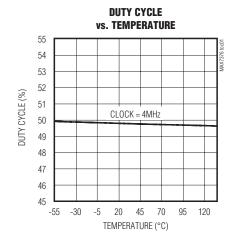
Note 1: All parameters are tested at $T_A = +25^{\circ}C$. Specifications over temperature are guaranteed by design.

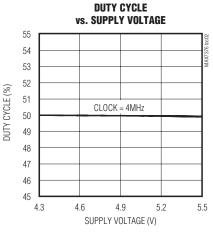
Note 2: The frequency is determined by part number selection. See Table 3.

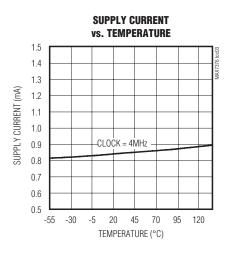
Note 3: Guaranteed by design. Not production tested.

_Typical Operating Characteristics

 $(V_{CC} = 5V, T_A = +25^{\circ}C, unless otherwise noted.)$

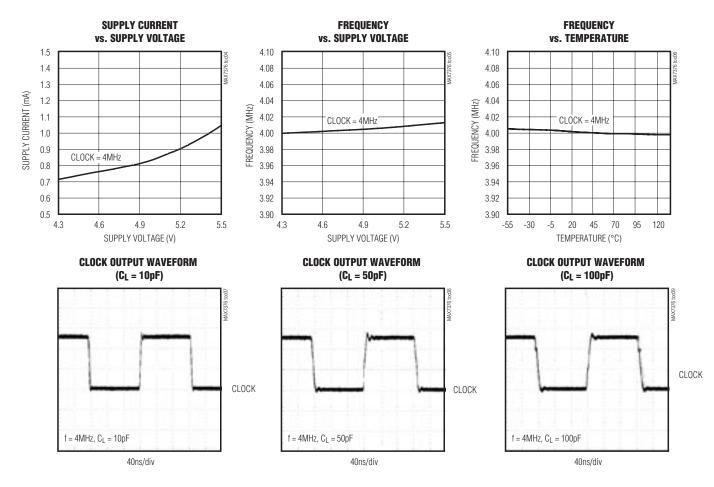


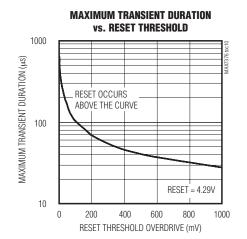




Typical Operating Characteristics (continued)

 $(V_{CC} = 5V, T_A = +25^{\circ}C, unless otherwise noted.)$





Pin Description

PIN	NAME	FUNCTION		
1	CLOCK	Push-Pull Clock Output		
2	GND	Ground		
3	RST	leset Output. See the RST Reset Output Options section for more details.		
4	Vcc	ositive Supply Voltage. Bypass V _{CC} to GND with a 0.1µF capacitor.		
5	E.C.	Must Be Externally Connected to V _{CC}		

Detailed Description

The MAX7376 is a clock generator with integrated reset for µCs and UARTs in 3V, 3.3V, and 5V applications (Figure 1). The MAX7376 is a replacement for crystal oscillator modules, crystals, or ceramic resonators and a system-reset IC. The clock frequency is factory trimmed to specific values. A variety of popular standard frequencies are available (Table 2). No external components are required for setting or adjusting the frequency.

Supply Voltage

The MAX7376 has been designed for use in systems with nominal supply voltages of 3V, 3.3V, or 5V and is specified for operation with supply voltages in the 2.7V to 5.5V range. See the *Absolute Maximum Ratings* section for limit values of power-supply and pin voltages.

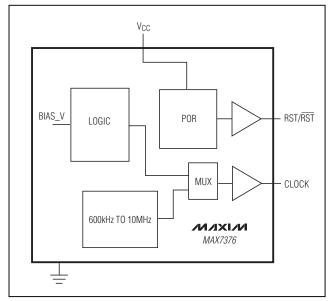


Figure 1. Functional Diagram

Oscillator

The clock output is a push-pull configuration and is capable of driving a ground-connected 500Ω or a positive-supply-connected 250Ω load to within 400mV of either supply rail. The clock output remains stable over the full operating voltage range and does not generate short output cycles during either power-on or power-off. A typical startup characteristic is shown in the *Typical Operating Characteristics*.

_**Applications Information** Interfacing to a µC Clock Input

The MAX7376 clock output is a push-pull, CMOS, logic output that directly drives any μP or μC clock input. There are no impedance-matching issues when using the MAX7376. The MAX7376 is not sensitive to its position on the board and it does not need to be placed right next to the μP . Refer to the μC data sheet for clock-input compatibility with external clock signals. The MAX7376 requires no biasing components or load capacitance. When using the MAX7376 to retrofit a crystal oscillator, remove all biasing components from the oscillator input.

RST Reset Output Options

The MAX7376 is available with three reset output stage options: push-pull with active-low output, push-pull with active-high output, and open drain with active-low output. The RST output is asserted when the monitored input (VCC) drops below the internal VTH- threshold and remains asserted for 100 μ s after the monitored input exceeds the internal VTH+ threshold. The open-drain RST output requires an external pullup resistor.

Output Jitter

The MAX7376's jitter performance is given in the *Electrical Characteristics* table as a peak-to-peak value obtained by observing the output of the MAX7376 for 20s with a 500MHz oscilloscope. Jitter values are approximately proportional to the period of the output frequency of the device. Thus, a 4MHz part has approximately twice the jitter value of an 8MHz part. The jitter performance of clock sources degrades in the presence of mechanical and electrical interference.

The MAX7376 is relatively immune to vibration, shock, and EMI influences, and thus provides a considerably more robust clock source than crystal or ceramic resonator-based oscillator circuits.

Initial Power-Up and Operation

The clock starts up within 30µs (typ) after V_{CC} has risen above the reset threshold. The reset output remains asserted for 100µs after the reset threshold is reached.

Extended Temperature Operation

The MAX7376 was tested to +135°C during product characterization and shown to function normally at this temperature (see the *Typical Operating Characteristics*). However, production test and qualification is only performed from -40°C to +125°C at this time. Contact the factory if operation outside this range is required.

Power-Supply Considerations

The MAX7376 operates with a 2.7V and 5.5V power-supply voltage. Good power-supply decoupling is needed to maintain the power-supply rejection performance of the MAX7376. Bypass VCC to GND with a 0.1 μ F surface-mount ceramic capacitor. Mount the bypass capacitor as close to the device as possible. If possible, mount the MAX7376 close to the μ C's decoupling capacitor so that additional decoupling is not required. A larger value bypass capacitor is recommended if the MAX7376 is to operate with a large capacitive load. Use a bypass capacitor value of at least 1000 times that of the output load capacitance.

Table 1. Standard Reset Threshold Levels

SUFFIX	RESET THRESHOLD (V)			
R	2.57			
М	4.29			

For all other reset threshold options, contact factory.

Table 2. Standard Frequencies

SUFFIX	STANDARD FREQUENCY (MHz)			
AA	32.768kHz			
MG	1			
ОК	1.8432			
QT	3.39545			
QW	3.6864			
RD	4			
RH	4.1943			
TP	8			

For all other reset threshold options, contact factory.

Typical Application Circuit

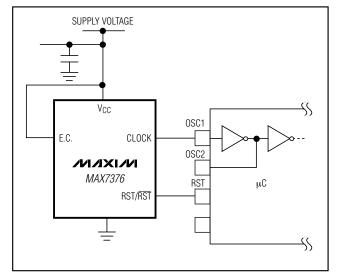


Table 3. Standard Part Numbers

PART	PIN-PACKAGE	RESET THRESHOLD (V)	FREQUENCY (Hz)	TOP MARK
MAX7376CRAA	5 SOT23	2.57	32.768k	AEUZ
MAX7376CRMG	5 SOT23	2.57	1M	AEUN
MAX7376CROK	5 SOT23	2.57	1.8432M	AEUJ
MAX7376CRQT	5 SOT23	2.57	3.39545M	AETT
MAX7376CRQW	5 SOT23	2.57	3.6864M	AETU
MAX7376CRRD	5 SOT23	2.57	4M	AETV
MAX7376CRRH	5 SOT23	2.57	4.1943M	AETW
MAX7376CRTP	5 SOT23	2.57	8M	AESF
MAX7376CMMG	5 SOT23	4.29	1M	AEUM
MAX7376CMOK	5 SOT23	4.29	1.8432M	AEUI
MAX7376CMQT	5 SOT23	4.29	3.39545M	AESH
MAX7376CMQW	5 SOT23	4.29	3.6864M	AETP
MAX7376CMRD	5 SOT23	4.29	4M	AESJ
MAX7376CMRH	5 SOT23	4.29	4.1943M	AETR
MAX7376CMTP	5 SOT23	4.29	8M	AETS

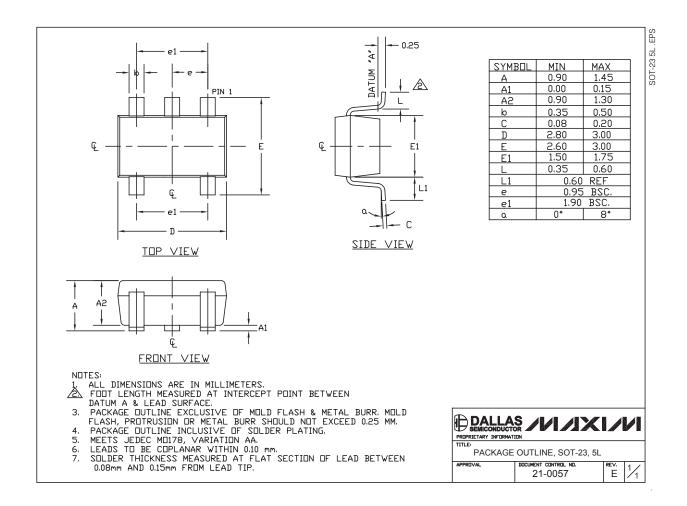
_Chip Information

TRANSISTOR COUNT: 2027

PROCESS: BiCMOS

Package Information

(The package drawing(s) in this data sheet may not reflect the most current specifications. For the latest package outline information go to www.maxim-ic.com/packages.)



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