



# Si9987

Vishay Siliconix

## Buffered H-Bridge

### FEATURES

- 1.0-A H-Bridge
- 500-kHz Switching Rate
- Shoot-Through Limited
- TTL Compatible Inputs
- 3.8- to 13.2-V Operating Range
- Surface Mount Packaging

### APPLICATIONS

- VCM Driver
- Brushed Motor Driver
- Stepper Motor Driver
- Power Converter
- Optical Disk Drives
- Power Supplies
- High Performance Servo

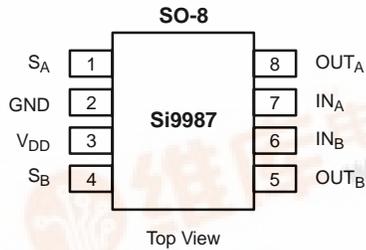
### DESCRIPTION

The Si9987 is an integrated, buffered H-bridge with TTL compatible inputs and the capability of delivering a continuous 1.0 A @  $V_{DD} = 5.0\text{ V}$  (room temperature) at switching rates up to 500 kHz. Internal logic prevents the upper and lower outputs of either half-bridge from being turned on simultaneously. Unique input codes allow both outputs to be forced low (for braking) or

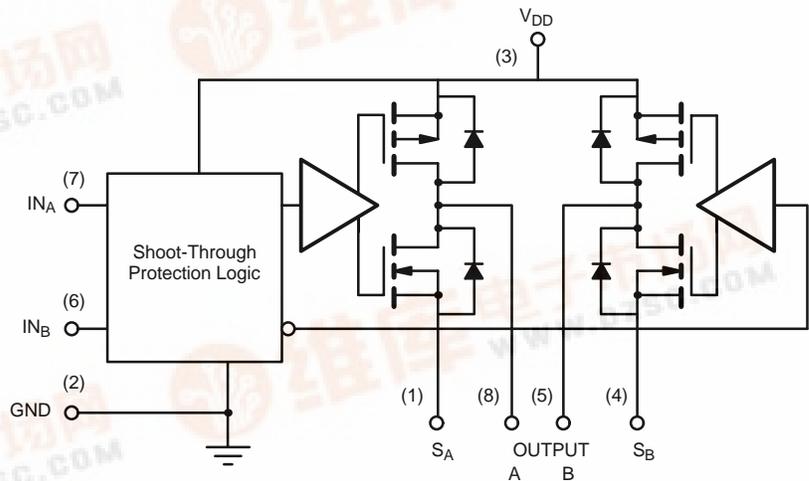
forced to a high impedance level.

The Si9987 is available in an 8-Pin SOIC package, specified to operate over a voltage range of 3.8 V to 13.2 V, and the commercial temperature range of 0 to 70°C (C suffix) and -40 to 85°C (D suffix).

### FUNCTIONAL BLOCK DIAGRAM, PIN CONFIGURATION AND TRUTH TABLE



TRUTH TABLE			
IN <sub>A</sub>	IN <sub>B</sub>	OUT <sub>A</sub>	OUT <sub>B</sub>
1	0	1	0
0	1	0	1
0	0	0	0
1	1	HiZ	HiZ



### ORDERING INFORMATION

Part Number	Temperature Range	Package
Si9987CY-T1	0 to 70°C	Tape and Reel
Si9987DY-T1	-40 to 85°C	
Si9987CY	0 to 70°C	Bulk (tubes)
Si9987DY	-40 to 85°C	



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### ABSOLUTE MAXIMUM RATINGS<sup>a</sup>

Voltage on any pin with respect to ground	−0.3 V to V <sub>DD</sub> +0.3 V
Voltage on pins 5, 8 with respect to GND	−1 V to V <sub>DD</sub> +1 V
Voltage on pins 1, 4	−0.3 V to GND +1 V
Maximum V <sub>DD</sub>	15 V
Peak Output Current	1.5 A
Storage Temperature	−65 to 150°C
Maximum Junction Temperature (T <sub>J</sub> )	150°C
Power Dissipation <sup>b</sup>	1 W
θ <sub>JA</sub>	100°C/W

Continuous I <sub>OUT</sub> Current (T <sub>J</sub> = 135°C) <sup>c</sup>	
T <sub>A</sub> = 25°C	± 1.02 A
T <sub>A</sub> = 70°C	± 0.75 A
T <sub>A</sub> = 85°C	± 0.65 A
Operating Temperature Range	
Si9987CY	0 to 70°C
Si9987DY	−40 to 85°C

- Notes
- Device mounted with all leads soldered or welded to PC board.
  - Derate 10 mW/°C above 25°C.
  - T<sub>J</sub> = T<sub>A</sub> + (P<sub>D</sub> × θ<sub>JA</sub>), P<sub>D</sub> = Power Dissipation.

### RECOMMENDED OPERATING RANGE

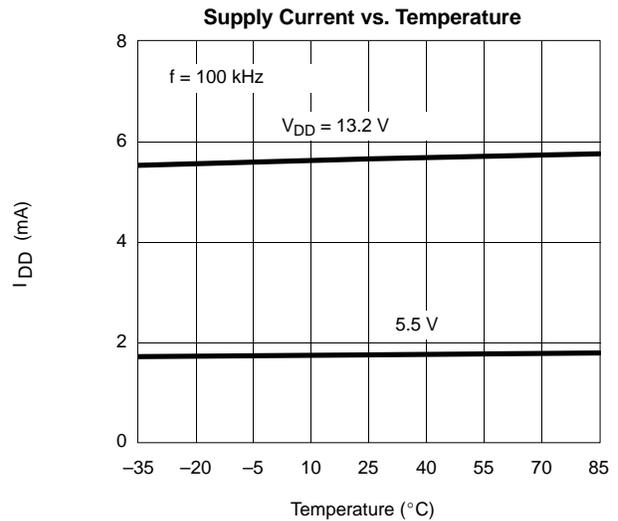
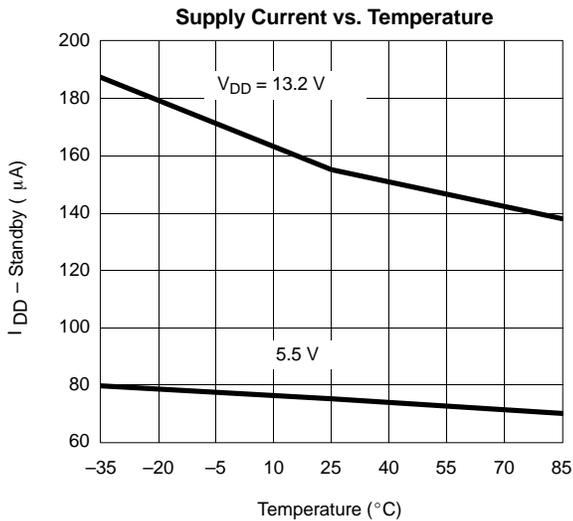
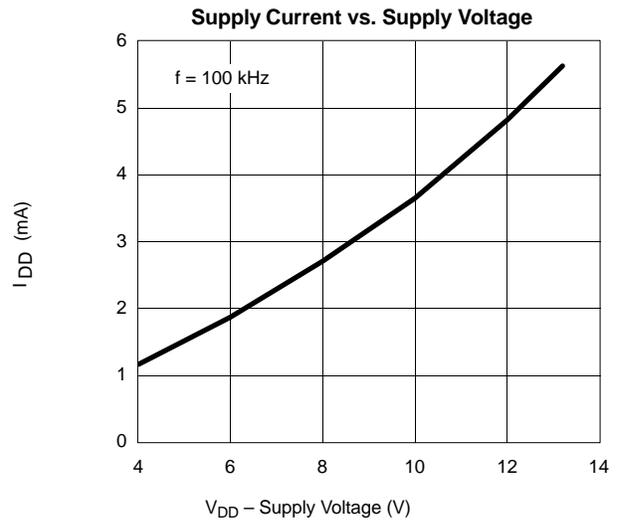
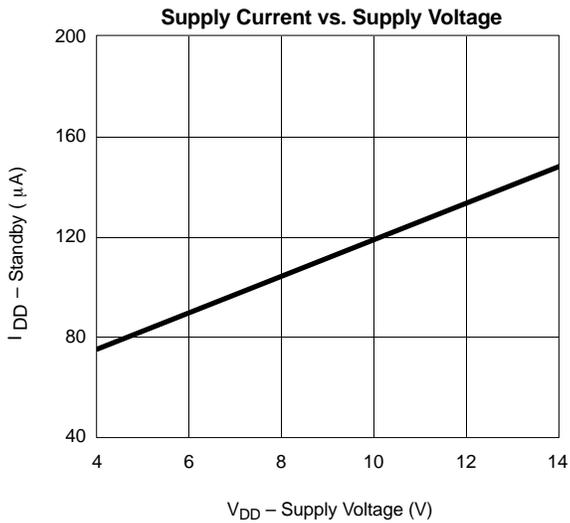
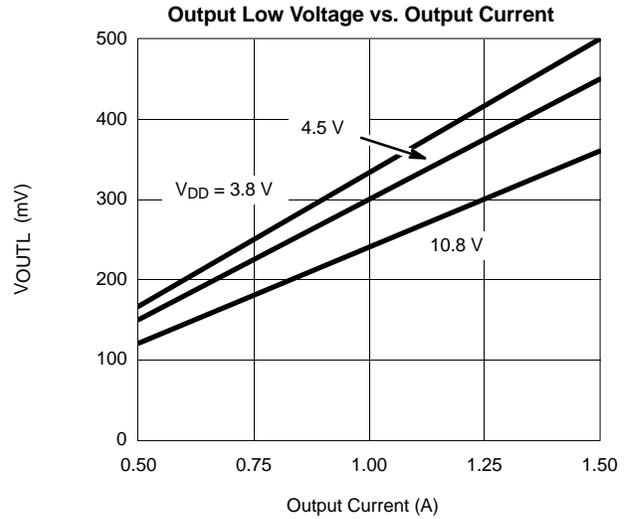
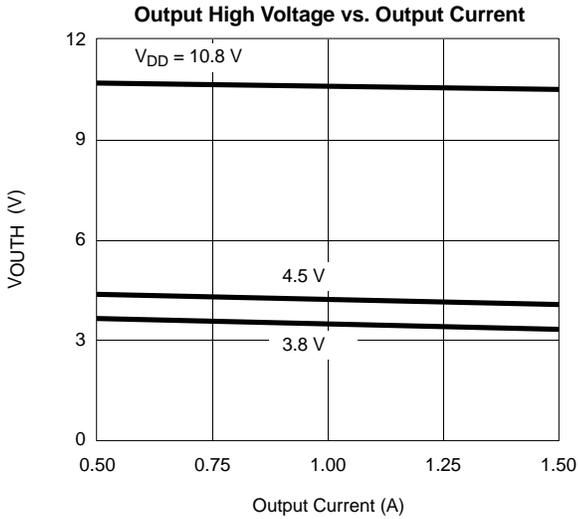
V <sub>DD</sub>	3.8 V to 13.2 V
Maximum Junction Temperature (T <sub>J</sub> )	135°C

SPECIFICATIONS							
Parameter	Symbol	Test Conditions Unless Specified V <sub>DD</sub> = 3.8 to 13.2 V S <sub>A</sub> @ GND, S <sub>B</sub> @ GND		Limits			Unit
				Min <sup>a</sup>	Typ <sup>b</sup>	Max <sup>a</sup>	
<b>Input</b>							
Input Voltage High	V <sub>INH</sub>		2				V
Input Voltage Low	V <sub>INL</sub>				1		
Input Current with Input Voltage High	I <sub>INH</sub>	V <sub>IN</sub> = 2 V			1		μA
Input Current with Input Voltage Low	I <sub>INL</sub>	V <sub>IN</sub> = 0 V	−1				
<b>Output</b>							
Output Voltage High <sup>c</sup>	V <sub>OUTH</sub>	I <sub>OUT</sub> = −1 A	V <sub>DD</sub> = 10.8 V	10.40	10.56		V
			V <sub>DD</sub> = 4.5 V	4.00	4.20		
		I <sub>OUT</sub> = −500 mA	V <sub>DD</sub> = 10.8 V	10.60	10.68		
			V <sub>DD</sub> = 4.5 V	4.25	4.35		
I <sub>OUT</sub> = −300 mA, V <sub>DD</sub> = 3.8 V		3.63	3.70				
Output Voltage Low <sup>c</sup>	V <sub>OUTL</sub>	I <sub>OUT</sub> = 1 A	V <sub>DD</sub> = 10.8 V		0.24	0.40	
			V <sub>DD</sub> = 4.5 V		0.30	0.50	
		I <sub>OUT</sub> = 500 mA	V <sub>DD</sub> = 10.8 V		0.12	0.20	
			V <sub>DD</sub> = 4.5 V		0.15	0.25	
I <sub>OUT</sub> = 300 mA, V <sub>DD</sub> = 3.8 V			0.10	0.17			
Output Leakage Current Low	I <sub>OLL</sub>	I <sub>NA</sub> = I <sub>NB</sub> ≥ 2 V, V <sub>OUT</sub> = V <sub>DD</sub> = 13.2 V		0	10		μA
Output Leakage Current High	I <sub>OLH</sub>	V <sub>OUT</sub> = 0, V <sub>DD</sub> = 13.2 V	−10	0			
Output V Clamp High	V <sub>CLH</sub>	I <sub>NA</sub> = I <sub>NB</sub> ≥ 2 V	I <sub>OUT</sub> = 100 mA		V <sub>DD</sub> +0.7	V <sub>DD</sub> +0.9	V
Output V Clamp Low	V <sub>CLL</sub>		I <sub>OUT</sub> = −100 mA	−0.9	−0.7		
<b>Supply</b>							
V <sub>DD</sub> Supply Current	I <sub>DD</sub>	I <sub>N</sub> = 100 kHz, V <sub>DD</sub> = 5.5 V		1.8	2.5	mA	
		I <sub>NA</sub> = I <sub>NB</sub> = 4.5 V, V <sub>DD</sub> = 5.5 V		75	125	μA	
<b>Dynamic</b>							
Propagation Delay Time	T <sub>PLH</sub>	V <sub>DD</sub> = 5 V		300		nS	
	T <sub>PHL</sub>			100			

- Notes
- The algebraic convention whereby the most negative value is a minimum and the most positive a maximum, is used in this data sheet.
  - Typical values are for DESIGN AID ONLY, not guaranteed nor subject to production testing.
  - Maximum value measured at T<sub>J</sub> = 135°C. Typical value measured at T<sub>J</sub> = T<sub>A</sub> = 25°C (pulse width ≤ 300 μsec, duty cycle ≤ 2%).



**TYPICAL CHARACTERISTICS (25°C UNLESS NOTED)**





**TYPICAL CHARACTERISTICS (25 °C UNLESS NOTED)**

