

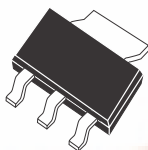
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CZS5064

SILICON CONTROLLED RECTIFIER



SOT-223 CASE

Central
Semiconductor Corp.

DESCRIPTION

The CENTRAL SEMICONDUCTOR CZS5064 type is an epoxy molded PNP Silicon Controlled Rectifier manufactured in an epoxy molded surface mount package, designed for control systems and sensing circuit applications.

MAXIMUM RATINGS (T_A=25°C unless otherwise noted)

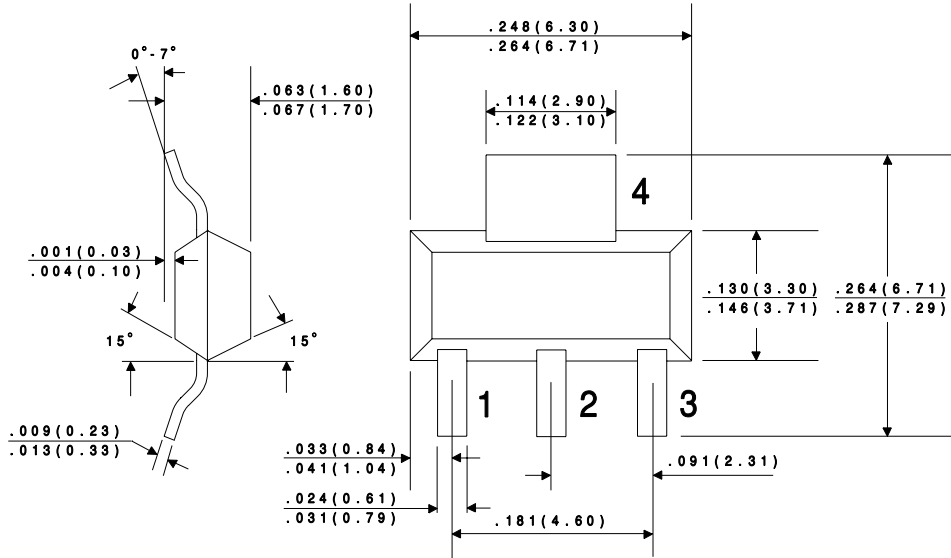
	SYMBOL		UNITS
Peak Repetitive Off-State Voltage	V _{DRM}	400	V
Peak Repetitive Reverse Voltage	V _{RRM}	400	V
RMS On-State Current	I _T (RMS)	0.8	A
Average On-State Current (T _C =67°C)	I _T (AV)	0.51	A
Operating Junction Temperature	T _J	-40 to +125	°C
Storage Temperature	T _{stg}	-40 to +150	°C
Thermal Resistance	θ _{JA}	150	°C/W
Thermal Resistance	θ _{JC}	25	°C/W

ELECTRICAL CHARACTERISTICS (T_A=25°C unless otherwise noted)

SYMBOL	TEST CONDITIONS	MIN	MAX	UNITS
I _{DRM}	V _D =400V, R _{GK} =1KΩ, T _C =125°C		50	μA
I _{RRM}	V _D =400V, R _{GK} =1KΩ, T _C =125°C		50	μA
V _T	I _T =1.2A		1.7	V
I _{GT}	V _D =7.0V, R _L =100Ω, R _{GK} =1KΩ		200	μA
V _{GT}	V _D =7.0V, R _L =100Ω, R _{GK} =1KΩ		0.8	V
V _{GD}	V _D =400V, R _L =100Ω, T _C =125°C	0.1		V
I _H	V _D =7.0, R _{GK} =1KΩ		5.0	mA
t _{ON}	V _D =400V, I _{GT} =1.0mA, I _F =1.0A, R _{GK} =1.0Ω, di/dt=6.0A/μs		2.8 TYP	μs



All dimensions in inches (mm).



LEAD CODE:

- 1) CATHODE
- 2) ANODE
- 3) GATE
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