

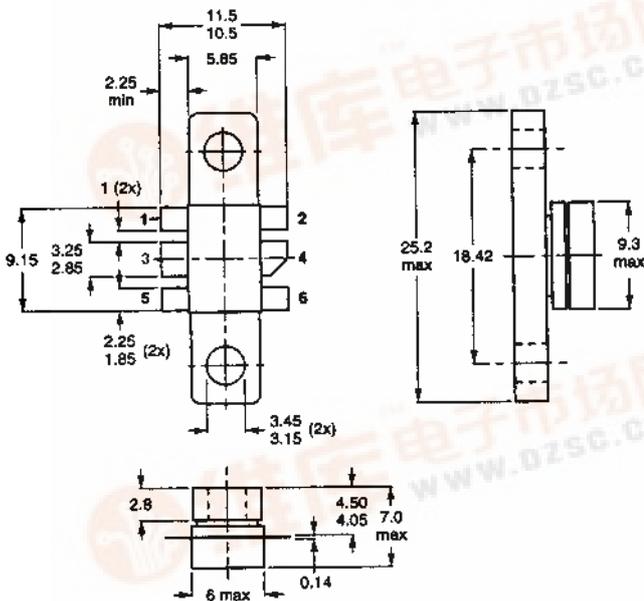
TetraFET

D1093UK

METAL GATE RF SILICON FET

MECHANICAL DATA

**GOLD METALLISED
MULTI-PURPOSE SILICON
DMOS RF FET
10W – 28V – 500MHz
SINGLE ENDED**



FEATURES

- SIMPLIFIED AMPLIFIER DESIGN
- SUITABLE FOR BROAD BAND APPLICATIONS
- VERY LOW C_{rss}
- SIMPLE BIAS CIRCUITS
- LOW NOISE
- HIGH GAIN – 13 dB MINIMUM

SOT 171

PIN 1	SOURCE	PIN 2	SOURCE
PIN 3	GATE	PIN 4	DRAIN
PIN 5	SOURCE	PIN 6	SOURCE

APPLICATIONS

- HF/VHF/UHF COMMUNICATIONS
from 1 MHz to 1 GHz

ABSOLUTE MAXIMUM RATINGS ($T_{case} = 25^{\circ}C$ unless otherwise stated)

P_D	Power Dissipation	42W
BV_{DSS}	Drain – Source Breakdown Voltage *	65V
BV_{GSS}	Gate – Source Breakdown Voltage *	$\pm 20V$
$I_{D(sat)}$	Drain Current *	4A
T_{stg}	Storage Temperature	-65 to 150°C
	Maximum Operating Junction Temperature	200°C



ELECTRICAL CHARACTERISTICS (T_{case} = 25°C unless otherwise stated)

Parameter	Test Conditions	Min.	Typ.	Max.	Unit
BV _{DSS} Drain-Source Breakdown Voltage	V _{GS} = 0 I _D = 10mA	65			V
I _{DSS} Zero Gate Voltage Drain Current	V _{DS} = 28V V _{GS} = 0			1	mA
I _{GSS} Gate Leakage Current	V _{GS} = 20V V _{DS} = 0			4	μA
V _{GS(th)} Gate Threshold Voltage *	I _D = 10mA V _{DS} = V _{GS}	1		7	V
g _{fs} Forward Transconductance *	V _{DS} = 10V I _D = 0.4A	0.72			S
G _{PS} Common Source Power Gain	P _O = 10W	13			dB
η Drain Efficiency	V _{DS} = 28V I _{DQ} = 0.8A	50			%
VSWR Load Mismatch Tolerance	f = 500MHz	20:1			—
C _{iSS} Input Capacitance	V _{DS} = 0 V _{GS} = -5V f = 1MHz			48	pF
C _{oSS} Output Capacitance	V _{DS} = 28V V _{GS} = 0 f = 1MHz			24	pF
C _{rSS} Reverse Transfer Capacitance	V _{DS} = 28V V _{GS} = 0 f = 1MHz			2	pF

* Pulse Test: Pulse Duration = 300 μs , Duty Cycle ≤ 2%

HAZARDOUS MATERIAL WARNING

The ceramic portion of the device between leads and metal flange is beryllium oxide. Beryllium oxide dust is highly toxic and care must be taken during handling and mounting to avoid damage to this area.

THESE DEVICES MUST NEVER BE THROWN AWAY WITH GENERAL INDUSTRIAL OR DOMESTIC WASTE.

THERMAL DATA

R _{THj-case}	Thermal Resistance Junction – Case	Max. 4.2°C / W
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