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Ordering number : ENN6781A

NPN Triple Diffused Planar Silicon Transistor

2SD2650

Color TV Horizontal Deflection Output Applications

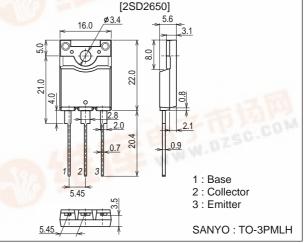
Features

- High speed.
- High breakdown voltage(VCBO=1500V).
- High reliability(Adoption of HVP process).
- · Adoption of MBIT process. WWW.DZSC.COM

Package Dimensions

unit : mm 2174A





Specifications

Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Collector-to-Base Voltage	VCBO		1500	V
Collector-to-Emitter Voltage	VCEO		700	V
Emitter-to-Base Voltage	VEBO		5	V
Collector Current	IC	1 B-2	8	А
Collector Current (Pulse)	ICP		20	А
Collector Dissipation	De		3.0	W
	PC	Tc=25°C	65	W
Junction Temperature	Тј	A A A	150	°C
Storage Temperature	Tstg	.C. C.	-55 to +150	°C

Electrical Characteristics at Ta=25°C

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	Onic
Collector Cutoff Current	ICBO	VCB=800V, IE=0	1.1.1		10	μA
Collector Cutoff Current	ICES	V _{CE} =1500V, R _{BE} =0	5- F	6.8	1.0	mA
Collector Sustain Voltage	VCEO(sus)	IC=100mA, IB=0	700	W Inc.	Dr.	V
Emitter Cutoff Current	IEBO	VEB=4V, IC=0		10.00	1	mA

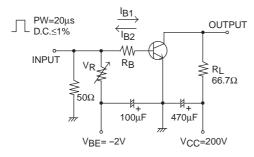
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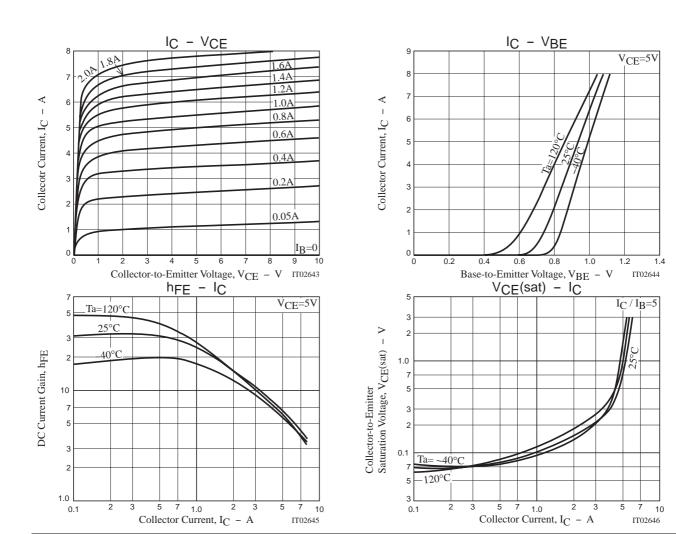
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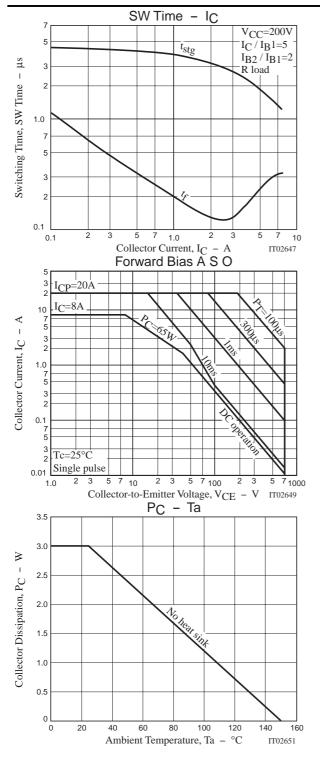
Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	Offic
Collector-to-Emitter Saturation Voltage	VCE(sat)	IC=4.5A, IB=0.9A			3	V
Base-to-Emitter Saturation Voltage	V _{BE} (sat)	IC=4.5A, IB=0.9A			1.5	V
DC Current Gain	hFE1	V _{CE} =5V, I _C =1A	10			
	hFE2	V _{CE} =5V, I _C =5A	5		8	
Fall Time	tf	IC=3A, IB1=0.6A, IB2=-1.2A			0.3	μs

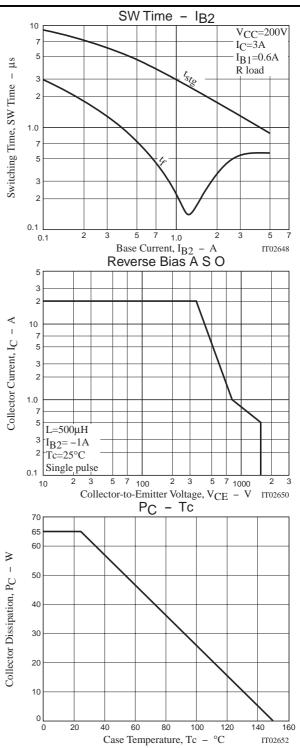
Switching Time Test Circuit





2SD2650





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