

## AUDIO 1 CHIP TUNER SYSTEM

The DBL1091 is electronic tuner one chip IC for car stereo system and easily designed to smallize system. It is consists of AM Tuner, FM IF, MPX with HCC, SNC

### FUNCTIONS

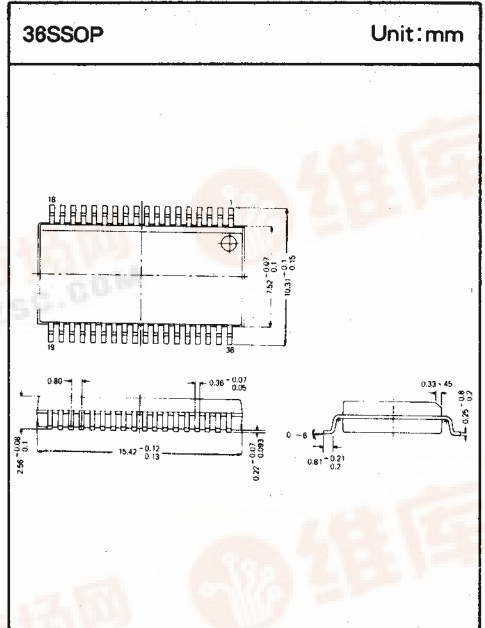
- AM : MIX, OSC, RF-AGC, ANT DAMPING AGC, IF AMP, IF BUFFER, DETECTOR, S-METER, TU-LED
- FM : IF AMP, QUADRATURE DET, AFC, S-METER, TU-LED, IF BUFFER, SOFT MUTE, BAND MUTE
- MPX : NO CONTROLLED VCO, HCC, SNC COMPULSION MONO, ST-LED

### FEATURES

- Be reduce peripheral component
- No controled VCO
- Small package (36SSOP)
- Band mute function is possible for Europe model.
- Reduced side stop at seek, scan.

### APPLICATIONS

- The Car Stereo, General Audio System.



# DBL 1091-V

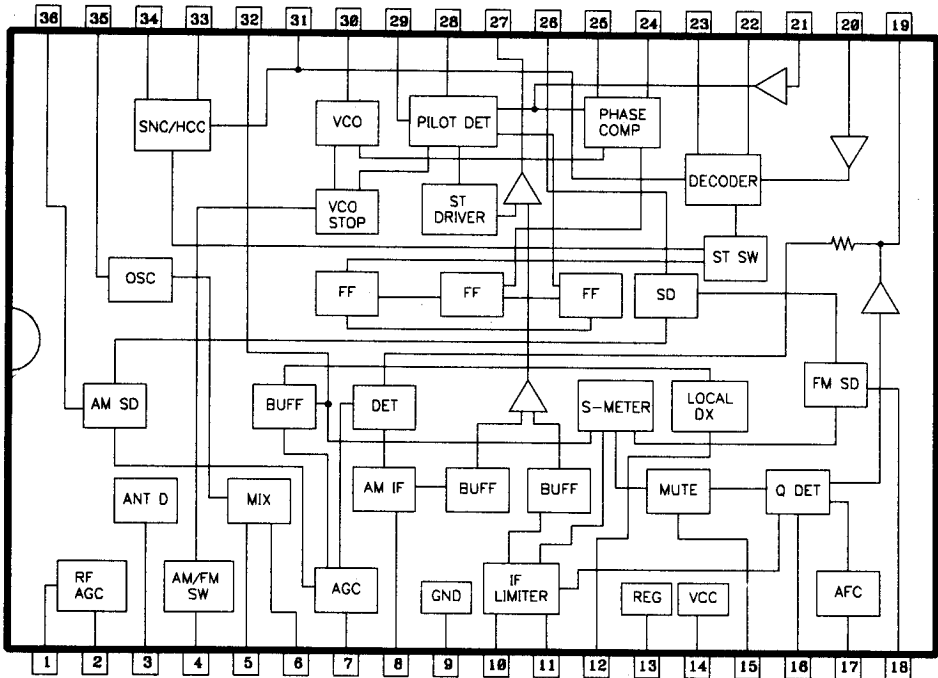
MAXIMUM RATINGS(Ta=25°C)

Characteristics	Symbol	Rating	Unit
Supply Voltage	Vcc max	11	V
Power Dissipation	Pd max	720	mW
Operating Temperature	Topg	-30 ~ +80	°C
Storage Temperature	Tstg	-40 ~ +150	°C

OPERATING CONDITION(Ta=25°C)

Characteristics	Symbol	Rating	Unit
Supply Voltage	Vcc	8.5	V
Operating Supply Voltage Range	Vcc op	7.0 ~ 10	V

BLOCK DIAGRAM



# DBL 1091-V

(2) AM Mode :  $f_c = 1\text{MHz}$ ,  $f_m = 1\text{KHz}$ , 30% Mod,  $T_a = 25^\circ\text{C}$ ,  $V_{CC} = 8.5\text{V}$

Characteristic	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Supply Current	ICCQ	No Input	29	38	47	mA
Detection Output	V <sub>o</sub> (1)	V <sub>in</sub> = 25dB $\mu$	18	40	68	mV
	V <sub>o</sub> (2)	V <sub>in</sub> = 74dB $\mu$	60	80	120	mV
Signal to Noise Ratio	S/N(1)	V <sub>in</sub> = 25dB $\mu$	17	21	-	dB
	S/N(2)	V <sub>in</sub> = 74dB $\mu$	42	49	-	dB
Total Harmonic Distortion	THD(1)	V <sub>in</sub> = 74dB $\mu$	-	0.35	1.0	%
	THD(2)	V <sub>in</sub> = 130dB $\mu$	-	0.4	2.0	%
LED ON Sens.	LED-ON	V <sub>26</sub> = 2.0V	21	30	39	dB $\mu$
IF Buffer Output	V <sub>IF</sub>	V <sub>in</sub> = 50dB $\mu$ , V <sub>12</sub> = 5.0V	100	180	260	mV
Signal Meter Output	VSM(1)	No Input	0	0.7	1.0	V
	VSM(2)	V <sub>in</sub> = 74dB $\mu$	2.6	3.7	5.2	V
RF AGC on Input	AGC-ON	V <sub>1</sub> = 3.0V	50	57	64	dB $\mu$
Pin diode drive current	I <sub>ANTD</sub>	V <sub>1</sub> = 0.7V	2.5	3.0	4.0	mA
AM OSC Level	V <sub>OSC</sub>	No Input	160	180	200	mV

# DBL 1091-V

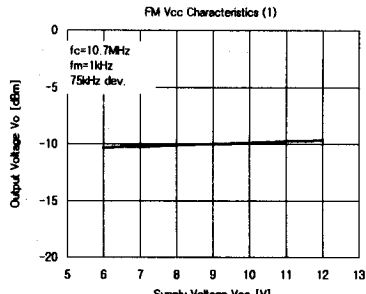
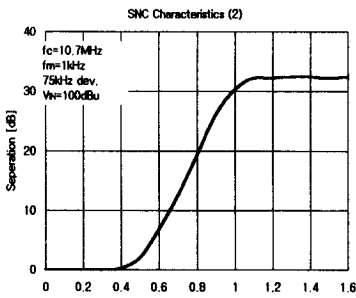
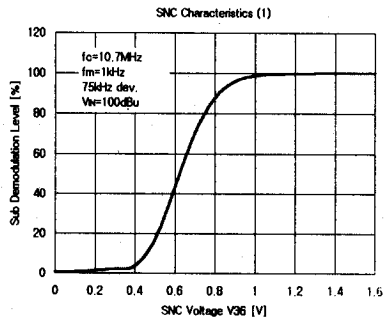
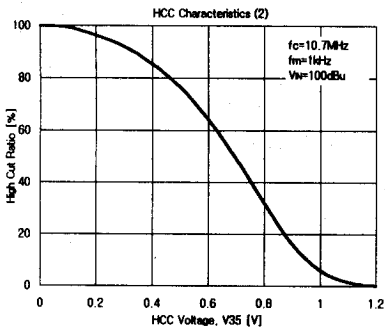
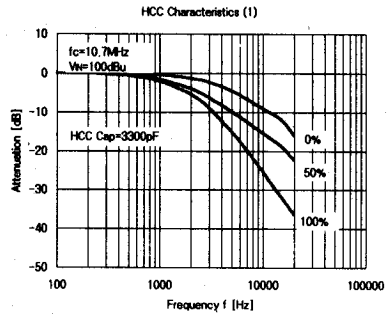
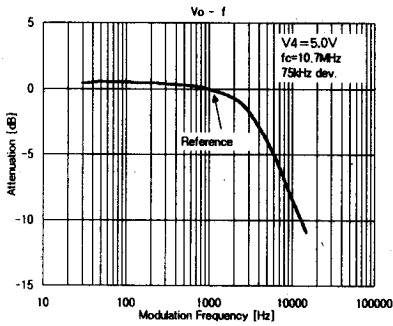
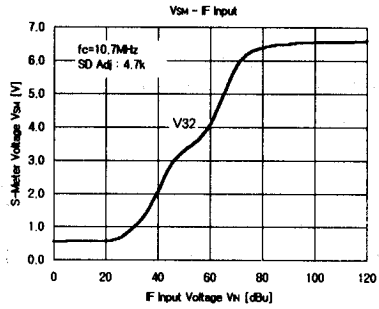
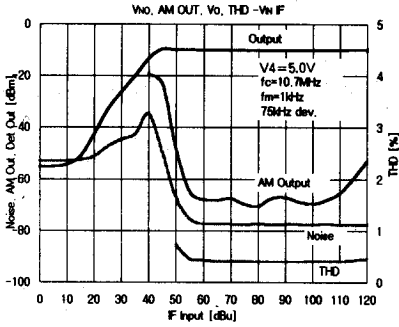
## □ ELECTRICAL CHARACTERISTICS

(1) FM Mode :  $f_c = 10.7\text{MHz}$ ,  $f_m = 1\text{kHz}$ , 75kHz Dev.,  $T_a = 25^\circ\text{C}$ ,  $V_{cc} = 8.5\text{V}$

Characteristic	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Supply Current	ICCQ	No Input	35	45	55	mA
Detection Output	V <sub>o</sub>	V <sub>in</sub> = 100dB $\mu$	165	250	345	mV
Signal to Noise Ratio	S/N	V <sub>in</sub> = 100dB $\mu$	66	75	-	dB
-3dB Limiting Sens.	-3dBLS	V <sub>od</sub> = 3dB down	27	37	47	dB $\mu$
AM Rejection Ratio	AMR	V <sub>in</sub> = 100dB $\mu$ , AM = 1kHz, 30% mod	48	60	-	dB
Signal Meter Output	VSM(1)	No Input	0	0.15	0.7	V
	VSM(2)	V <sub>in</sub> = 100dB $\mu$	5.0	6.1	7.0	V
LED ON Sens.	LED-ON	V <sub>26</sub> = 2.0V	43	58	73	dB $\mu$
LED ON BW	LED-BW	V <sub>in</sub> = 100dB $\mu$ , V <sub>26</sub> $\geq$ 2.0V	95	145	190	KHz
IF Buffer Output	V <sub>F</sub>	V <sub>in</sub> = 100dB $\mu$ , V <sub>12</sub> = 5.0V	160	210	360	mV
Channel Separation	SEP.	V <sub>in</sub> = 100dB $\mu$ , L+R=90%, PILOT = 10%	30	45	-	dB
ST LED ON Sens.	LED-ON	V <sub>in</sub> = 100dB $\mu$ PILOT mod. degree at ST-LED ON	1.8	3.2	5.0	%
ST LED OFF Sens.	LED-OFF	V <sub>in</sub> = 100dB $\mu$ PILOT mod. degree at ST-LED OFF	-	2.2	-	%
Total Harmonic Distortion	THD(1)	V <sub>in</sub> = 100dB $\mu$ , MONO	-	0.5	2.5	%
	THD(2)	V <sub>in</sub> = 100dB $\mu$ , MAIN	-	0.5	2.5	%
Channel Balance	CB	V <sub>in</sub> = 100dB $\mu$ , MONO	-1.5	0	+1.5	dB
SNC Output Voltage	V <sub>O_sus</sub>	V <sub>in</sub> = 100dB $\mu$ , L-R=90% PILOT = 10%, V <sub>34</sub> = 0.1V	-	0.5	5.0	mV
HCC Output ATT	Att HCC	V <sub>in</sub> = 100dB $\mu$ , L+R=90% PILOT = 10%, V <sub>33</sub> = 0.6V, f <sub>m</sub> = 10kHz	-10	-5.0	-0.5	dB

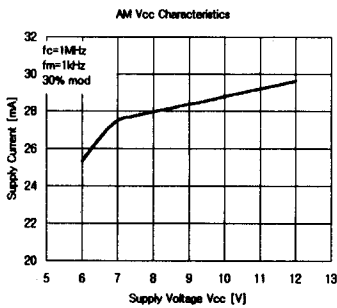
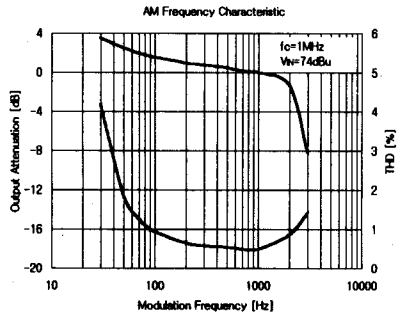
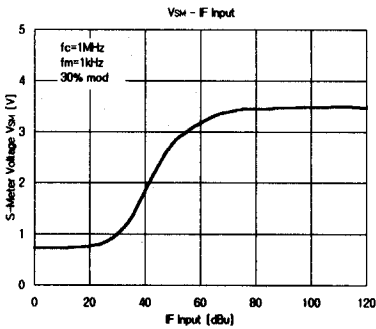
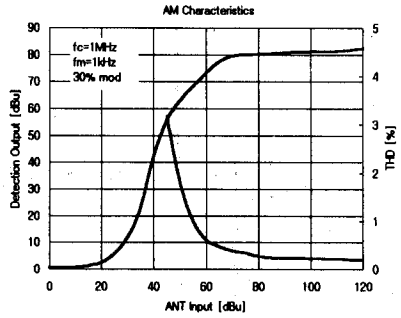
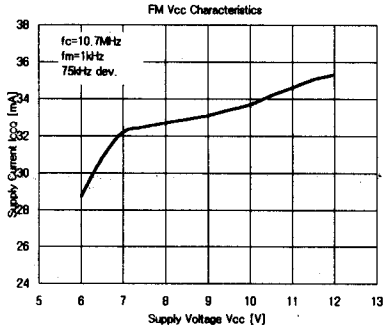
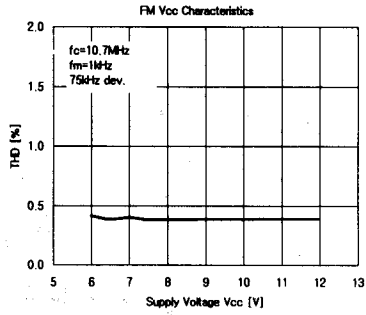
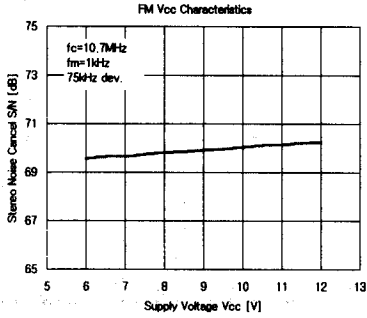
# DBL 1091-V

## TYPICAL PERFORMANCE CHARACTERISTICS



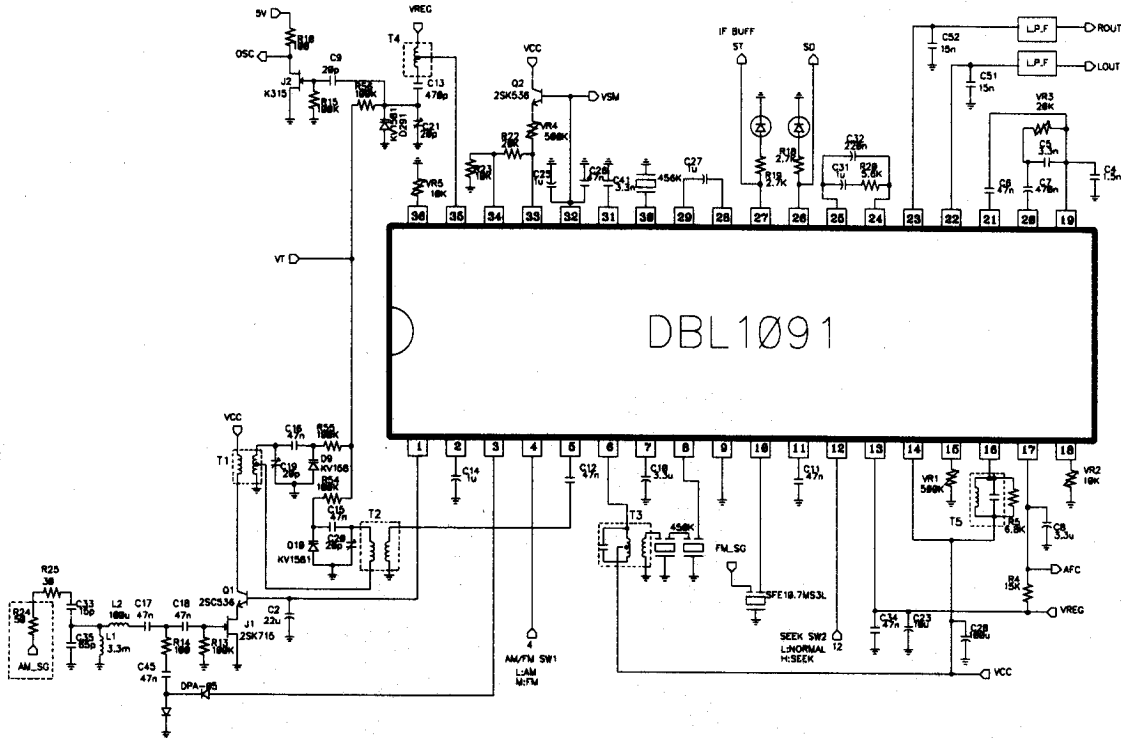
# DBL 1091-V

## □ TYPICAL PERFORMANCE CHARACTERISTICS(continued)



# DBL 1091-V

TEST CIRCUIT



# DBL 1091-V

## □ APPLICATION CIRCUIT

