查询DFLS130供应商 捷多邦，专业PCB打样工厂，24小时加急出货

DFLS130

DIODES INCORPORATED

1.0A SURFACE MOUNT SCHOTTKY BARRIER RECTIFIER

PowerDI™123

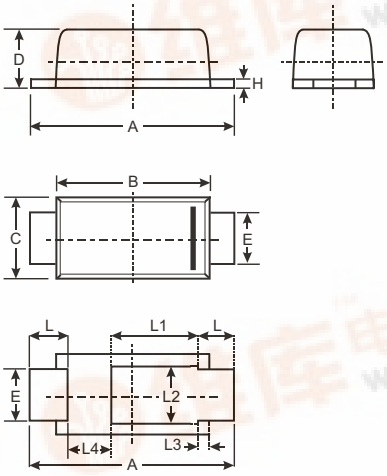
NEW PRODUCT

Features

- Guard Ring Die Construction for Transient Protection
- Low Power Loss, High Efficiency
- High Surge Capability
- High Current Capability and Low Forward Voltage Drop

Mechanical Data

- Case: PowerDI™123, Plastic
- Plastic Material: UL Flammability Classification Rating 94V-0
- Moisture sensitivity: Level 1 per J-STD-020A
- Polarity: Cathode Band
- Terminals: Matte Tin Finish (Lead Free), Solderable per MIL-STD-202, Method 208
- Marking: Date Code & Type Code, See Page 3
- Type Code: F01
- Weight: 0.01 grams (approx.)
- Ordering Information: See Page 3



PowerDI™123			
Dim	Min	Max	Typ
A	3.50	3.90	3.70
B	2.60	3.00	2.80
C	1.63	1.93	1.78
D	0.93	1.00	0.98
E	0.85	1.25	1.00
H	0.15	0.25	0.20
L	0.45	0.85	0.65
L1	—	—	1.35
L2	—	—	1.10
L3	—	—	0.20
L4	0.90	1.30	1.05
All Dimensions in mm			

Maximum Ratings @ T_A = 25°C unless otherwise specified

Single phase, half wave, 60Hz, resistive or inductive load. For capacitive load, derate current by 20%.

Characteristic	Symbol	Value	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V_{RRM} V_{RWM} V_R	30	V
RMS Reverse Voltage	$V_{R(RMS)}$	21	V
Average Forward Current @ T_T = 120°C	$I_{F(AV)}$	1.0	A
Non-Repetitive Peak Forward Surge Current 8.3ms single half sine-wave superimposed on rated load (JEDEC Method)	I_{FSM}	22	A
Power Dissipation (Note 1)	P_D	1.67	W
Power Dissipation (Note 2)	P_D	556	mW
Thermal Resistance Junction to Ambient (Note 1)	$R_{\theta JA}$	60	°C/W
Thermal Resistance Junction to Ambient (Note 2)	$R_{\theta JA}$	180	°C/W
Thermal Resistance Junction to Soldering (Note 3)	$R_{\theta JS}$	10	°C/W
Operating Temperature Range	T_j	-55 to +125	°C
Storage Temperature Range	T_{STG}	-55 to +150	°C

Electrical Characteristics @ T_A = 25°C unless otherwise specified

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
Reverse Breakdown Voltage (Note 4)	$V_{(BR)R}$	30	—	—	V	I_R = 1.5mA
Forward Voltage (Note 4)	V_F	— — —	0.25 0.33 0.36	— 0.37 0.42	V	I_F = 0.1A I_F = 0.7A I_F = 1.0A
Leakage Current (Note 4)	I_R	—	0.15	1.0	mA	V_R = 30V, T_A = 25°C
Total Capacitance	C_T	—	40	—	pF	V_R = 10V, f = 1.0MHz

Notes:
1. Part mounted on 2"x2" GETEK board with 1"x1" copper pad, 25% anode, 75% cathode.
2. Part mounted on FR-4 board with recommended pad layout, which can be found on our website at http://www.diodes.com/datasheets/ap02001.pdf.
3. Theoretical $R_{\theta JS}$ calculated from the top center of the die straight down to the PCB cathode tab solder junction.
4. Short duration pulse test to minimize self-heating effect.

找PDF 维库一下 pdf.dzsc.com

NEW PRODUCT

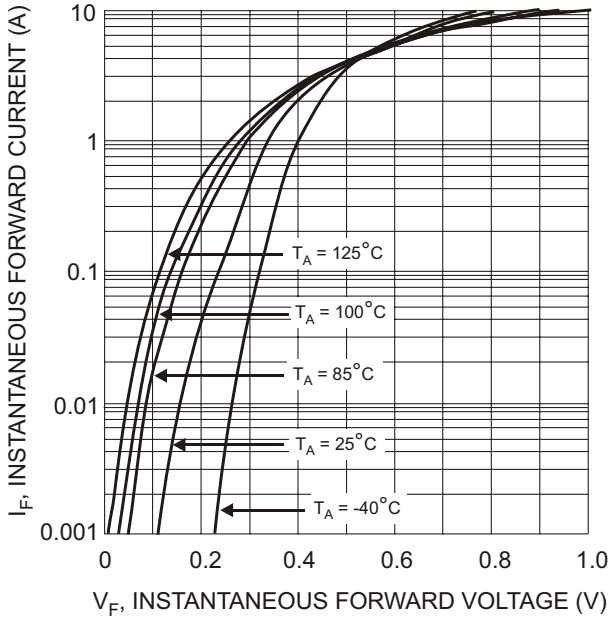


Fig. 1, Typical Forward Characteristics

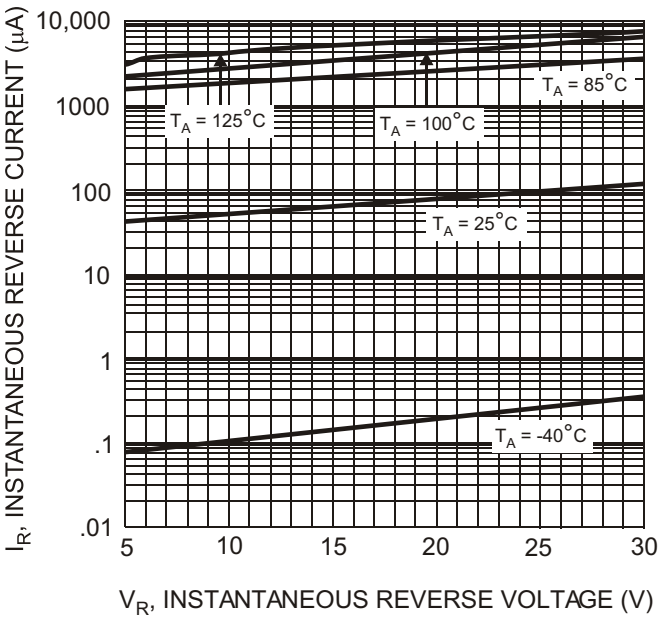


Fig. 2, Typical Reverse Characteristics

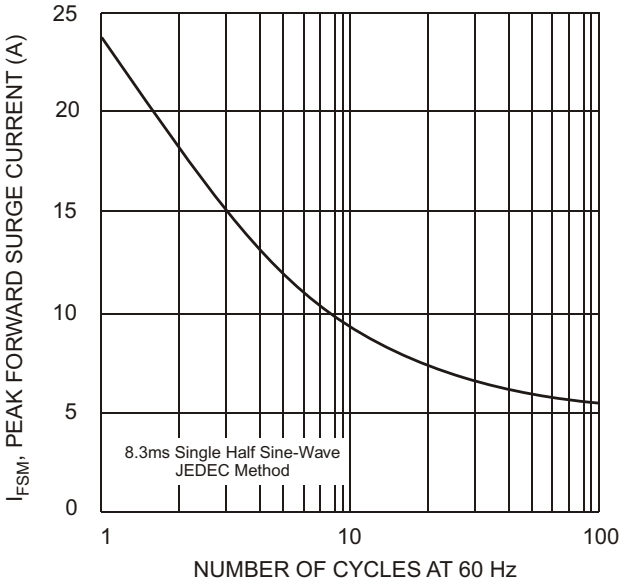


Fig. 3, Maximum Non-Repetitive Peak Forward Surge Current

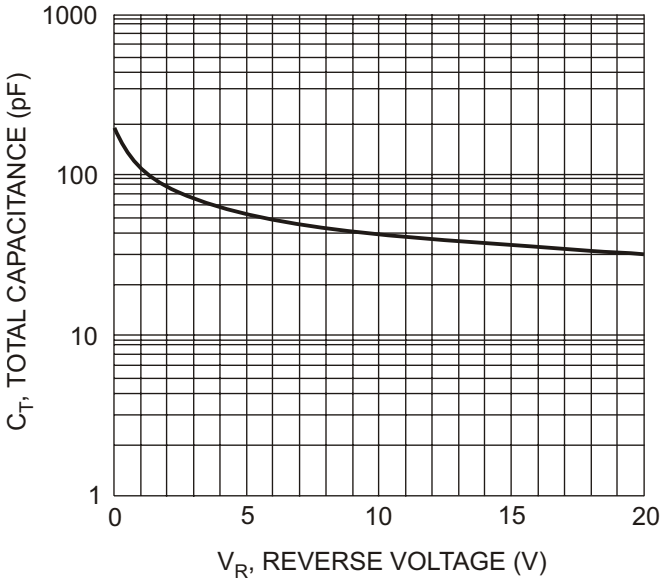


Fig. 4, Typical Total Capacitance

NEW PRODUCT

Ordering Information (Note 5)

Device	Packaging	Shipping
DFLS130-7	PowerDI™123	3000/Tape & Reel

Notes: 5. For Packaging Details, go to our website at http://www.diodes.com/datasheets/ap02007.pdf.

Marking Information

F01 = Product Type Marking Code
YM = Date Code Marking
Y = Year (ex: P = 2003)
M = Month (ex: 9 = September)

Date Code Key

Year	2003	2004	2005	2006	2007	2008	2009
Code	P	R	S	T	U	V	W

Month	Jan	Feb	March	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	O	N	D