

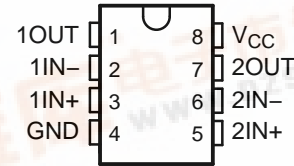
# LP358, LP2904 ULTRA-LOW-POWER DUAL OPERATIONAL AMPLIFIERS

SLOS475–AUGUST 2005

## FEATURES

- Low Supply Current . . . 85  $\mu$ A Typ
- Low Offset Voltage . . . 2 mV Typ
- Low Input Bias Current . . . 2 nA Typ
- Input Common Mode to GND
- Wide Supply Voltage . . . 3 V <  $V_{CC}$  < 32 V
- Pin Compatible With LM358

D OR DGK PACKAGE  
(TOP VIEW)



## APPLICATIONS

- LCD Displays
- Portable Instrumentation
- Sensor/Metering Equipment
- Consumer Electronics (MP3 Players, Toys)
- Power Supplies

## DESCRIPTION/ORDERING INFORMATION

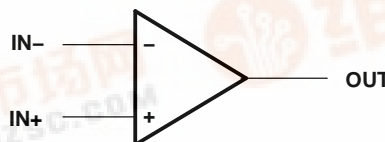
The LP358 and LP2904 are dual low-power operational amplifiers especially suited for battery-operated applications. Good input specifications and wide supply-voltage range still are achieved, despite the ultra-low supply current. Single-supply operation is achieved with an input common-mode range that includes GND.

The LP358 and LP2904 are ideal in applications where wide supply voltage and low power are more important than speed and bandwidth. These applications include portable instrumentation, LCD displays, consumer electronics (MP3 players, toys, etc.), and power supplies.

## ORDERING INFORMATION

$T_A$	PACKAGE <sup>(1)</sup>		ORDERABLE PART NUMBER	TOP-SIDE MARKING
0°C to 70°C	SOIC – D	Tube of 75	LP358D	LP358
		Reel of 2500	LP358DR	
	VSSOP – DGK	Tube of 100	LP358DGK	PREVIEW
		Reel of 250	LP358DGKT	
		Reel of 2500	LP358DGKR	
–40°C to 85°C	SOIC – D	Tube of 75	LP2904D	PREVIEW
		Reel of 2500	LP2904DR	
	VSSOP – DGK	Tube of 100	LP2904DGK	PREVIEW
		Reel of 250	LP2904DGKT	
		Reel of 2500	LP2904DGKR	

## SYMBOL (EACH AMPLIFIER)



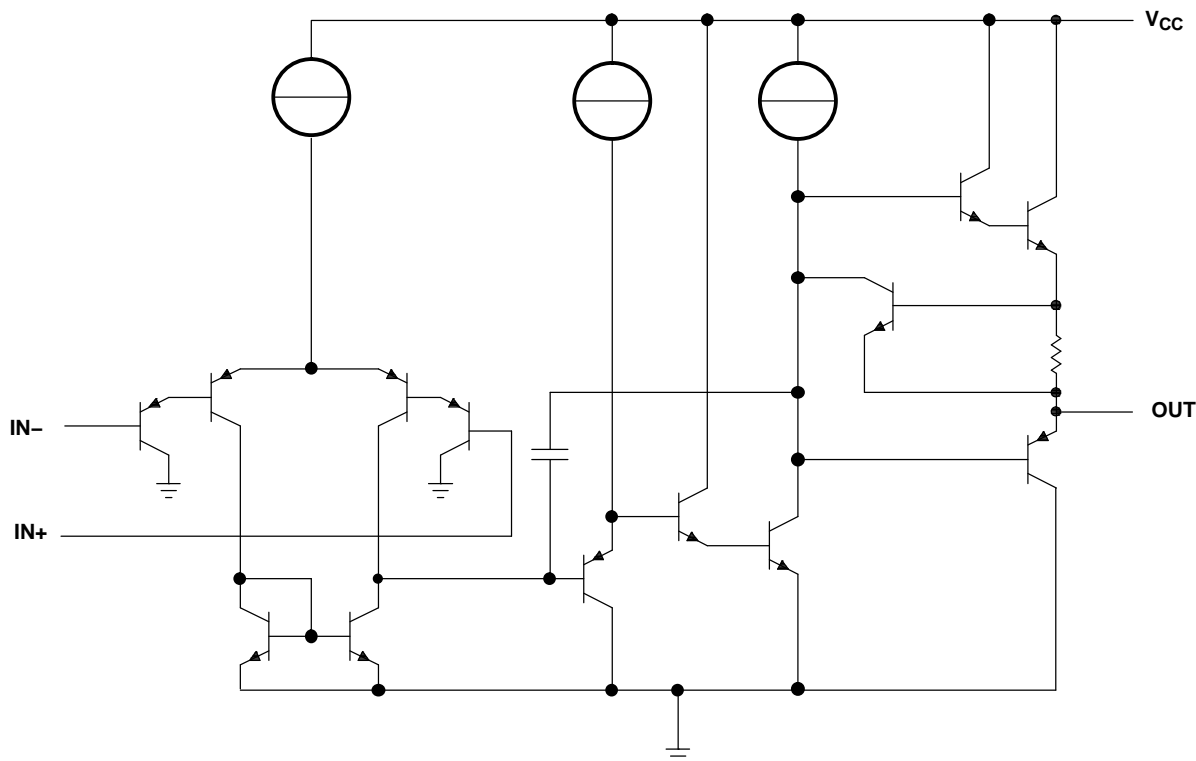
(1) Package drawings, standard packing quantities, thermal data, symbolization, and PCB design guidelines are available at [www.ti.com/sc/package](http://www.ti.com/sc/package).

# LP358, LP2904 ULTRA-LOW-POWER DUAL OPERATIONAL AMPLIFIERS

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SCHEMATIC (EACH AMPLIFIER)



## Absolute Maximum Ratings<sup>(1)</sup>

over operating free-air temperature range (unless otherwise noted)

		MIN	MAX	UNIT
$V_{CC}$	Supply voltage range <sup>(2)</sup>		$\pm 16$ or 32	V
$V_{ID}$	Differential input voltage <sup>(3)</sup>		$\pm 32$	V
$V_I$	Input voltage (either input)	-0.3	32	V
	Duration of output short circuit (one amplifier) to ground at (or below) $T_A = 25^\circ\text{C}$ , $V_{CC} \leq 15\text{ V}$ <sup>(4)</sup>		Unlimited	
$\theta_{JA}$	Package thermal impedance <sup>(5)(6)</sup>	D package		97
		DGK package		172
$T_J$	Operating virtual junction temperature		150	$^\circ\text{C}$
$T_{stg}$	Storage temperature range	-65	150	$^\circ\text{C}$

- (1) Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.
- (2) All voltage values (except differential voltages and  $V_{CC}$  specified for the measurement of  $I_{OS}$ ) are with respect to the network GND.
- (3) Differential voltages are at IN+, with respect to IN-.
- (4) Short circuits from outputs to  $V_{CC}$  can cause excessive heating and eventual destruction.
- (5) Maximum power dissipation is a function of  $T_J(\text{max})$ ,  $\theta_{JA}$ , and  $T_A$ . The maximum allowable power dissipation at any allowable ambient temperature is  $P_D = (T_J(\text{max}) - T_A)/\theta_{JA}$ . Operating at the absolute maximum  $T_J$  of  $150^\circ\text{C}$  can affect reliability.
- (6) The package thermal impedance is calculated in accordance with JESD 51-7.

## ESD Protection

TEST CONDITIONS	TYP	UNIT
Human-Body Model	$\pm 2$	kV

## Electrical Characteristics

$T_A = 25^\circ\text{C}$ ,  $V_{CC} = 5\text{ V}$ ,  $V_{IC} = V_{CC}/2$ ,  $R_L = 100\text{ k}\Omega$  to GND (unless otherwise noted)

PARAMETER		TEST CONDITIONS <sup>(1)</sup>	T <sub>A</sub> <sup>(2)</sup>	LP358			LP2904			UNIT
				MIN	TYP <sup>(3)</sup>	MAX	MIN	TYP <sup>(3)</sup>	MAX	
V <sub>IO</sub>	Input offset voltage		25°C		2	4		2	4	mV
			Full range			9		10		
I <sub>IB</sub>	Input bias current		25°C		2	10		2	20	nA
			Full range			20		40		
I <sub>IO</sub>	Input offset current		25°C		0.2	2		0.5	4	nA
			Full range			4		8		
A <sub>V</sub>	Large-signal voltage gain	R <sub>L</sub> = 10 kΩ to GND, V <sub>CC</sub> = 30 V	25°C	50	100		40	70	V/mV	
			Full range	40		30				
CMRR	Common-mode rejection ratio	V <sub>CC</sub> = 30 V, V <sub>IC</sub> = 0 V to V <sub>CC</sub> – 1.5 V	25°C	80	90		80	90	dB	
			Full range	75		75				
k <sub>VSR</sub>	Power-supply rejection ratio	V <sub>CC</sub> = 5 V to 30 V	25°C	80	90		80	90	V	
			Full range	75		75				
I <sub>CC</sub>	Supply current	R <sub>L</sub> = ∞	25°C		85	150		85	150	μA
			Full range			250		275		
V <sub>OH</sub>	Output voltage swing (high)	I <sub>L</sub> = 0.35 mA to GND, V <sub>IC</sub> = 0 V	25°C	3.4	3.6		3.4	3.6	V	
			Full range	V <sub>CC</sub> – 1.9			V <sub>CC</sub> – 1.9			
V <sub>OL</sub>	Output voltage swing (low)	I <sub>L</sub> = 0.35 mA from V <sub>CC</sub> , V <sub>IC</sub> = 0 V	25°C	0.82	0.7		0.82	0.7	V	
			Full range	1		1				
I <sub>O</sub>	Output source current	V <sub>O</sub> = 3 V, V <sub>ID</sub> = 1 V	25°C	7	10		7	10	mA	
			Full range	4		4				
I <sub>O</sub>	Output sink current	V <sub>O</sub> = 1.5 V, V <sub>ID</sub> = –1 V	25°C	4	5		4	5	mA	
			Full range	3		3				
		V <sub>O</sub> = 1.5 V, V <sub>ID</sub> = –1 V, V <sub>IC</sub> = 0 V	25°C	2	4		2	4		
			Full range	1		1				
I <sub>OS,GND</sub>	Output short to GND	V <sub>ID</sub> = 1 V	25°C		20	35		20	35	mA
			Full range		40		40			
I <sub>OS,VCC</sub>	Output short to V <sub>CC</sub>	V <sub>ID</sub> = –1 V	25°C		15	30		15	30	mA
			Full range		45		45			
αV <sub>IO</sub>	Input offset voltage drift		25°C		10			10		μV/°C
αI <sub>IO</sub>	Input offset current drift		25°C		10			10		pA/°C

(1) For full-range temperature limits:  $V_{CC} = 3\text{ V}$  to  $32\text{ V}$ ,  $V_{ICR} = 0\text{ V}$  to  $V_{CC} - 1.5\text{ V}$  (unless otherwise noted)

(2) Full range is  $0^\circ\text{C}$  to  $70^\circ\text{C}$  for LP358 and  $-40^\circ\text{C}$  to  $85^\circ\text{C}$  for LP2904.

(3) All typical values are at  $T_A = 25^\circ\text{C}$ .

## Operating Conditions

$V_{CC} = \pm 15\text{ V}$ ,  $T_A = 25^\circ\text{C}$

PARAMETER		TYP	UNIT
GBW	Gain bandwidth product	100	kHz
SR	Slew rate	50	V/ms

## PACKAGING INFORMATION

Orderable Device	Status <sup>(1)</sup>	Package Type	Package Drawing	Pins	Package Qty	Eco Plan <sup>(2)</sup>	Lead/Ball Finish	MSL Peak Temp <sup>(3)</sup>
LP358D	ACTIVE	SOIC	D	8	75	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
LP358DE4	ACTIVE	SOIC	D	8	75	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
LP358DR	ACTIVE	SOIC	D	8	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
LP358DRE4	ACTIVE	SOIC	D	8	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM

<sup>(1)</sup> The marketing status values are defined as follows:

**ACTIVE:** Product device recommended for new designs.

**LIFEBUY:** TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

**NRND:** Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

**PREVIEW:** Device has been announced but is not in production. Samples may or may not be available.

**OBSOLETE:** TI has discontinued the production of the device.

<sup>(2)</sup> Eco Plan - The planned eco-friendly classification: Pb-Free (RoHS) or Green (RoHS & no Sb/Br) - please check <http://www.ti.com/productcontent> for the latest availability information and additional product content details.

**TBD:** The Pb-Free/Green conversion plan has not been defined.

**Pb-Free (RoHS):** TI's terms "Lead-Free" or "Pb-Free" mean semiconductor products that are compatible with the current RoHS requirements for all 6 substances, including the requirement that lead not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, TI Pb-Free products are suitable for use in specified lead-free processes.

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<sup>(3)</sup> MSL, Peak Temp. -- The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

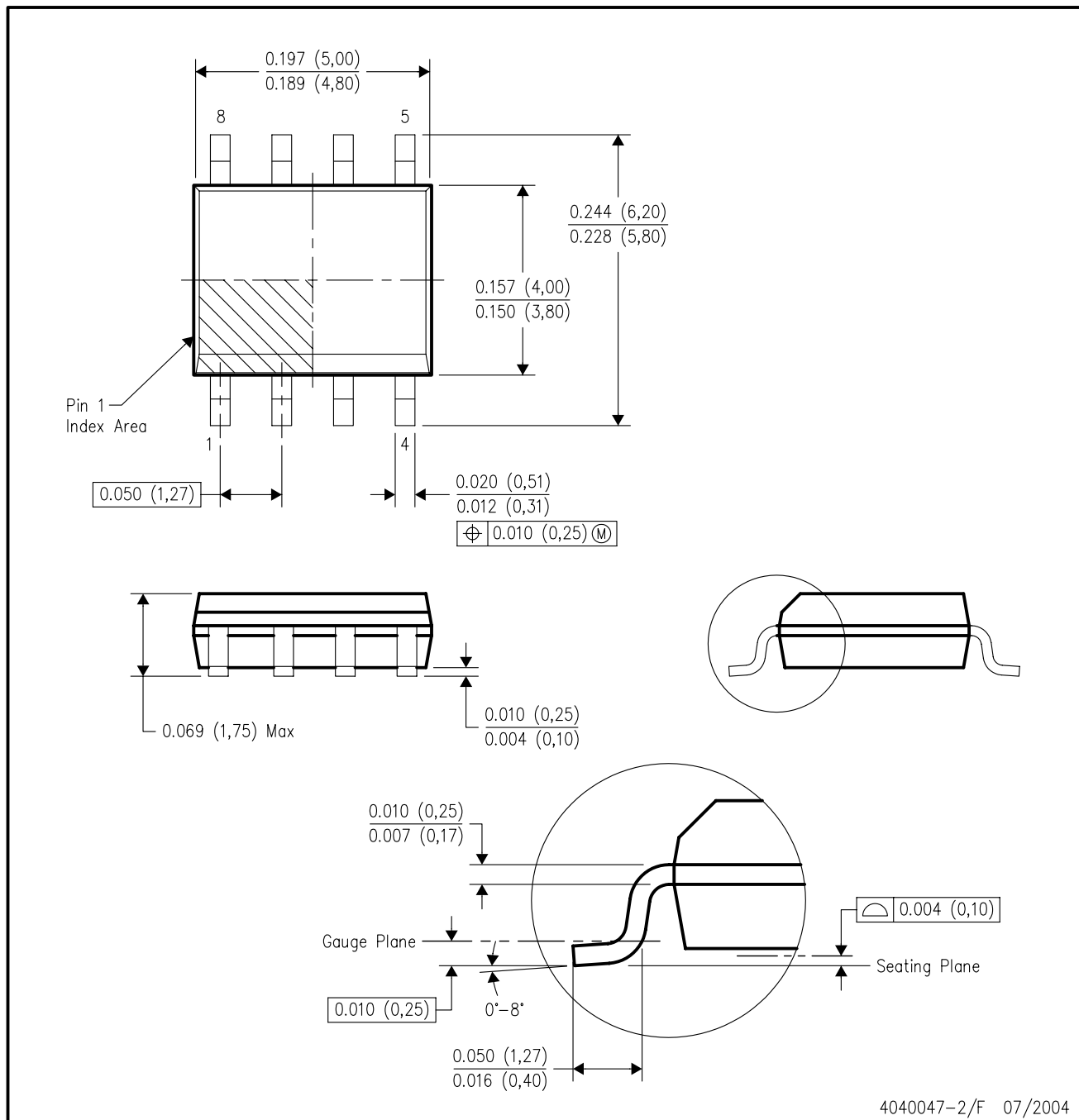
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# MECHANICAL DATA

D (R-PDSO-G8)

PLASTIC SMALL-OUTLINE PACKAGE



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