

March 1995

DS3658 Quad High Current Peripheral Driver

General Description

The DS3658 quad peripheral driver is designed for those applications where low operating power, high breakdown voltage, high output current and low output ON voltage are required. A unique input circuit combines TTL compatibility with high impedance. In fact, its extreme low input current allows it to be driven directly by a CMOS device.

The outputs are capable of sinking 600 mA each and offer a 70V breakdown. However, for inductive loads the output should be clamped to 35V or less to avoid latch-up during turn off (inductive fly back protection—refer AN-213). An onchip clamp diode capable of handling 800 mA is provided at each output for this purpose. In addition, the DS3658 incorporates circuitry that guarantees glitch-free power up or down operation and a fail-safe feature which puts the output in a high impedance state when the input is open.

The molded package is specifically constructed to allow increased power dissipation over conventional packages. The four ground pins are directly connected to the device chip with a special copper lead frame. When the quad driver is soldered into a PC board, the power rating of the device improves significantly.

Applications

- Relay drivers
- Lamp driversSolenoid drivers
- Hammer drivers
- Stepping motor drivers
- Triac drivers

■ LED drivers

- High current, high voltage drivers
- Level translators
- Fiber optic LED drivers

Features

- Single saturated transistor outputs
- Low standby power, 10 mW typical
- High impedance TTL compatible inputs
- Outputs may be tied together for increased current capacity
- High output current 600 mA per output 2.4A per package
- No output latch-up at 35V
- Low output ON voltage (350 mV typ @ 600 mA)
- High breakdown voltage (70V)
- Open collector outputs
- Output clamp diodes for inductive fly back protection
- NPN inputs for minimal input currents (1 µA typical)
- Low operating power
- Standard 5V power supply
- Power up/down protection
- Fail safe operation
- 2W power package
- Pin-for-pin compatible with SN75437

Connection Diagram

Dual-In-Line Package IN A IN B EN GND GND VCC IN C IN D 16 15 14 13 12 11 10 9 1 2 3 4 5 6 7 8 OUT A CLAMP 1 OUT B GND GND OUT C CLAMP 2 OUT D

Top View

Truth Table

IN	EN	OUT
Н	Н	L
L	Н	Z
Н	L	Z
L	L	Z

H = High state

L = Low state

Z = High impedance state

Order Number DS3658N See NS Package Number N16E



Absolute Maximum Ratings (Note 1)

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/Distributors for availability and specifications.

 Supply Voltage
 7V

 Input Voltage
 15V

 Output Voltage
 70V

 Output Current
 1.5A

Continuous Power Dissipation

@ 25°C Free-Air (Note 5) 2075 mW Storage Temperature Range -65° C to $+150^{\circ}$ C Lead Temperature (Soldering, 4 sec.) 260°C

Operating Conditions

	Min	Max	Units
Supply Voltage	4.75	5.25	V
Ambient Temperature	0	70	°C

Electrical Characteristics (Notes 2 and 3)

Symbol	Parameter	Conditions	Min	Тур	Max	Units
V _{IH}	Input High Voltage		2.0			V
V_{IL}	Input Low Voltage				0.8	V
I _{IH}	Input High Current	$V_{IN} = 5.25V, V_{CC} = 5.25V$		1.0	10	μΑ
I _{IL}	Input Low Current	$V_{IN} = 0.4V$			±10	μΑ
V _{IK}	Input Clamp Voltage	$I_{\parallel} = -12 \text{ mA}$		-0.8	-1.5	V
V _{OL}	Output Low Voltage	$I_L = 300 \text{ mA}$		0.2	0.4	V
		I _L = 600 mA (Note 4)		0.35	0.7	V
I _{CEX}	Output Leakage Current	$V_{CE} = 70V, V_{IN} = 0.8V$			100	μΑ
V _F	Diode Forward Voltage	$I_F = 800 \text{ mA}$		1.0	1.6	V
I _R	Diode Leakage Current	V _R = 70V			100	μΑ
Icc	Supply Current	All Inputs High		60	85	mA
		All Inputs Low		2	4	mA

Switching Characteristics (Note 2)

Symbol	Parameter	Conditions	Min	Тур	Max	Units
t _{PHL}	Turn On Delay	$R_L = 60\Omega, V_L = 30V$		226	500	ns
t _{PLH}	Turn Off Delay	$R_L = 60\Omega$, $V_L = 30V$		2430	8000	ns

Note 1: "Absolute Maximum Ratings" are those values beyond which the safety of the device cannot be guaranteed. They are not meant to imply that the device should be operated at these limits. The table of "Electrical Characteristics" provides conditions for actual device operation.

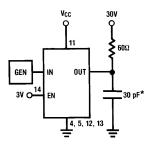
Note 2: Unless otherwise specified, min/max limits apply across the 0°C to +70°C temperature range and the 4.75V to 5.25V power supply range. All typical values are for $T_A = 25$ °C and $V_{CC} = 5.0$ V.

Note 3: All currents into device pins are shown as positive; all currents out of device pins are shown as negative; all voltages are referenced to ground, unless otherwise specified. All values shown as max or min are so classified on absolute value basis.

Note 4: All sections of this quad circuit may conduct rated current simultaneously; however, power dissipation averaged over a short interval of time must fall within specified continuous dissipation ratings.

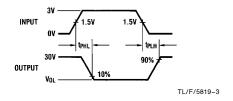
Note 5: For operation over 25°C free-air temperature, derate linearly to 1328 mW @ 70°C @ the rate of 16.6 mW/°C.

AC Test Circuit

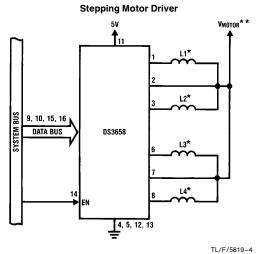


 $$\operatorname{TL/F}/5819-2$$ *Includes probe and jig capacitance

Switching Waveforms

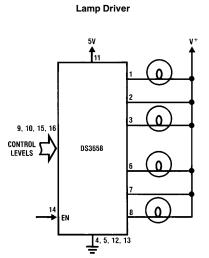


Typical Applications



*L1, L2, L3, L4 are the windings of a bifilar stepping motor

 $\ensuremath{^{**}}\ensuremath{\text{V}_{\text{MOTOR}}}$ is the supply voltage of the motor



TL/F/5819-5

Physical Dimensions inches (millimeters) (18.80 - 19.81)(2.286)16 15 14 13 12 11 10 9 16 15 F INDEX AREA 0.250 ± 0.010 (6.350 ± 0.254) PIN NO. 1 PIN NO. 1 1 2 3 4 5 6 7 8 1 2 _ OPTION 01 OPTION 02 $\frac{0.065}{(1.651)}$ 0.130 ± 0.005 $\frac{0.060}{(1.524)}$ TYP 4º TYP 0.300 = 0.320 (7.620 = 8.128) $\overline{(3.302 \pm 0.127)}$ OPTIONAL $\frac{0.145 - 0.200}{(3.683 - 5.080)}$ 95°±5° 0.008 = 0.016 (0.203 = 0.406) TYP 0.280 $\frac{0.125 - 0.150}{(3.175 - 3.810)}$ (7.112) MIN 0.030 ± 0.015 (0.762 ± 0.381) $\frac{0.014 - 0.023}{(0.356 - 0.584)}$ $\frac{0.100 \pm 0.010}{(2.540 \pm 0.254)}$ (0.325 **+**0.040 **-**0.015 0.050 ± 0.010 N16E (REV F) (8.255 **+**1.016 **-**0.381 TYP (1.270 ± 0.254) TYP

Molded Dual-In-Line Package (N) Order Number DS3658N NS Package Number N16E

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National Semiconductor Corporation 1111 West Bardin Road Arlington, TX 76017 Tel: 1(800) 272-9959 Fax: 1(800) 737-7018 National Semiconductor Europe

Fax: (+49) 0-180-530 85 86
Email: cnjwge@tevm2.nsc.com
Deutsch Tel: (+49) 0-180-530 85 85
English Tel: (+49) 0-180-532 78 32
Français Tel: (+49) 0-180-532 78 61
Italiano Tel: (+49) 0-180-534 16 80

National Semiconductor Hong Kong Ltd. 13th Floor, Straight Block, Ocean Centre, 5 Canton Rd. Tsimshatsui, Kowloon Hong Kong Tel: (852) 2737-1600 Fax: (852) 2736-9960 National Semiconductor Japan Ltd. Tel: 81-043-299-2309 Fax: 81-043-299-2408