



# DS56 Dual Temperature Comparator

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#### **FEATURES**

- Factory calibrated for sensitivity of +6.20 mV/°C and thermometer accuracy of ±2.0°C over the 0°C to 85°C range and ±3°C over the 40°C to 0°C and +85°C to +125°C range
- Measurement range of -40°C to +125°C
- Integrated temperature sensor and voltage reference
- Two independent temperature setpoints with respective logic outputs with set hysteresis
- Narrow-body SO8 (150-mil) package
- Wide power supply range (2.7V ≤ V<sub>DD</sub> ≤ 5.5V)
- Functionally compatible with LM56xIM
- Applications include monitoring disk drives, printers, office equipment, HVAC, appliances, or any power/cost/temperaturesensitive environments.

#### **PIN ASSIGNMENT**

V <sub>REF</sub> [[[]] 1	8 🔟 V <sub>DD</sub>						
V <sub>T2</sub> 1 2	7 🎹 T <sub>OUT1</sub>						
V <sub>T1</sub> 3	6 Ш Т <sub>ОUT2</sub>						
GND 🎹 4	5 🔟 V <sub>TEMP</sub>						
DS56S 8-Pin SOIC (150-mil)							

#### **PIN DESCRIPTION**

V <sub>REF</sub>	- Bandgap Voltage Reference
	Output
$V_{T2}$	- Temperature Trip Point 2
$V_{T1}$	- Temperature Trip Point 1
GND	- Ground
V <sub>TEMP</sub>	- Temp Sensor Output Voltage
T <sub>OUT2</sub>	- Thermostat output for Trip
	Point 2
T <sub>OUT1</sub>	- Thermostat output for Trip
	Point 1
V <sub>DD</sub>	- Power Supply Voltage (2.7V
	to 5.5V)

#### DESCRIPTION

The DS56 Dual Temperature Comparator has two independent open-drain thermostat outputs and respective trip point inputs. The trip points are set with external resistors that divide down the 1.25V internal bandgap voltage reference. The voltage resulting from this resistive division is compared with the voltage corresponding to the device's junction temperature to define the thermostat output logic state. The internal temperature sensor has a typical sensitivity of  $+6.2\text{mV}/^{\circ}\text{C}$  and DC offset of +395 mV at 0°C. Both thermostat outputs have 5°C of hysteresis.

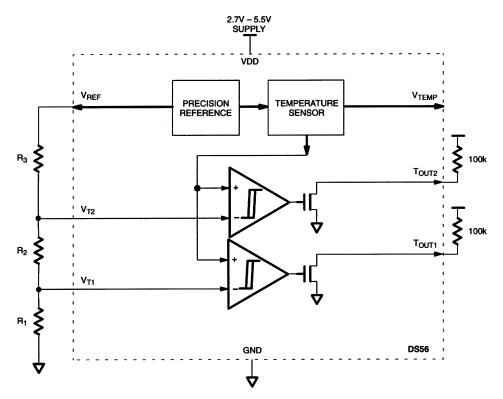
For applications that require temperature measurement as well as control, the temperature sensor voltage is brought out to a pin.

The DS56 is packaged in a compact 150-mil, 8-pin SOIC. Applications include disk drives, printers, office equipment, HVAC, appliances, or any power/cost/temperature-sensitive environments.



DETA	DETAILED PIN DESCRIPTION Table 1						
PIN	SYMBOL	TYPE	DESCRIPTION				
1	V <sub>REF</sub>	Analog output	Bandgap output 1.25V (nom) voltage reference output.				
2	V <sub>T2</sub>	Analog input	<b>Trip point 2</b> input associated with thermostat output 2.				
3	V <sub>T1</sub>	Analog input	Trip point 1 input associated with thermostat output 1.				
4	GND	GND	Ground pin.				
5	V <sub>TEMP</sub>	Analog output	<b>Temperature sensor output.</b> (+6.2 T <sub>DegC</sub> + 395) mV.				
6	Т	Digital output	Thermostat output 2. Active low output corresponding to trip				
0	T <sub>OUT2</sub>	(open-drain)	point 2. Typical hysteresis is 5.0°C.				
7	Т	Digital output	Thermostat output 1. Active low output corresponding to trip				
/	T <sub>OUT1</sub>	(open-drain)	point 1. Typical hysteresis is 5.0°C.				
8	V <sub>DD</sub>	V <sub>DD</sub>	Supply Voltage 2.7V - 5.5V input power pin.				

## DS56 FUNCTIONAL BLOCK DIAGRAM Figure 1



DS56

#### OVERVIEW

A block diagram of the DS56 is shown in Figure 1. The DS56 consists of three major components:

- 1. Precision bandgap voltage reference
- 2. Temperature sensor
- 3. Thermostat comparators

The DS56 incorporates an internal temperature sensor with a typical transfer function of (in mV with T expressed in  $^{\circ}$ C):

 $V_{\text{TEMP}}(T) = 6.2 T + 395$ 

The voltage associated with this transfer function is output on the  $V_{TEMP}$  pin, which has a typical output impedance of 1.5 k $\Omega$ .

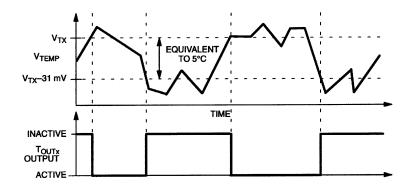
The DS56 has two independent temperature comparators, each with its own input (or trip point) and output. The comparison voltage (the  $V_{TEMP}$  transfer function) is the same for both comparators. Additionally, the hysteresis for both comparators is typically 5°C (31.0 mV).

The DS56 bandgap voltage reference is available on pin 1 to allow for a designer to set the trip point(s) by resistively dividing the reference voltage. One possible scenario is illustrated in Figure 1. For optimum performance, the VREF pin should be connected to a 50  $\mu$ A load.

The comparator outputs are taken off-chip via open-drain FETs, thus requiring external pullups. The thermostat transfer function is illustrated in Figure 2.

The supply range of the DS56 is 2.7V to 5.5V, allowing for applications ranging from industrial control to battery-powered portable equipment.

## THERMOSTAT OUTPUT TRANSFER FUNCTION Figure 2



#### **ABSOLUTE MAXIMUM RATINGS\*** Voltage on V<sub>DD</sub> (GND-0.3V) to +7.0V Input Current at any pin 5.0 mA Package Input Current 20 mA **Operating Temperature** -40°C to +125°C Storage Temperature -55°C to 150°C ESD Susceptibility (Human Body Model) 1kV Soldering Temperature (Note 2) 215°C for 60 seconds (Vapor Phase) 220°C for 15 seconds (IR)

\* This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operation sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods of time may affect reliability.

The Dallas Semiconductor DS56 is built to the highest quality standards and manufactured for long-term reliability. All Dallas Semiconductor devices are made using the same quality materials and manufacturing methods. However, the DS56 is not exposed to environmental stresses, such as burn-in, that some industrial applications require. For specific reliability information on this product, please contact the factory in Dallas at (972) 371-4448.

## **RECOMMENDED DC OPERATING CONDITIONS:**

	$(-40^{\circ}\text{C to } +125^{\circ}\text{C}; 2.7\text{V} \le \text{V}_{\text{DD}} \le 5.5\text{V})$						
PARAMETER	SYMBOL	CONDITION	MIN	TYP	MAX	UNITS	NOTES
Supply Voltage	V <sub>DD</sub>		2.7		5.5	V	1

## DC ELECTRICAL CHARACTERISTICS:

<b>Power Supply (Note 3)</b> $(-40^{\circ}\text{C to } +125^{\circ}\text{C}; 2.7\text{V} \le \text{V}_{\text{DD}} \le 50^{\circ}\text{C})$				$_{\text{DD}} \leq 5.5 \text{V}$ )			
PARAMETER	SYMBOL	CONDITION	MIN	TYP	MAX	UNITS	NOTES
Supply Current	I <sub>DD</sub>				225	μA	

## DC ELECTRICAL CHARACTERISTICS:

Temperature Sensor (Note 3)			(-40°C to +125°C; $2.7V \le V_{DD} \le 5.5V$ )				
PARAMETER	SYMBOL	CONDITION	MIN	TYP	MAX	UNITS	NOTES
Trip Point	TP <sub>ERR</sub>	$-40^{\circ}C \le T_A \le 125^{\circ}C$			±3	°C	4
Accuracy	I F ERR	$0^{\circ}C \le T_A \le 85^{\circ}C$			±2	C	4
V Accuracy	т	$-40^{\circ}C \le T_A \le 125^{\circ}C$			±3	°C	5
V <sub>TEMP</sub> Accuracy	T <sub>ERR</sub>	$0^{\circ}C \le T_A \le 85^{\circ}C$			±2	C	5
V <sub>TEMP</sub> DC Offset		T=0°C		395		mV	
Sensor Gain	? V/? T			6.25		mV/°C	
Trip Point Hysteresis	TP <sub>HYST</sub>		3.0	5.0	7.0	°C	
Power Supply		$2.7V \le V_{DD} \le 3.3V$			±2.3	mV	
Regulation		$3.0V \le V_{DD} \le 5.5V$			±0.3	mV/V	
V <sub>TEMP</sub> Output Impedance					1500	Ω	

## DC ELECTRICAL CHARACTERISTICS:

Bandgap Voltage Reference V <sub>REF</sub>			(-40°C to +125°C; $2.7V \le V_{DD} \le 5.5V$ )				
PARAMETER	SYMBOL	CONDITION	MIN	TYP	MAX	UNITS	NOTES
V <sub>REF</sub> Output	V <sub>REF</sub>		1.238	1.25	1.263	V	
Power Supply	? V <sub>REF</sub> /	$2.7V \le V_{DD} \le 3.3V$			±1.45	mV	
Regulation	? V <sub>DD</sub>	$30V \le V_{DD} \le 5.5V$			±0.2	mV/V	
Load Regulation	? V <sub>REF</sub> / ? I <sub>L</sub>	$+3.0\mu A{\leq}I_L{\leq}\\+50\mu A$			0.15	$mV/\mu A$	

# DC ELECTRICAL CHARACTERISTICS:

Trip Point Inputs V <sub>T1</sub> /V <sub>T2</sub>			$(-40^{\circ}\text{C to } +125^{\circ}\text{C}; 2.7\text{V} \le \text{V}_{\text{DD}} \le 5.5\text{V})$				
PARAMETER	SYMBOL	CONDITION	MIN	TYP	MAX	UNITS	NOTES
Input Bias Current	I <sub>BIAS</sub>				300	nA	
Input Range	V <sub>TIN</sub>		0		(V <sub>DD</sub> -1.0)	V	

#### DC ELECTRICAL CHARACTERISTICS: Thermostat Outputs Tout1/Tout2

(-40°C to +125°C;  $2.7V \le V_{DD} \le 5.5V$ )

			(				
PARAMETER	SYMBOL	CONDITION	MIN	TYP	MAX	UNITS	NOTES
Logic 1 Output Leakage Current	I <sub>OUT1</sub>	$V_{DD} = 5.0 V$			1.0	μA	
LOGICAL 0 Output Voltage	V <sub>OUT0</sub>	$I_{OUT}=+50\;\mu A$			0.4	V	

#### NOTES:

- 1. All voltages are referenced to ground, unless otherwise specified.
- 2. Solder according to IPC standards.
- 3. Specified for  $V_{\text{TEMP}}$  sourcing 1.0  $\mu$ A (max) and  $V_{\text{REF}}$  load current = 50  $\mu$ A.
- 4. Includes error associated with internal temperature sensor, bandgap voltage  $V_{REF}$ , and comparator offset. External resistance tolerance and temperature coefficient not included in this spec.
- 5. Thermometer error (expressed in °C) is the difference between  $[V_O(T) 395]/6.20$  and the DS56 case temperature at  $V_{DD} = 3.0V$ , thus taking into account sensor error, DC offset error, sensor amplifier gain variations, and amplifier nonlinearity.

#### TYPICAL DS56 THERMOMETER ERROR Figure 3

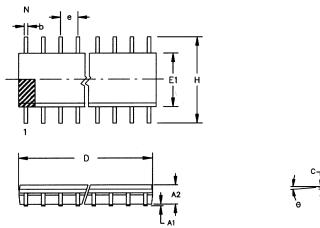
# TBD

## TYPICAL DS56 V<sub>TEMP</sub> TRANSFER FUNCTION Figure 4

## TBD

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# 8-, 14-, AND 16-PIN SOIC (.150" BODY WIDTH)



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PKG	8-P	IN	14-P	PIN	16-PIN		
DIM	MIN	MAX	MIN	MAX	MIN	MAX	
A IN.	0.053	0.069	0.053	0.069	0.053	0.069	
MM	1.35	1.75	1.35	1.75	1.35	1.75	
A1 IN.	0.004	0.010	0.004	0.010	0.004	0.010	
MM	0.10	0.25	0.10	0.25	0.10	0.25	
A2 IN.	0.048	0.062	0.048	0.062	0.048	0.062	
MM	1.24	1.57	1.24	1.57	1.24	1.57	
b IN.	0.012	0.020	0.012	0.020	0.012	0.020	
MM	0.030	0.50	0.30	0.50	0.30	0.50	
C IN.	0.007	0.011	0.007	0.011	0.007	0.011	
MM	0.17	0.28	0.17	0.28	0.17	0.28	
D IN.	0.188	0.196	0.337	0.344	0.386	0.393	
MM	4.78	4.98	8.55	8.74	9.80	9.98	
e IN.	0.050	BSC	0.050	BSC	0.050	BSC	
MM	1.27	BSC	1.27 1	BSC	1.27 I	BSC	
E1 IN.	0.150	0.158	0.150	0.158	0.150	0.158	
MM	3.81	4.01	3.81	4.01	3.81	4.01	
H IN.	0.230	0.244	0.230	0.244	0.230	0.244	
MM	5.84	6.20	5.84	6.20	5.84	6.20	
L IN.	0.016	0.050	0.016	0.050	0.016	0.050	
MM	0.40	0.89	0.40	0.89	0.40	0.89	
Q	0°	8°	0°	8°	0°	8°	