

National Semiconductor

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DS75114 Dual Differential Line Drivers

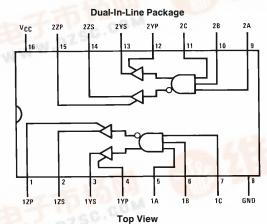
General Description

The DS75114 dual differential line driver is designed to provide differential output signals with high current capability for driving balanced lines, such as twisted pair at normal line impedances, without high power dissipation. The output stages are similar to TTL totem-pole outputs, but with the sink outputs, YS and ZS, and the corresponding active pullup terminals, YP and ZP, available on adjacent package pins. Since the output stages provide TTL compatible output levels, these devices may also be used as TTL expanders or phase splitters.

Features

- Each circuit offers a choice of open-collector or active pull-up (totem-pole) outputs
- Single 5V supply
- Differential line operation
- Dual channels
- TTL/LS compatibility
- Designed to be interchangeable with Fairchild 9614 line drivers
- Short-circuit protection of outputs
- High current outputs
- Clamp diodes at inputs and outputs to terminate line
- Single-ended or differential AND/NAND outputs
- Triple inputs

Connection Diagram



TL/F/5786-1

Positive logic: Y = ABC $Z = \overline{ABC}$

Order Number DS75114N See NS Package Number N16A

Truth Table

Inputs			Out	puts
Α	В	С	Υ	Z
Н	Н	Н	Н	L
All Other In	All Other Input Combinations			Н

W.DZSG.GOM H = high level



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Absolute Maximum Ratings (Note 1)

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/Distributors for availability and specifications.

Supply Voltage (V_{CC} Input Voltage 5.5V OFF-State Voltage Applied to Open-Collector Outputs 12V Maximum Power Dissipation* at 25°C Cavity Package 1433 mW Molded Package 1362 mW Operating Free-Air Temperature Range DS55114 -55°C to +125°C DS75114 0°C to +70°C

Storage Temperature Range -65°C to $+150^{\circ}\text{C}$ Lead Temperature ($1/_{16}^{\circ}$ from case

for 60 seconds): J Package 300°C

Lead Temperature (1/16" from case for 4 seconds): N Package

*Derate cavity package 9.6 mW/°C above 25°C; derate molded package 10.9 mW/°C above 25°C (Note 2).

260°C

Operating Conditions

	Min	Max	Units
Supply Voltage (V _{CC})			
DS75114	4.75	5.25	V
High Level Output Current (IOH)		-40	mA
Low Level Output Current (IOL)		40	mA
Operating Free-Air			
Temperature (T _A)			
DS75114	0	70	°C

Electrical Characteristics Over recommended operating free-air temperature range (unless otherwise noted)

				DS75114					
Symbol	Parameter	Conditions (Note 3)			Min	Typ (Note 4)	Max	Units	
V _{IH}	High Level Input Voltage				2			V	
V _{IL}	Low Level Input Voltage						0.8	•	
V _{IK}	Input Clamp Voltage	V _{CC} = Min, I	$_{ m I}=-12{ m mA}$			-0.9	-1.5	V	
V _{OH}	High Level Output Voltage	V _{CC} = Min, \	/ _{IH} = 2V	$I_{OH} = -10 \text{ mA}$	2.4	3.4		V	
		$V_{IL} = 0.8V$		$I_{OH} = -40 \text{ mA}$	2	3.0			
V _{OL}	Low Level Output Voltage	$V_{CC} = Min, V_{IH} = 2V, V_{IL} = 0.8V,$ $I_{OL} = 40 \text{ mA}$				0.2	0.45	V	
V _{OK}	V _{OK} Output Clamp Voltage		$V_{CC} = 5V, I_{O} = 40 \text{ mA}, T_{A} = 25^{\circ}\text{C}$			6.1	6.5	V	
		$V_{CC} = Max$, $I_O = -40$ mA, $T_A = 25$ °C				-1.1	-1.5	1 .	
I _{O(off)}	OFF-State Open-Collector Output Current	V _{CC} = Max	V _{OH} = 12V	T _A = 25°					
Output Current			VOH 12V	$T_A = 125^{\circ}C$				μA	
			V _{OH} = 5.25V	$T_A = 25^{\circ}C$		1	100		
				$T_A = 70^{\circ}C$			200		
II	Input Current at Maximum Input Voltage	$V_{CC} = Max, V_I = 5.5V$					1	mA	
I _{IH}	High Level Input Current	$V_{CC} = Max, V_I = 2.4V$					40	μΑ	
I _{IL}	Low Level Input Current	$V_{CC} = Max, V_I = 0.4V$				-1.1	-1.6	mA	
I _{OS}	Short-Circuit Output Current (Note 5)	$V_{CC} = Max, V_O = 0V$			-40	-90	-120	mA	
Icc	Supply Current	Inputs Grounded, No Load, T _A = 25°C		V _{CC} = Max		37	50	mA	
	(Both Drivers)			V _{CC} = 7V		47	70		

Note 1: All voltage values are with respect to network ground terminal.

Note 2: For operation above 25°C free-air temperature, refer to Dissipation Derating Curves in the Thermal information section.

Note 3: All parameters, with the exception of OFF-state open-collector output current, are measured with the active pull-up connected to the sink output.

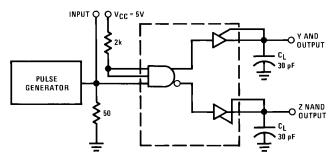
Note 4: All typical values are at $T_A = 25^{\circ}C$ and $V_{CC} = 5V$, with the exception of I_{CC} at 7V.

Note 5: Only one output should be shorted at a time, and duration of the short-circuit should not exceed one second.

Switching Characteristics	$V_{CC} = 5V, T_A = 25^{\circ}C$
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Symbol	Parameter	Conditions	DS75114			Units
- Oyillboi	T di diffictor	Conditions	Min	Тур	Max	Onito
^t PLH	Propagation Delay Time, Low-to-High-Level Output	C _L = 30 pF, <i>(Figure 1)</i>		15	30	ns
t _{PHL}	Propagation Delay Time High-to-Low-Level Output			11	30	ns

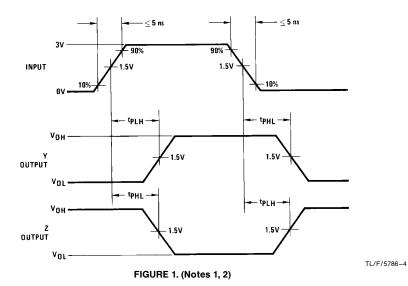
AC Test Circuit and Switching Time Waveforms

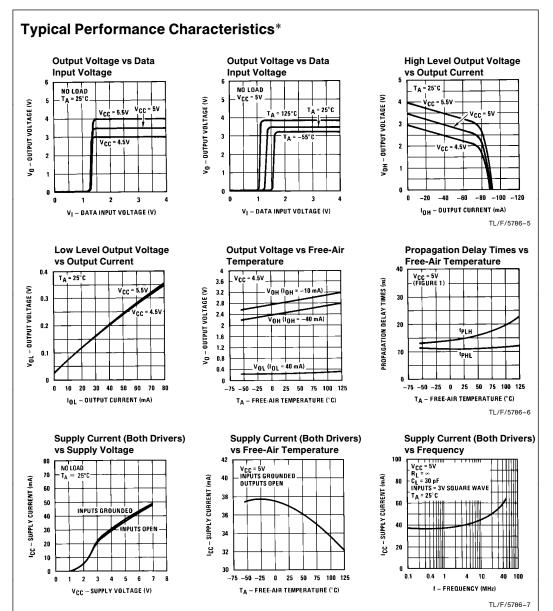


TL/F/5786-3

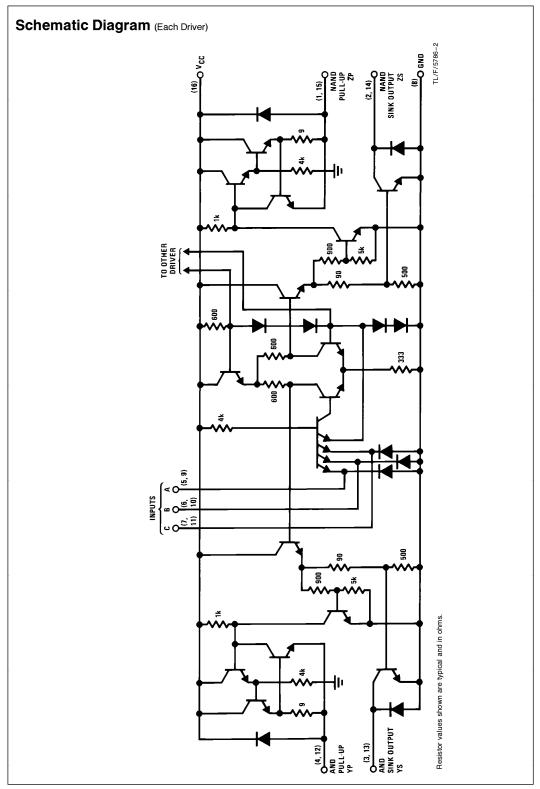
Note 1: The pulse generator has the following characteristics: $\rm Z_{OUT}=50\Omega$, $\rm t_{w}=100$ ns, PRR = 500 kHz.

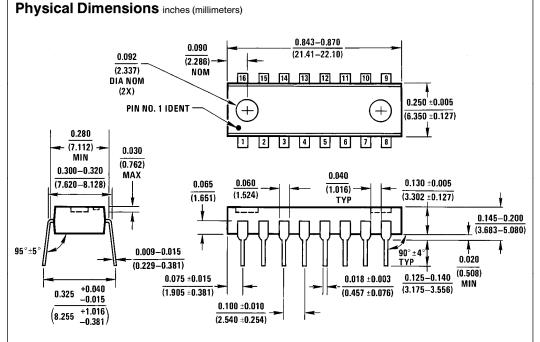
Note 2: C_L includes probe and jig-capacitance.





*Data for temperatures below 0°C and above 70°C and for supply voltages below 4.75V and above 5.25V are applicable to DS55114 circuits only. These parameters were measured with the active pull-up connected to the sink output.





Molded Dual-In-Line Package (N) Order Number DS75114N NS Package Number N16A

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N16A (REV E)