

-500mA / -50V Digital transistors

(with built-in resistors)

DTB114EK

Applications

Inverter, Interface, Driver

• Features

- 1)Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors (see equivalent circuit).
- 2)The bias resistors consist of thin-film resistors with complete isolation to allow positive biasing of the input. They also have the advantage of almost completely eliminating parasitic effects.
- 3)Only the on / off conditions need to be set for operation, making the device design easy.

• Structure

PNP epitaxial planar silicon transistor (Resistor built-in type)

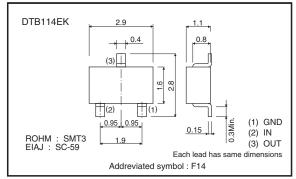
Packaging specifications

	Package	SMT3
	Packaging type	Taping
	Code	T146
Part No.	Basic ordering unit (pieces)	3000
DTB114EK		0
DTB114ES		-

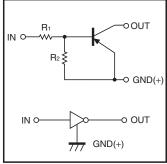
• Absolute maximum ratings (Ta=25°C)

Parameter	Symbol	Limits	Unit	
	Symbol	DTB114EK	Offic	
Supply voltage	Vcc	-50	V	
Input voltage	Vin	-40 to +10	V	
Output current	lc	-500	mA	
Power dissipation	Po	200	mW	
Junction temperature	Tj	150	Ĵ	
Storage temperature	Tstg	-55 to +150	°C	

• Dimensions (Unit : mm)



Inner circuit

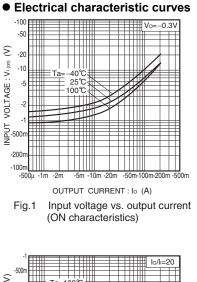


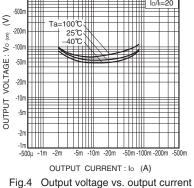
 $R_1=R_2=10k\Omega$

• Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
	VI(off)	-	-	-0.5	v	Vcc=-5V, Io=-100µA
Input voltage	VI(on)	-3	_	-	ľ	Vo=-0.3V, Io=-10mA
Output voltage	VO(on)	-	-0.1	-0.3	V	lo/l=-50mA/-2.5mA
Input current	h	-	-	-0.88	mA	Vi=-5V
Output current	IO(off)	-	-	-0.5	μΑ	Vcc=-50V, VI=0V
DC current gain	Gi	56	-	-	-	Vo=-5V, Io=-50mA
Input resistance	R1	7	10	13	kΩ	_
Resistance ratio	R2/R1	0.8	1	1.2	_	_
Transition frequency	f⊤ ∗	-	200	-	MHz	Vce=-10V, Ie=50mA, f=100MHz

* Characteristics of built-in transistor





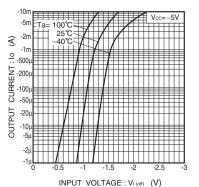
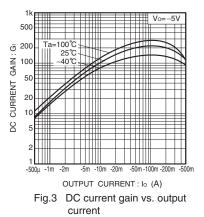
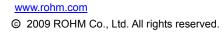


Fig.2 Output current vs. input voltage (OFF characteristics)





	Notes
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