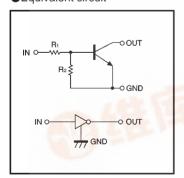
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# Digital transistors (built-in resistors) DTC143ZE / DTC143ZUA / DTC143ZKA DTC143ZCA / DTC143ZSA

### Features

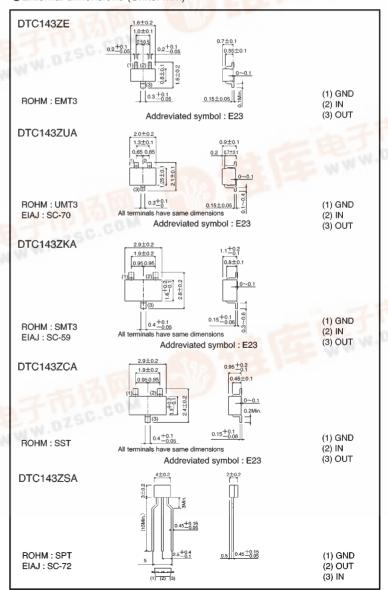
- Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors (see equivalent circuit).
- The bias resistors consist of thinfilm resistors with complete isolation to allow negative biasing of the input. They also have the advantage of almost completely eliminating parasitic effects.
- Only the on/off conditions need to be set for operation, making device design easy.
- •Structure

  NPN digital transistor
  (Built-in resistor type)
- Equivalent circuit





External dimensions (Units: mm)



# **Transistors**

# ●Absolute maximum ratings (Ta = 25°C)

Parameter	Symbol	Lim	its(DTC143	I India	
Parameter		Е	UA KA (	CA SA	Unit
Supply voltage	Vcc		50	V	
Input voltage	Vin		-5~+3	V	
Output current	lo	100			mA
	IC(Max.)	100			
Power dissipation	Pd	150	200	300	mW
Junction temperature	Tj	150			°C
Storage temperature	Tstg	<b>−55∼+150</b>			°C

# ●Electrical characteristics (Ta = 25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions		
Input voltage	VI (off)	_	_	0.5	.,	Vcc=5V, Io=100 μ A		
	VI (on)	1.3	_	_	V	Vo=0.3V, Io=5mA		
Output voltage	Vo(on)	_	0.1	0.3	V	lo/li=5mA/0.25mA		
Input current	lı	_	_	1.8	mA	V <sub>I</sub> =5V		
Output current	IO(off)	_	_	0.5	μΑ	Vcc=50V, Vi=0V		
DC current gain	Gı	80	_	_	_	Vo=5V, lo=10mA		
Input resistance	Rı	3.29	4.7	6.11	kΩ	_		
Resistance ratio	R2/R1	8	10	12	_	_		
Transition frequency	fτ	_	250	_	MHz	Vc=10V, I=-5mA, f=100MHz *		

<sup>\*</sup> Transition frequency of the device

# Packaging specifications

	Package	ЕМТЗ	UMT3	SMT3	SST3	SPT
	Packaging type	Taping	Taping	Taping	Taping	Taping
	Code	TL	T106	T146	T116	TP
Part No.	Basic ordering unit (pieces)	3000	3000	3000	3000	5000
DTC143ZE		0	_	_	_	_
DTC143ZU	A	_	0	_	_	_
DTC143ZK	A	_	_	0	_	_
DTC143ZC	A	_	_	_	0	_

### Electrical characteristic curves

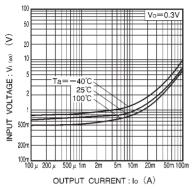


Fig.1 Input voltage vs. output current (ON characteristics)

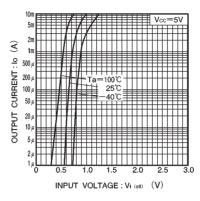


Fig.2 Output current vs. input voltage (OFF characteristics)

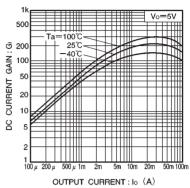


Fig.3 DC current gain vs. output current

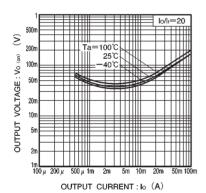


Fig.4 Output voltage vs. output current