600mA / 15V Digital transistors (with built-in resistors) DTC323TU / DTC323TK / DTC323TS

Applications

Muting, Inverter, Interface

Features

In addition to the features of regular digital transistors,

- 1) Low V_{CE(sat)} makes these transistors ideal for muting circuits. (Typ. 0.04V at I_C/I_B=50mA/2.5mA)
- 2) They can be used at high current. (I_{CMax}. =600mA)

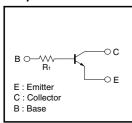
Structure

NPN epitaxial planar silicon transistor (Resistor built-in type)

Packaging specifications

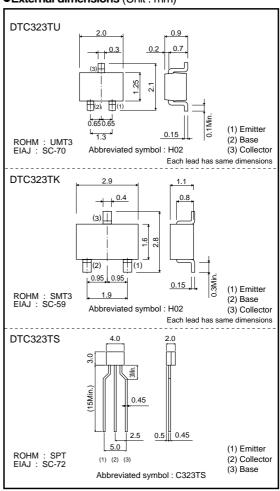
	Package	UMT3	SMT3	SPT
	Packaging type	Taping	Taping	Taping
	Code	T106	T146	TP
Part No.	Basic ordering unit (pieces)	3000	3000	5000
DTC323TU		0	_	_
DTC323TK		_	0	_
DTC323TS		_	_	0

Equivalent circuit



R1=2.2kΩ

●External dimensions (Unit: mm)



● Absolute maximum ratings (Ta=25°C)

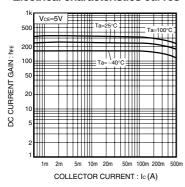
Parameter		Symbol	Limits	Unit	
Collector-base voltage		Vсво	30	V	
Collector-emitter voltage		VCEO	15	V	
Emitter-base voltage		VEBO	5	V	
Collector current		lc	600	mA	
Collector power	DTC323TU / DTC323TK	Pc	200	mW	
dissipation	DTC323TS	PC	300	IIIVV	
Junction temperature		Tj	150	°C	
Storage temperature		Tstg	-55 to +150	°C	

●External characteristics (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Collector-base breakdown voltage	ВУсво	30	-	-	V	Ic=50μA
Collector-emitter breakdown voltage	BVceo	15	_	_	V	Ic=1mA
Emitter-base breakdown voltage	ВУево	5	-	-	V	Iε=50μA
Collector cutoff current	Ісво	-	-	0.5	μА	Vcb=20V
Emitter cutoff current	ІЕВО	_	_	0.5	μА	V _{EB} =4V
Collector-emitter saturation voltage	VCE(sat)	_	40	80	mV	Ic/I _B =50mA/2.5mA
DC current transfer ratio	hfe	100	250	600	-	Ic=50mA , VcE=5V
Input resistance	R ₁	1.54	2.2	2.86	kΩ	-
Transition frequency	f⊤ *	_	200	-	MHz	Vc=10V , I=-50mA , f=100MHz
Output on resistance	Ron	_	0.65	_	Ω	V⊫7V , R∟=1kΩ , f=1kHz

^{*} Characteristics of built-in transistor

•Electrical characteristics curves



Ta=100°C

Ta=100

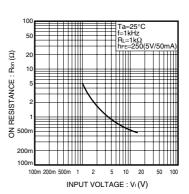


Fig.1 DC current gain vs. Collector current

Fig.2 Collector-emitter saturation voltage vs. Collector current

Fig.3 ON resistance vs. Input voltage

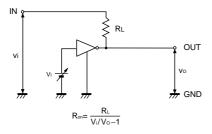


Fig.4 Output "ON" resistance (Ron) measurement circuit

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