# DTD133HK / DTD133HS

### Transistors

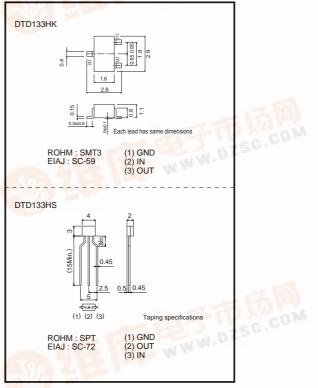
# Digital transistors (Includes resistors) DTD133HK / DTD133HS

#### Features

- 1) A built-in bias resistor allows inverter circuit configuration without external resistors for input (see equivalent circuit diagram).
- 2) The bias resistor consists of a thin-film resistor which is completely isolated, providing the capability to negative-bias the input, and avoiding parasitic effects.
- Operation starts by simply setting On/Off conditions, simplifying the design of equipment using the transistors.
- 4) High packing density.

# ●Equivalent circuit

## •External dimensions (Unit : mm)



#### Absolute maximum ratings (Ta=25°C)

Para	ameter	Symbol	Symbol Limits		
Supply volta	age	Vcc	50	V	
Input voltage	e	Vi	-6 to +20	V	
Output current		lc	500	mA	
Power dissipation	DTD133HK	Pd	200	mW	
	DTD133HS	Pu	300		
Junction temperature		Tj	150	°C	
Storage terr	Storage temperature		-55 to +150	°C	



#### Package, marking, and packaging specifications

Part No.	DTD133HK	DTD133HS
Package	SMT3	SPT
Marking	G08	_
Packaging code	T146	TP
Basic ordering unit (pieces)	3000	5000





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# Transistors

#### •Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Inputvoltogo	VI(off)	-	-	0.5	V	Vcc=5V , Io=100µA
Input voltage	VI(on)	2.0	-	-	V	Vo=0.3V , Io=20mA
Output voltage	VO(on)	-	0.1	0.3	V	lo=50mA , l⊫2.5mA
Input current	h	-	-	2.4	mA	Vi=5V
Output current	IO(off)	-	-	0.5	μΑ	Vcc=50V , VI=0V
DC current gain	Gi	56	-	-	-	Io=50mA , Vo=5V
Input resistance	R1	2.31	3.3	4.29	kΩ	_
Resistance ratio	R2/R1	2.4	3.0	3.7	-	-
Transition frequency	f⊤	-	200	-	MHz	Vce=10V , Ie= -50mA , f=100MHz *

\*Transition frequency of the device.

#### •Electrical characteristics curves

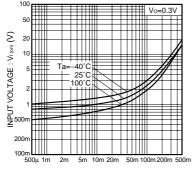




Fig.1 Input voltage vs. output current (ON characteristics)

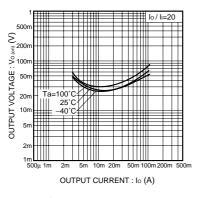


Fig.4 Output voltage vs. output current characteristics

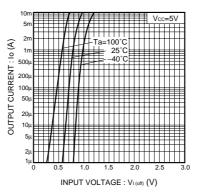
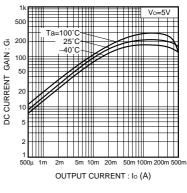
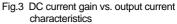


Fig.2 Output current vs. input voltage (OFF characteristics)





# Appendix

#### Notes

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