



New Product

**Si4453DY**  
Vishay Siliconix

**P-Channel 12-V (D-S) MOSFET**

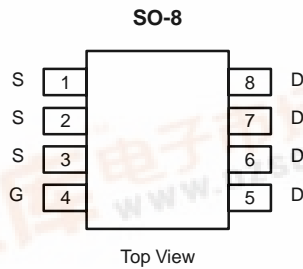
PRODUCT SUMMARY		
$V_{DS}$ (V)	$r_{DS(on)}$ ( $\Omega$ )	$I_D$ (A)
-12	0.0065 @ $V_{GS} = -4.5$ V	-14
	0.00775 @ $V_{GS} = -2.5$ V	-13
	0.01025 @ $V_{GS} = -1.8$ V	-12

**FEATURES**

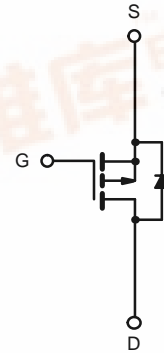
- TrenchFET® Power MOSFET

**APPLICATIONS**

- Load Switch
- Battery Switch



Ordering Information: Si4453DY  
Si4453DY-T1 (with Tape and Reel)



P-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS ( $T_A = 25^\circ\text{C}$ UNLESS OTHERWISE NOTED)					
Parameter	Symbol	10 secs	Steady State	Unit	
Drain-Source Voltage	$V_{DS}$	-12		V	
Gate-Source Voltage	$V_{GS}$	$\pm 8$			
Continuous Drain Current ( $T_J = 150^\circ\text{C}$ ) <sup>a</sup>	$I_D$	$T_A = 25^\circ\text{C}$	-14	-10	A
		$T_A = 70^\circ\text{C}$	-11.5	-8	
Pulsed Drain Current	$I_{DM}$	-50			
continuous Source Current (Diode Conduction) <sup>a</sup>	$I_S$	-2.7	-1.36		
Maximum Power Dissipation <sup>a</sup>	$P_D$	$T_A = 25^\circ\text{C}$	3.0	1.5	W
		$T_A = 70^\circ\text{C}$	1.9	0.95	
Operating Junction and Storage Temperature Range	$T_J, T_{stg}$	-55 to 150		$^\circ\text{C}$	

THERMAL RESISTANCE RATINGS					
Parameter	Symbol	Typical	Maximum	Unit	
Maximum Junction-to-Ambient <sup>a</sup>	$R_{thJA}$	$t \leq 10$ sec	33	42	$^\circ\text{C/W}$
		Steady State	70	84	
Maximum Junction-to-Foot (Drain)	$R_{thJF}$	16	21		

Notes:  
a. Surface Mounted on 1" x 1" FR4 Board.

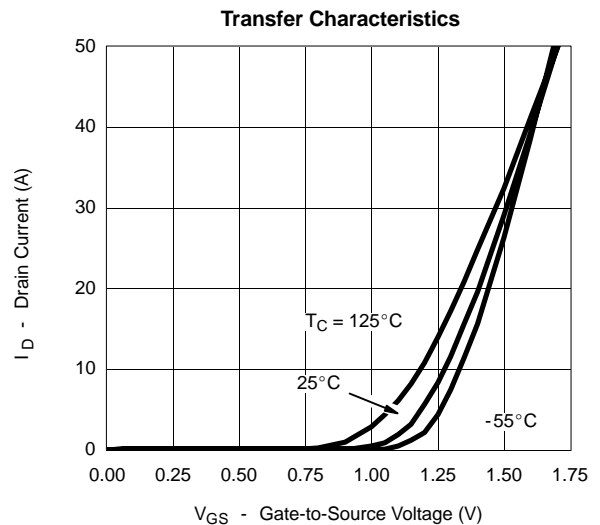
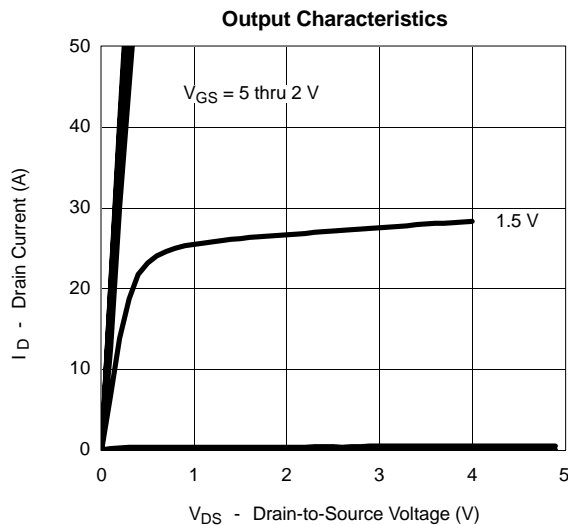


<b>SPECIFICATIONS (T<sub>J</sub> = 25 °C UNLESS OTHERWISE NOTED)</b>						
Parameter	Symbol	Test Condition	Min	Typ	Max	Unit
<b>Static</b>						
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = -600 μA	-0.4		-0.9	V
Gate-Body Leakage	I <sub>GSS</sub>	V <sub>DS</sub> = 0 V, V <sub>GS</sub> = ±8 V			±100	nA
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = -9.6 V, V <sub>GS</sub> = 0 V			-1	μA
		V <sub>DS</sub> = -9.6 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 70 °C			-10	
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	V <sub>DS</sub> = -5 V, V <sub>GS</sub> = -4.5 V	-30			A
Drain-Source On-State Resistance <sup>a</sup>	r <sub>DS(on)</sub>	V <sub>GS</sub> = -4.5 V, I <sub>D</sub> = -14 A		0.0051	0.0065	Ω
		V <sub>GS</sub> = -2.5 V, I <sub>D</sub> = -13 A		0.0062	0.00775	
		V <sub>GS</sub> = -1.8 V, I <sub>D</sub> = -12 A		0.0082	0.01025	
Forward Transconductance <sup>a</sup>	g <sub>fs</sub>	V <sub>DS</sub> = -6 V, I <sub>D</sub> = -14 A		80		S
Diode Forward Voltage <sup>a</sup>	V <sub>SD</sub>	I <sub>S</sub> = -2.7 A, V <sub>GS</sub> = 0 V		-0.6	-1.1	V
<b>Dynamic<sup>b</sup></b>						
Total Gate Charge	Q <sub>g</sub>	V <sub>DS</sub> = -6 V, V <sub>GS</sub> = -5 V, I <sub>D</sub> = -14 A		110	165	nC
Gate-Source Charge	Q <sub>gs</sub>			15		
Gate-Drain Charge	Q <sub>gd</sub>			27.5		
Turn-On Delay Time	t <sub>d(on)</sub>	V <sub>DD</sub> = -6 V, R <sub>L</sub> = 6 Ω I <sub>D</sub> ≅ -1 A, V <sub>GEN</sub> = -4.5 V, R <sub>G</sub> = 6 Ω		110	170	ns
Rise Time	t <sub>r</sub>			235	350	
Turn-Off Delay Time	t <sub>d(off)</sub>			410	620	
Fall Time	t <sub>f</sub>			285	430	
Gate Resistance	R <sub>g</sub>			3.6		
Source-Drain Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> = -2.1 A, di/dt = 100 A/μs		180	270	ns

**Notes**

- a. Pulse test; pulse width ≤ 300 μs, duty cycle ≤ 2%.
- b. Guaranteed by design, not subject to production testing.

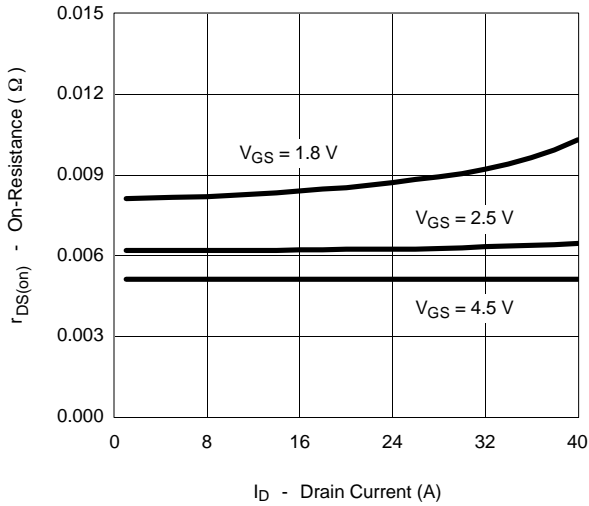
**TYPICAL CHARACTERISTICS (25 °C UNLESS NOTED)**



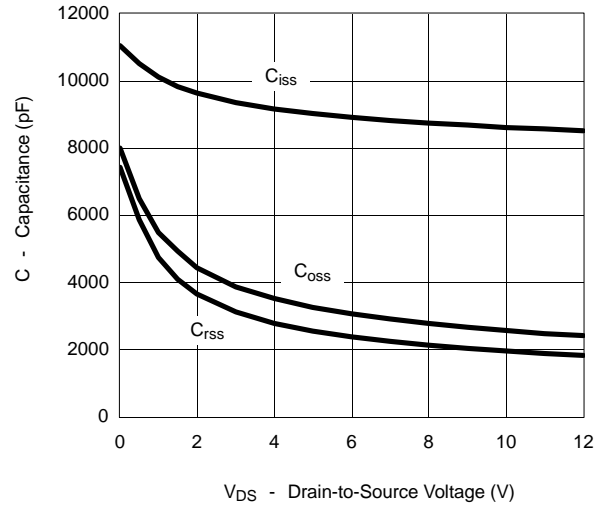


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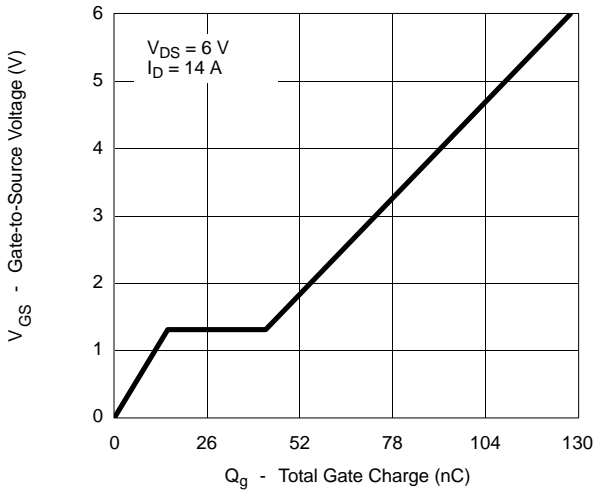
On-Resistance vs. Drain Current



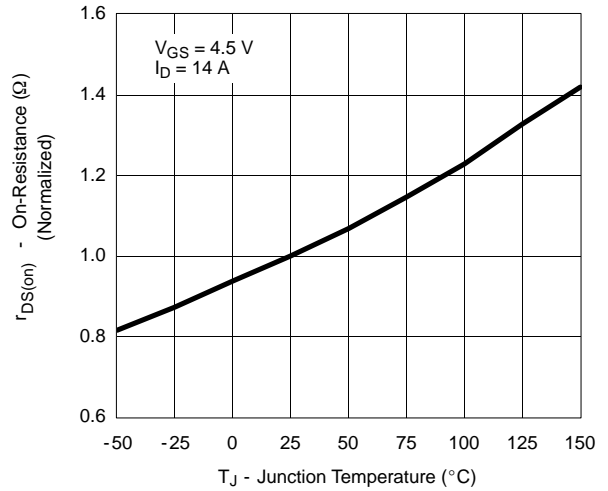
Capacitance



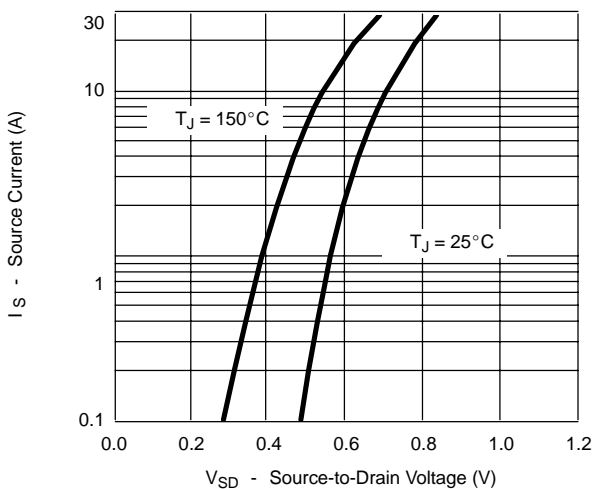
Gate Charge



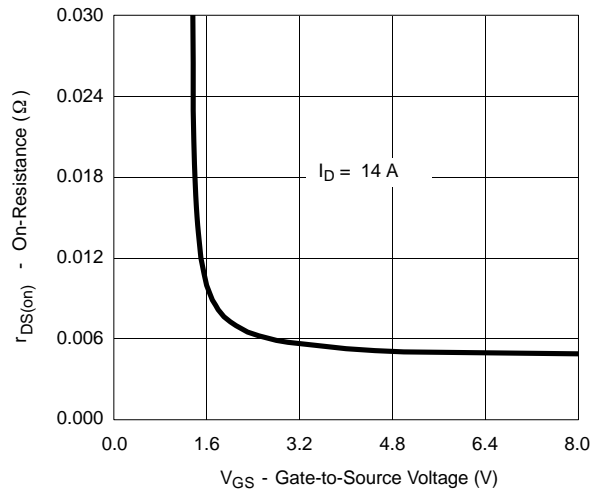
On-Resistance vs. Junction Temperature



Source-Drain Diode Forward Voltage

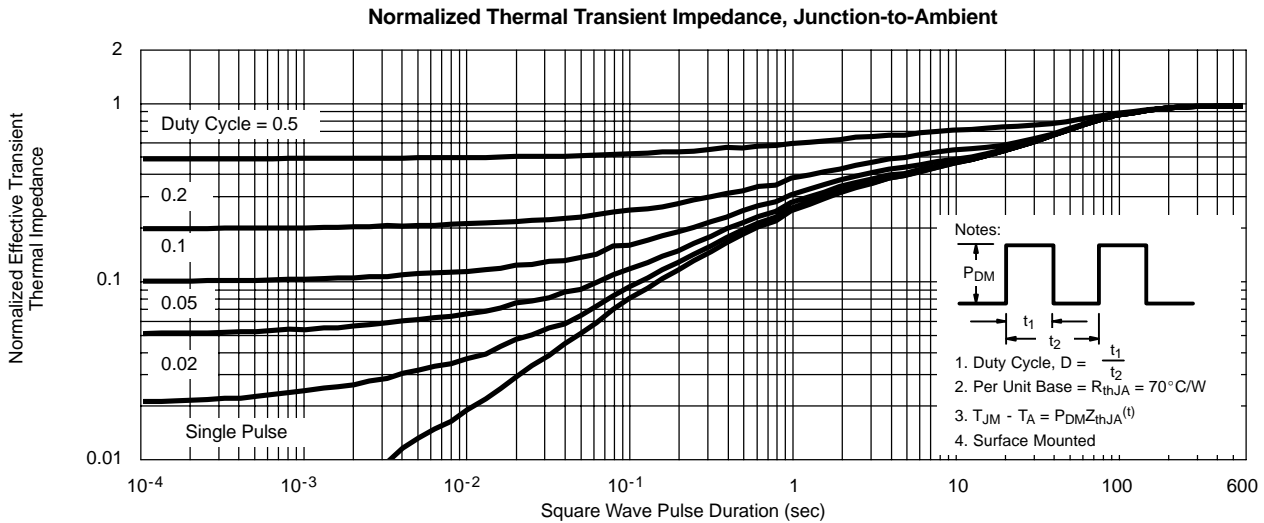
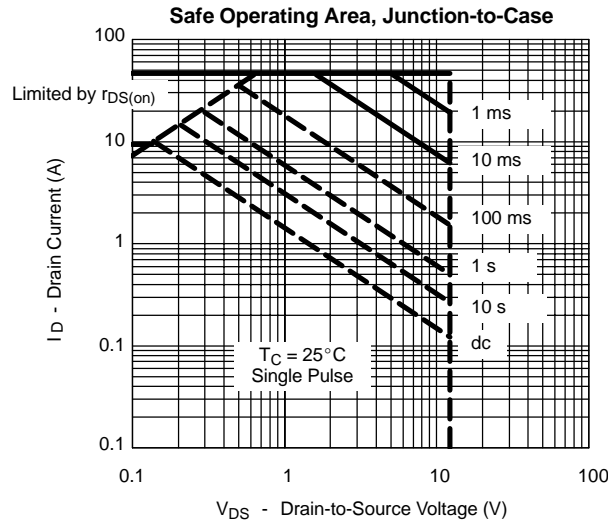
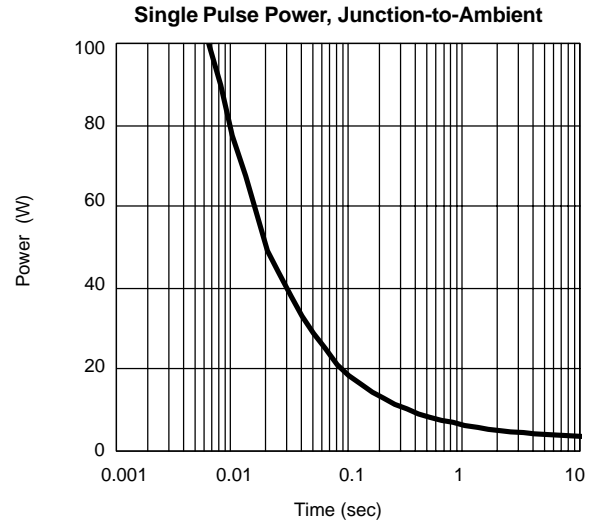
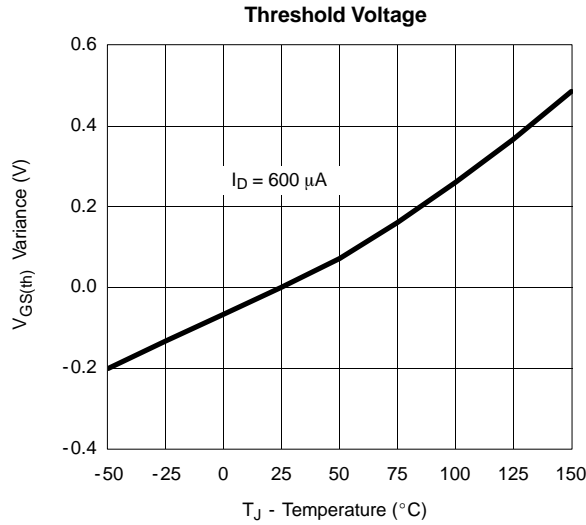


On-Resistance vs. Gate-to-Source Voltage





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