

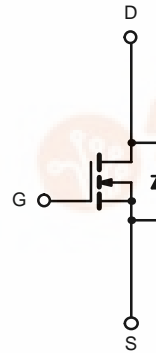
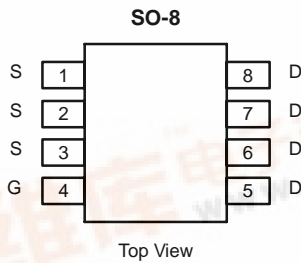


Si4488DY
Vishay Siliconix

N-Channel 150-V (D-S) MOSFET

PRODUCT SUMMARY		
V _{DS} (V)	r _{DS(on)} (Ω)	I _D (A)
150	0.050 @ V _{GS} = 10 V	5.0

TrenchFET[®]
Power MOSFETs



Ordering Information: Si4488DY
Si4488DY-T1 (with Tape and Reel)

ABSOLUTE MAXIMUM RATINGS (T _A = 25°C UNLESS OTHERWISE NOTED)					
Parameter	Symbol	10 secs	Steady State	Unit	
Drain-Source Voltage	V _{DS}	150		V	
Gate-Source Voltage	V _{GS}	±20			
Continuous Drain Current (T _J = 150°C) ^a	I _D	T _A = 25°C	5.0	3.5	A
		T _A = 70°C	4.0	2.8	
Pulsed Drain Current	I _{DM}	50			
Avalanch Current	L = 0.1 mH	25		I _{AS}	
Continuous Source Current (Diode Conduction) ^a	I _S	2.8	1.4	A	
Maximum Power Dissipation ^a	P _D	T _A = 25°C	3.1	1.56	W
		T _A = 70°C	2.0	1.0	
Operating Junction and Storage Temperature Range	T _J , T _{stg}	-55 to 150		°C	

THERMAL RESISTANCE RATINGS					
Parameter	Symbol	Typical	Maximum	Unit	
Maximum Junction-to-Ambient ^a	R _{thJA}	t ≤ 10 sec	33	40	°C/W
		Steady State	65	80	
Maximum Junction-to-Foot (Drain)	R _{thJF}	17	21		

Notes:
a. Surface Mounted on 1" x 1" FR4 Board.

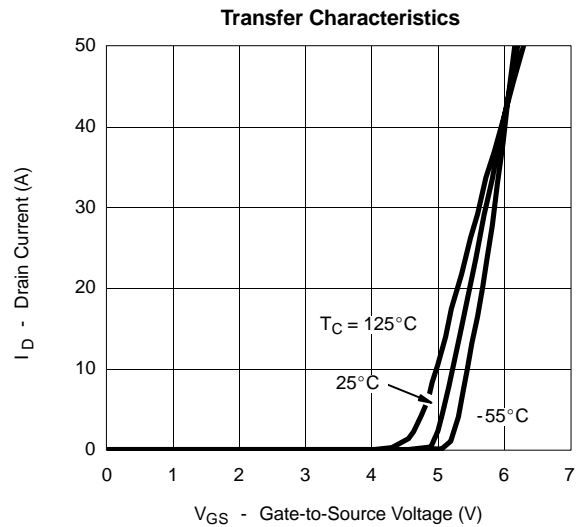
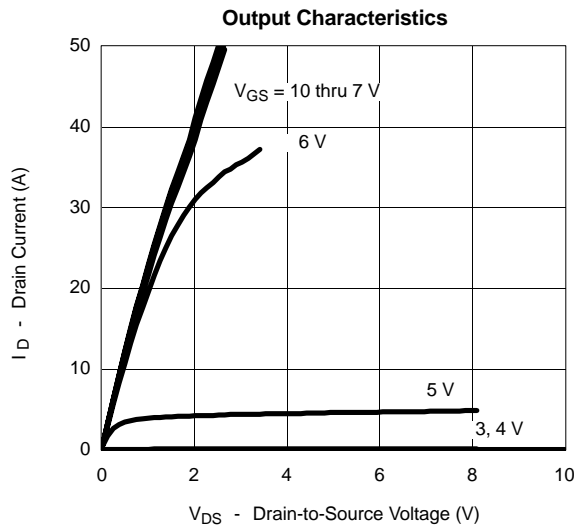


SPECIFICATIONS (T_J = 25 °C UNLESS OTHERWISE NOTED)						
Parameter	Symbol	Test Condition	Min	Typ	Max	Unit
Static						
Gate Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = 250 μA	2.0			V
Gate-Body Leakage	I _{GSS}	V _{DS} = 0 V, V _{GS} = ±20 V			±100	nA
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 120 V, V _{GS} = 0 V			1	μA
		V _{DS} = 120 V, V _{GS} = 0 V, T _J = 55 °C			5	
On-State Drain Current ^a	I _{D(on)}	V _{DS} ≥ 5 V, V _{GS} = 10 V	50			A
Drain-Source On-State Resistance ^a	r _{DS(on)}	V _{GS} = 10 V, I _D = 5 A		0.041	0.050	Ω
Forward Transconductance ^a	g _{fs}	V _{DS} = 15 V, I _D = 5 A		18		S
Diode Forward Voltage ^a	V _{SD}	I _S = 2.8 A, V _{GS} = 0 V		0.75	1.1	V
Dynamic^b						
Total Gate Charge	Q _g	V _{DS} = 75 V, V _{GS} = 10 V, I _D = 5 A		30	36	nC
Gate-Source Charge	Q _{gs}			8.5		
Gate-Drain Charge	Q _{gd}			8.5		
Gate Resistance	R _g		0.2	0.85	1.2	Ω
Turn-On Delay Time	t _{d(on)}	V _{DD} = 75 V, R _L = 15 Ω I _D = 5 A, V _{GEN} = 10 V, R _G = 6 Ω		12	18	ns
Rise Time	t _r			7	11	
Turn-Off Delay Time	t _{d(off)}			22	33	
Fall Time	t _f			10	15	
Source-Drain Reverse Recovery Time	t _{rr}		I _F = 2.8 A, di/dt = 100 A/μs		40	

Notes

- a. Pulse test; pulse width ≤ 300 μs, duty cycle ≤ 2%.
- b. Guaranteed by design, not subject to production testing.

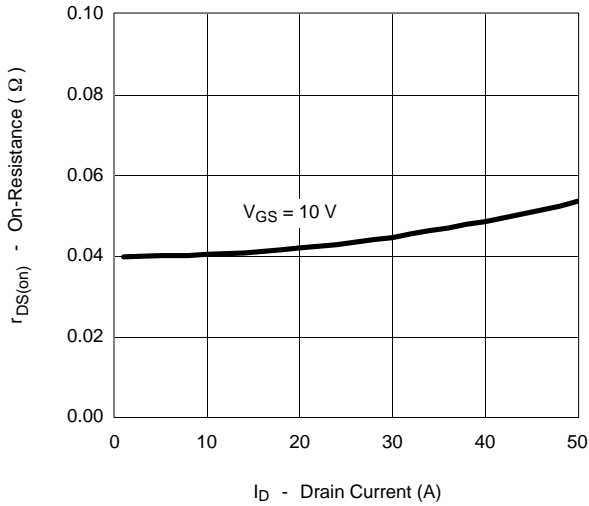
TYPICAL CHARACTERISTICS (25 °C UNLESS NOTED)



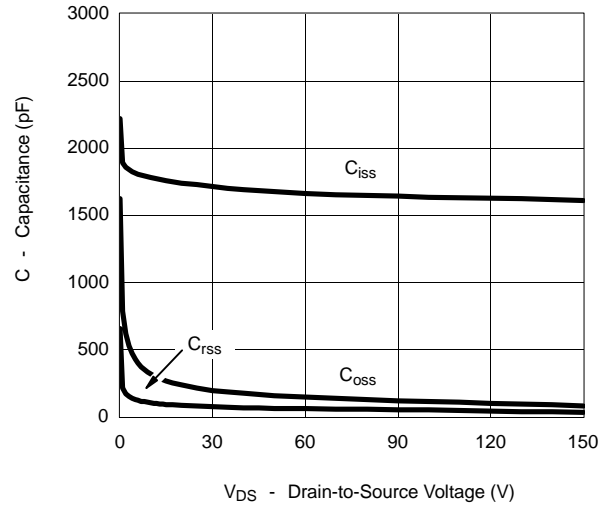


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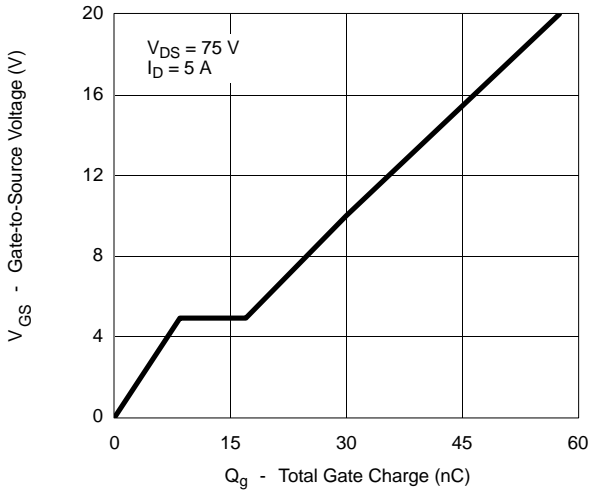
On-Resistance vs. Drain Current



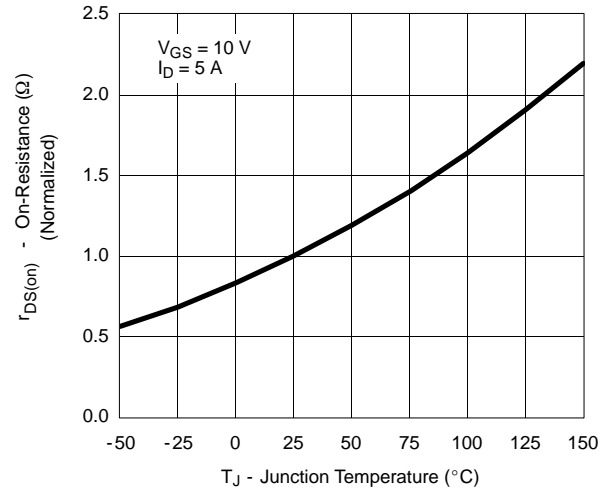
Capacitance



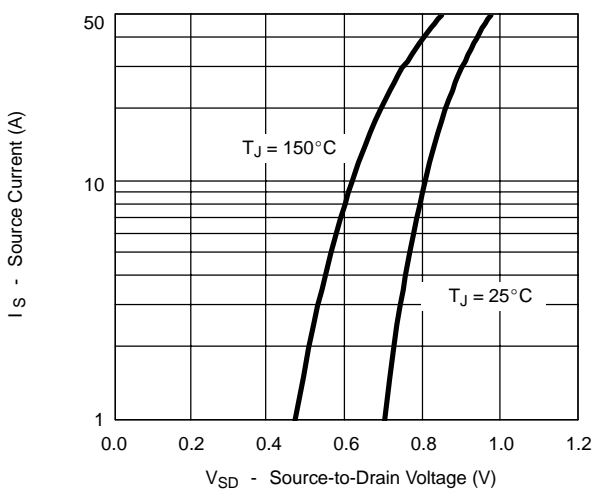
Gate Charge



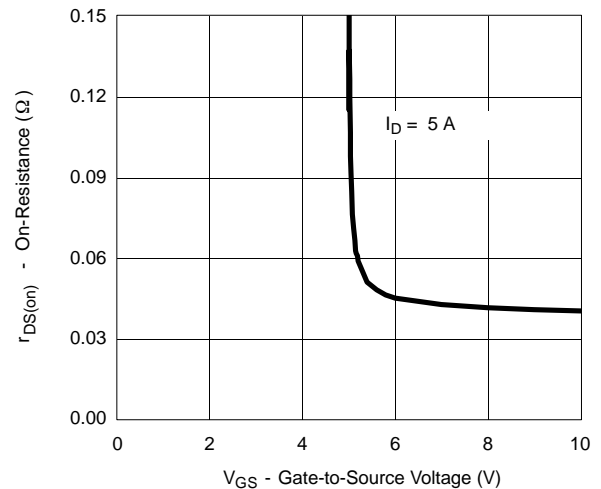
On-Resistance vs. Junction Temperature



Source-Drain Diode Forward Voltage

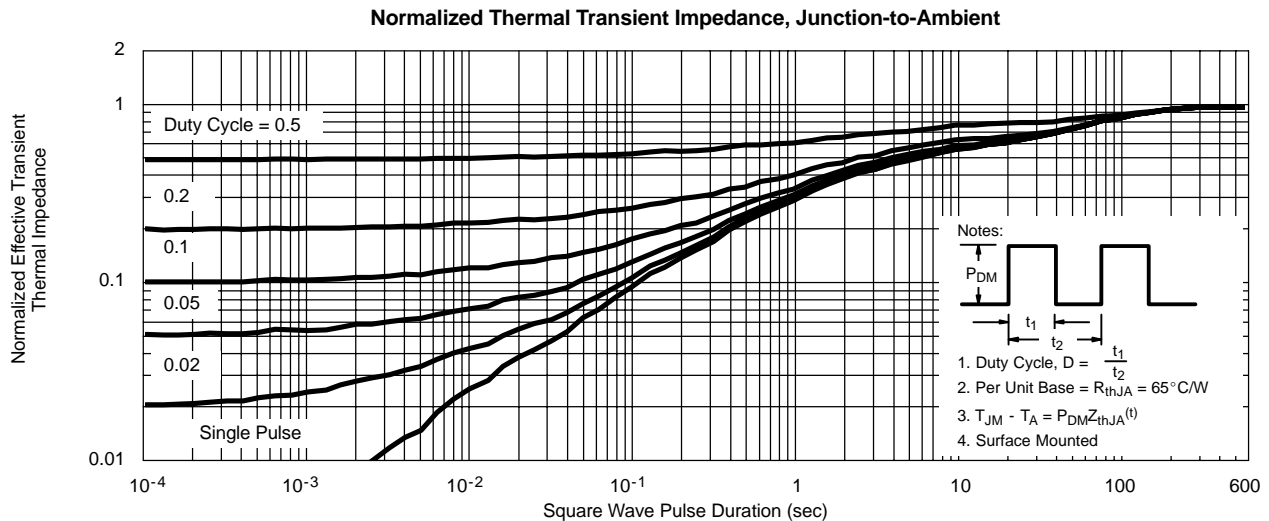
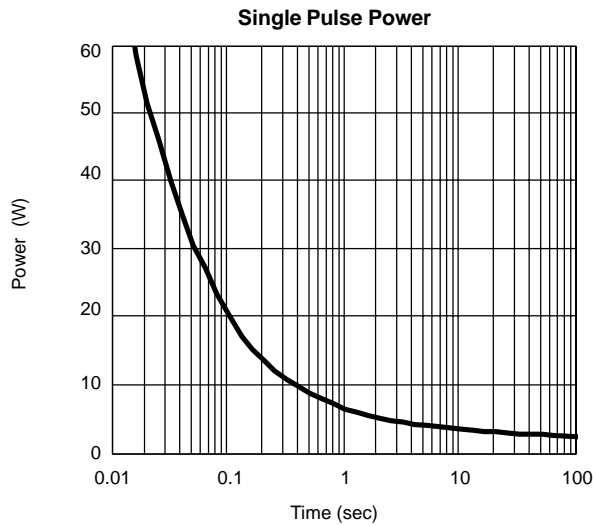
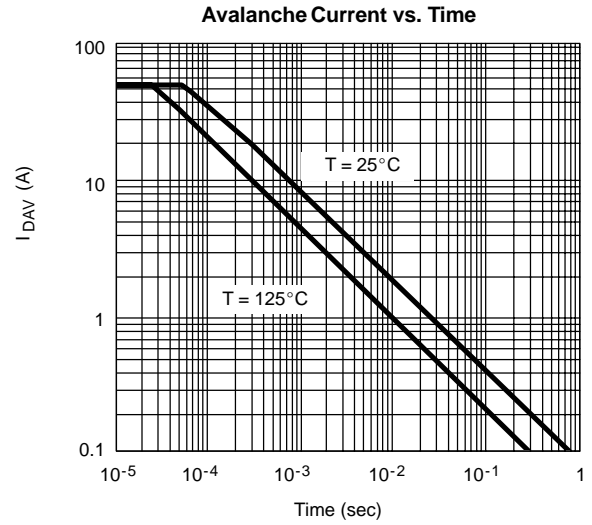
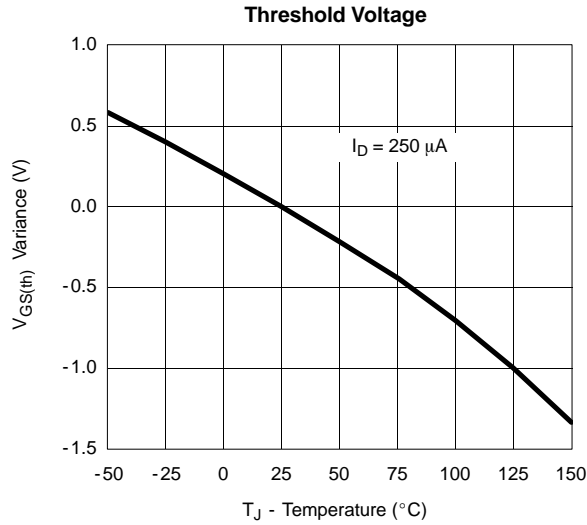


On-Resistance vs. Gate-to-Source Voltage





TYPICAL CHARACTERISTICS (25°C UNLESS NOTED)





TYPICAL CHARACTERISTICS (25°C UNLESS NOTED)

