



# EL1502C - Product Brief

## High Power Differential Line Driver/Receiver

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### Features

- Complete ADSL differential driver and receiver
- 54V<sub>P-P</sub> output drive into 100Ω differential
- 50V<sub>P-P</sub> output drive into 50Ω differential
- Driver 2<sup>nd</sup>/3<sup>rd</sup> harmonics of -70dBc/-75dBc at 2V<sub>P-P</sub> into 100Ω
- Low supply current - 12.5mA per drive amplifier and 5mA per receive amplifier
- Standard surface-mount package

### Applications

- ADSL line driver
- HDSL2 line driver
- Video distribution amplifier

### Ordering Information

Part No	Package	Tape & Reel	Outline #
EL1502CM	20-Lead SO		MDP0027

### General Description

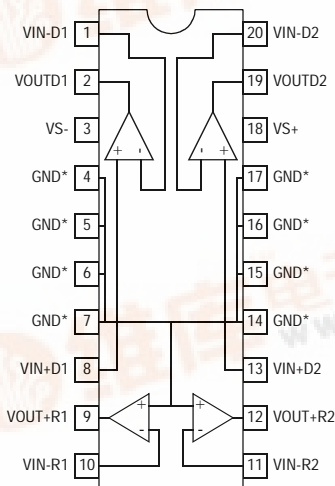
The EL1502C is a complete transceiver solution for high speed data transmission over twisted pair wires. It contains two wideband, high-voltage drivers and two low-noise receive amplifiers. The EL1502C is ideally suited for both ADSL and HDSL2 applications, in which it can supply a 50V<sub>P-P</sub> signal into a 50Ω differential load while exhibiting very low distortion. The receive amplifiers also have very low distortion and low-noise for superior reception of small signals.

The line drivers of the EL1502C are current-feedback type amplifiers, which achieve a high slew rate while consuming moderate power. They retain their frequency response over a wide range of externally set gains. The receive amplifiers are of the voltage-feedback type and can be used to implement a hybrid coupler or filter functions using external passive components.

The EL1502C operates on ±5V to ±15V supplies. It retains its bandwidth and linearity over the complete supply range.

The device is supplied in a standard form factor, 20-pin SOIC package. The eight center package pins of the package are used as ground connections and heat spreaders, allowing for operation over the full -40°C to +85°C temperature range.

### Connection Diagrams



20-Pin Wide Body SOIC

\* Gnd pins are heat spreaders

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Note: All information contained in this data sheet has been carefully checked and is believed to be accurate as of the date of publication; however, this data sheet cannot be a "controlled document". Current revisions, if any, to these specifications are maintained at the factory and are available upon your request. We recommend checking the revision level before finalization of your design documentation.



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HIGH PERFORMANCE ANALOG INTEGRATED CIRCUITS

### **Elantec Semiconductor, Inc.**

675 Trade Zone Blvd.  
Milpitas, CA 95035  
Telephone: (408) 945-1323  
(888) ELANTEC  
Fax: (408) 945-9305  
European Office: +44-118-977-6020  
Japan Technical Center: +81-45-682-5820

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