

Data Sheet May 6, 2005 FN7113.1

High Speed, Single Channel, Power MOSFET Driver

The EL7104 is a matched driver IC that improves the operation of the industry-standard TC-4420/29 clock drivers. The Elantec version is a very high speed driver capable of delivering peak currents of 1A into highly capacitive loads. The high speed performance is achieved by means of a proprietary "Turbo-Driver" circuit that speeds up input stages by tapping the wider voltage swing at the output. Improved speed and drive capability are enhanced by matched rise and fall delay times. These matched delays maintain the integrity of input-to-output pulse-widths to reduce timing errors and clock skew problems. This improved performance is accompanied by a 10-fold reduction in supply currents over bipolar drivers, yet without the delay time problems commonly associated with CMOS drivers.

The EL7104 is available in 8-pin SO and 8-pin PDIP packages and is specified for operation over the full -40°C to +85°C temperature range.

Ordering Information

PART NUMBER	PACKAGE	TAPE & REEL	PKG. DWG. #
EL7104CN	8-Pin PDIP	-	MDP0031
EL7104CS	8-Pin SO	-	MDP0027
EL7104CS-T7	8-Pin SO	7"	MDP0027
EL7104CS-T13	8-Pin SO	13"	MDP0027
EL7104CSZ (See Note)	8-Pin SO (Pb-free)	O.WWW.D	MDP0027
EL7104CSZ-T7 (See Note)	8-Pin SO (Pb-free)	7"	MDP0027
EL7104CSZ-T13 (See Note)	8-Pin SO (Pb-free)	13"	MDP0027

NOTE: Intersil Pb-free products employ special Pb-free material sets; molding compounds/die attach materials and 100% matte tin plate termination finish, which are RoHS compliant and compatible with both SnPb and Pb-free soldering operations. Intersil Pb-free products are MSL classified at Pb-free peak reflow temperatures that meet or exceed the Pb-free requirements of IPC/JEDEC J STD-020.

Features

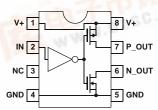
- Industry-standard driver replacement
- Improved response times
- · Matched rise and fall times
- Reduced clock skew
- · Low output impedance
- · Low input capacitance
- · High noise immunity
- · Improved clocking rate
- · Low supply current
- Wide operating range
- Separate drain connections
- Pb-Free available (RoHS compliant)

Applications

- · Clock/line drivers
- · CCD drivers
- · Ultrasound transducer drivers
- Power MOSFET drivers
- Switch mode power supplies
- Resonant charging
- Cascoded drivers

Pinout

EL7104 (8-PIN SO, PDIP) TOP VIEW





EL7104

Absolute Maximum Ratings $(T_A = 25^{\circ}C)$

Supply (V+ to GND)	Storage Temperature Range65°C to +150°C
Input Pins0.3V to +0.3V above V+	Operating Junction Temperature
Peak Output Current	Power Dissipation
Ambient Operating Temperature	SO570mW
	PDIP1050mW

CAUTION: Stresses above those listed in "Absolute Maximum Ratings" may cause permanent damage to the device. This is a stress only rating and operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied.

IMPORTANT NOTE: All parameters having Min/Max specifications are guaranteed. Typ values are for information purposes only. Unless otherwise noted, all tests are at the specified temperature and are pulsed tests, therefore: $T_J = T_C = T_A$

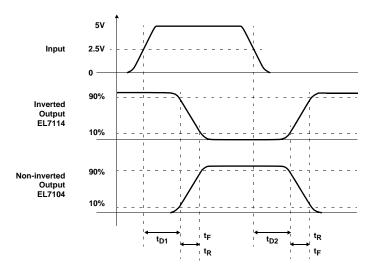
DC Electrical Specifications V+ = 15V, T_A = 25°C unless otherwise specified.

PARAMETER	DESCRIPTION	CONDITIONS	MIN	TYP	MAX	UNIT		
INPUT								
V _{IH}	Logic "1" Input Voltage		2.4			V		
I _{IH}	Logic "1" Input Current	@V+		0.1	10	μΑ		
V _{IL}	Logic "0" Input Voltage				0.8	V		
I _{IL}	Logic "0" Input Current	@0V		0.1	10	μΑ		
V _{HVS}	Input Hysteresis			0.3		V		
ОИТРИТ								
R _{OH}	Pull-Up Resistance	I _{OUT} = -100mA		1.5	4	Ω		
R _{OL}	Pull-Down Resistance	I _{OUT} = +100mA		2	4	Ω		
lout	Output Leakage Current	V+/GND		0.2	10	μΑ		
I _{PK}	Peak Output Current	Source/Sink		4.0		Α		
I _{DC}	Continuous Output Current	Source/Sink	200			mA		
POWER SUPPLY								
Is	Power Supply Current	Input = V+		4.5	7.5	mA		
V _S	Operating Voltage		4.5		16	V		

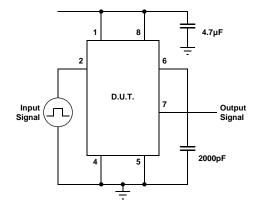
AC Electrical Specifications V = 15V, $T_A = 25$ °C unless otherwise specified.

PARAMETER	DESCRIPTION	CONDITIONS	MIN	TYP	MAX	UNIT	
SWITCHING CHARACTERISTICS ($V_{DD} = V_{H} = 12V$; $V_{L} = -3V$)							
t _R	Rise Time	C _L = 1000pF		7.5		ns	
		C _L = 2000pF		10	20	ns	
t _F	Fall Time	C _L = 1000pF		10		ns	
		C _L = 2000pF		15	20	ns	
t _{D-ON}	Turn-On Delay Time	See Timing Table		18	25	ns	
t _{D-OFF}	Turn-Off Delay Time	See Timing Table		18	25	ns	

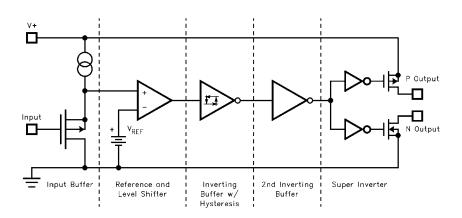
Timing Table



Standard Test Configuration

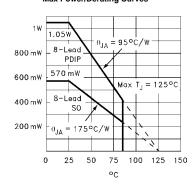


Simplified Schematic

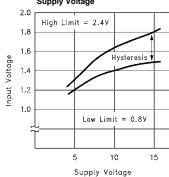


Typical Performance Curves

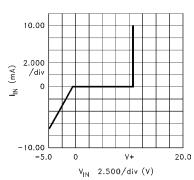
Max Power/Derating Curves



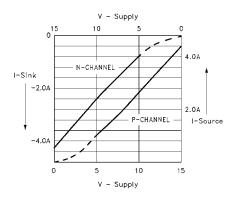
Switch Threshold vs Supply Voltage



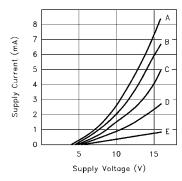
Input Current vs Voltage



Peak Drive vs Supply Voltage



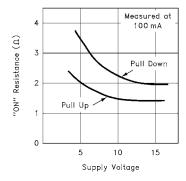
Quiescent Supply Current



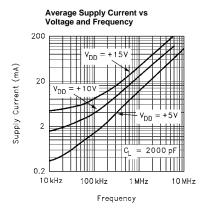
CASE

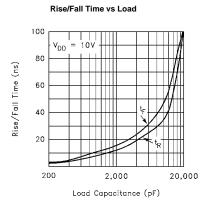
CASE.			
Device	Input Level	Curve	
EL7104	GND	A	
EL7104	V+	C	
EL7114	GND	C	
EL7114	V+	E	

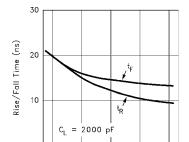
"ON" Resistance vs Supply Voltage



Typical Performance Curves (Continued)







12.5

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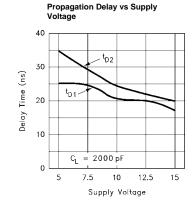
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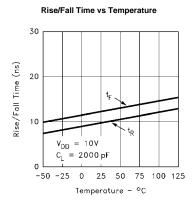
7.5

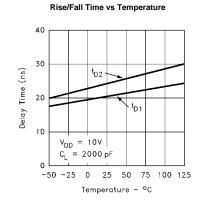
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Supply Voltage

Rise/Fall Time vs Supply Voltage







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