# EMG2 / UMG2N / FMG2A

### **Transistors**

# Emitter common (dual digital transistors)

# EMG2 / UMG2N / FMG2A

#### Features

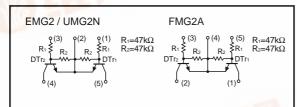
- 1) Two DTC144E chips in a EMT or UMT or SMT package.
- 2) Mounting cost and area can be cut in half.

#### Structure

Dual NPN digital transistor (each with a single built in resistors)

The following characteristics apply to both the DTr<sub>1</sub> and DTr<sub>2</sub>.

#### Equivalent circuit



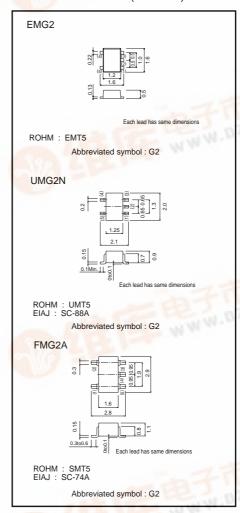
## ● Absolute maximum ratings (Ta = 25°C)

Parameter Parameter		Symbol	Limits	Unit	
Supply voltage		Vcc	50	V	
Input voltage		Vin	40	V	
		VIN	-10		
Output current		lo	30	m 1	
		Ic (Max.)	100	mA	
Power dissipation	EMG2, UMG2N	Pd	150 (TOTAL)	mW *1	
	FMG2A	Pu	300 (TOTAL)		
Junction temperature		Tj	150	°C	
Storage temperature		Tstg	-55 to +150	°C	

<sup>\*1 120</sup>mW per element must not be exceeded.

#### \*2 200mW per element must not be exceeded.

# ●External dimensions (Unit : mm)





#### ●Electrical characteristics (Ta = 25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions	
land a land	V <sub>I (off)</sub>	-	-	0.5	V	Vcc=5V, Io=100μA	
Input voltage	VI (on)	3	-	-	V	Vo=0.3V, Io=2mA	
Output voltage	Vo (on)	-	0.1	0.3	V	lo=10mA, li=0.5mA	
Input current	lı	-	-	0.18	mA	Vi=5V	
Output current	lo (off)	-	-	0.5	μΑ	Vcc=50V, Vi=0V	
DC current gain	Gı	68	-	-	-	Vo=5V, Io=5mA	
Transition frequency	f⊤	-	250	-	MHz	Vc=10V, I=-5mA, f=100MHz *	
Input resistance	R <sub>1</sub>	32.9	47	61.1	kΩ	-	
Resistance ratio	R2/R1	0.8	1	1.2	-	_	

<sup>\*</sup> Transition frequency of the device

Packaging specifications

	Package	Taping						
	Code	T2R	TR	T148				
Туре	Basic ordering unit (pieces)	8000	3000	3000				
EMG2		0	_	_				
UMG2N		_	0	_				
FMG2A		_	_					

#### •Electrical characteristic curves

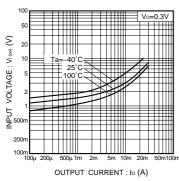


Fig.1 Input voltage vs. output current (on-characteristics)

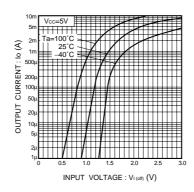


Fig.2 Output current vs. input voltage (off-characteristics)

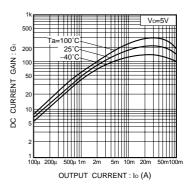


Fig.3 DC current gain vs. output

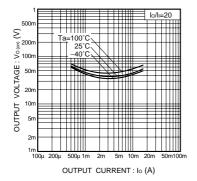


Fig.4 Output voltage vs. output current

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