



3.3V/5V LVTTTL/LVCMOS-to-DIFFERENTIAL LVPECL TRANSLATOR

SY10EPT20V
SY100EPT20V

FEATURES

- 3.3V and 5V power supply options
- 300ps typical propagation delay
- Differential LVPECL output
- I_{CC} Max 20mA
- PNP LVTTTL input for minimal loading
- Q output will default HIGH with inputs open
- High bandwidth to 800MHz typical
- Available in 8-pin MSOP and SOIC package

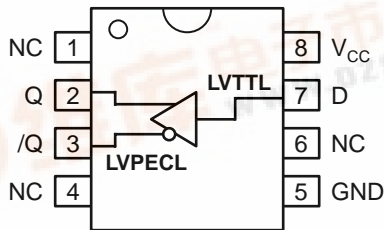
DESCRIPTION

The SY10/100EPT20V is a TTL/CMOS to differential PECL translator. Capable of running from a 3.3 or 5V supply, the part can be used in either LVTTTL/LVCMOS/LVPECL or TTL/CMOS/PECL systems.

The device only requires a single positive supply of 3.3V or 5V - no negative supply is required.

The tiny 8-pin MSOP package and the low skew, dual gate design of the EPT20V makes it ideal for those applications where space, performance, and low power are at a premium.

PIN CONFIGURATION/BLOCK DIAGRAM



(Available in 8-pin SOIC or 8-pin MSOP)

PIN NAMES

Pin	Function
Q, /Q	Differential LVPECL Output
D	LVTTTL Input
V _{CC}	Positive Supply
GND	Ground

ABSOLUTE MAXIMUM RATINGS⁽¹⁾

Symbol	Parameter	Value	Unit
V _{CC}	Power Supply Voltage	-0.5 to +7.0	V
V _I	TTL Input Voltage	-0.5 to V _{CC}	V
I _I	TTL Input Current	-30 to +5.0	mA
I _{OUT}	PECL Output Current		mA
	–Continuous	50	
	–Surge	100	
T _{store}	Storage Temperature	-65 to +150	°C
T _A	Operating Temperature	-40 to +85	°C

NOTE:

- Permanent device damage may occur if ABSOLUTE MAXIMUM RATINGS are exceeded. This is a stress rating only and functional operation is not implied at conditions other than those detailed in the operational sections of this data sheet. Exposure to ABSOLUTE MAXIMUM RATING conditions for extended periods may affect device reliability.

TRUTH TABLE

D	Q	/Q
H	H	L
L	L	H
Open	H	L

DC ELECTRICAL CHARACTERISTICSV_{CC} = +3.3V ±10% or +5.0V ±10%

Symbol	Parameter	T _A = -40°C		T _A = 0°C		T _A = +25°C			T _A = +85°C		Unit	Condition
		Min.	Max.	Min.	Max.	Min.	Typ.	Max.	Min.	Max.		
I _{CC}	Power Supply Current	—	20	—	20	—	—	20	—	20	mA	—

TTL DC ELECTRICAL CHARACTERISTICSV_{CC} = +3.3V ±10% or +5.0V ±10%

Symbol	Parameter	T _A = -40°C		T _A = 0°C		T _A = +25°C			T _A = +85°C		Unit	Condition
		Min.	Max.	Min.	Max.	Min.	Typ.	Max.	Min.	Max.		
V _{IH}	Input HIGH Voltage	2.0	—	2.0	—	2.0	—	—	2.0	—	V	—
V _{IL}	Input LOW Voltage	—	0.8	—	0.8	—	—	0.8	—	0.8	V	—
I _{IH}	Input HIGH Current	—	20	—	20	—	—	20	—	20	μA	V _{IN} = 2.7V V _{IN} = V _{CC}
I _{IL}	Input LOW Current	—	-0.2	—	-0.2	—	—	-0.2	—	-0.2	mA	V _{IN} = 0.5V
V _{IK}	Input Clamp Voltage	—	-1.2	—	-1.2	—	—	-1.2	—	-1.2	V	I _{IN} = -18mA

PECL DC ELECTRICAL CHARACTERISTICSV_{CC} = +3.3V ±10% or +5.0V ±10%

Symbol	Parameter	T _A = -40°C		T _A = 0°C		T _A = +25°C			T _A = +85°C		Unit	Condition
		Min.	Max.	Min.	Max.	Min.	Typ.	Max.	Min.	Max.		
V _{OH}	Output HIGH Voltage ⁽¹⁾										mV	
	10EPT	3920	4110	3980	4160	4020	—	4190	4090	4280		
	100EPT	3915	4120	3975	4120	3975	—	4120	3975	4120		
V _{OL}	Output LOW Voltage ⁽¹⁾										mV	
	10EPT	3050	3350	3050	3370	3050	—	3370	3050	3405		
	100EPT	3170	3445	3190	3380	3190	—	3380	3190	3380		

NOTES:

- These values are for V_{CC} = 5.0V. Level Specifications will vary 1:1 with V_{CC}.

AC ELECTRICAL CHARACTERISTICS(1) $V_{CC} = +3.3V \pm 10\%$ or $+5.0V \pm 10\%$

Symbol	Parameter	$T_A = -40^\circ\text{C}$		$T_A = 0^\circ\text{C}$		$T_A = +25^\circ\text{C}$			$T_A = +85^\circ\text{C}$		Unit	Condition
		Min.	Max.	Min.	Max.	Min.	Typ.	Max.	Min.	Max.		
t_{PLH} t_{PHL}	Propagation Delay ⁽¹⁾	100	600	100	600	100	—	600	100	600	ps	50Ω to $V_{CC} - 2.0V$
t_{skpp}	Part-to-Part Skew ⁽²⁾	—	500	—	500	—	—	500	—	500	ps	50Ω to $V_{CC} - 2.0V$
f_{MAX}	Maximum Input Frequency	350	—	350	—	350	—	—	350	—	MHz	50Ω to $V_{CC} - 2.0V$
f_{MAX}	Maximum Toggle Frequency	—	800	—	800	—	—	800	—	800	MHz	50Ω to $V_{CC} - 2.0V$
t_r t_f	Output Rise/Fall Time (20% to 80%)	200	500	200	500	200	—	500	200	500	ps	50Ω to $V_{CC} - 2.0V$

NOTES:

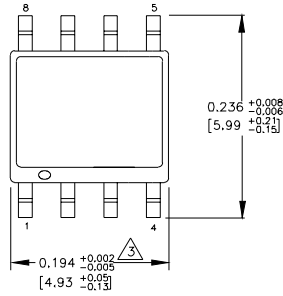
1. Input Rise Time < 1.0ns.
2. Guaranteed by design. Not tested in production.

PRODUCT ORDERING INFORMATION

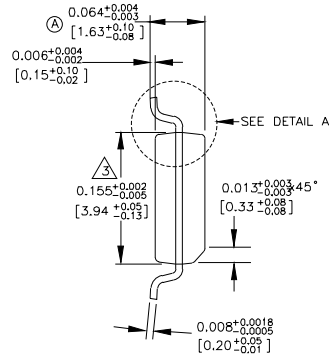
Ordering Code	Package Type	Operating Range	V_{CC} Range (V)
SY10EPT20VZC	Z8-1	Commercial	+3.3V $\pm 5\%$
SY10EPT20VZCTR	Z8-1	Commercial	+3.3V $\pm 5\%$
SY10EPT20VKC	K8-1	Commercial	+3.3V $\pm 5\%$
SY10EPT20VKCTR	K8-1	Commercial	+3.3V $\pm 5\%$
SY100EPT20VZC	Z8-1	Commercial	+3.3V $\pm 5\%$
SY100EPT20VZCTR	Z8-1	Commercial	+3.3V $\pm 5\%$
SY100EPT20VKC	K8-1	Commercial	+3.3V $\pm 5\%$
SY100EPT20VKCTR	K8-1	Commercial	+3.3V $\pm 5\%$

8 LEAD PLASTIC SOIC (Z8-1)

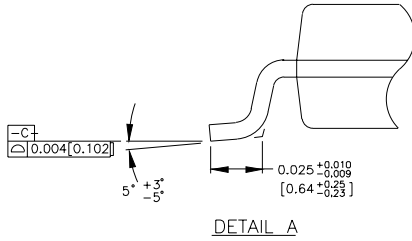
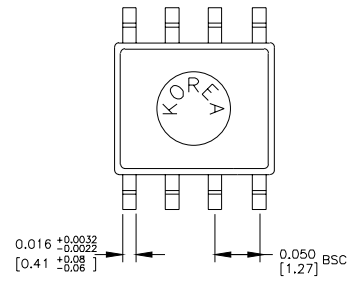
TOP VIEW



END VIEW

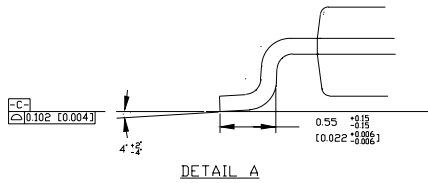
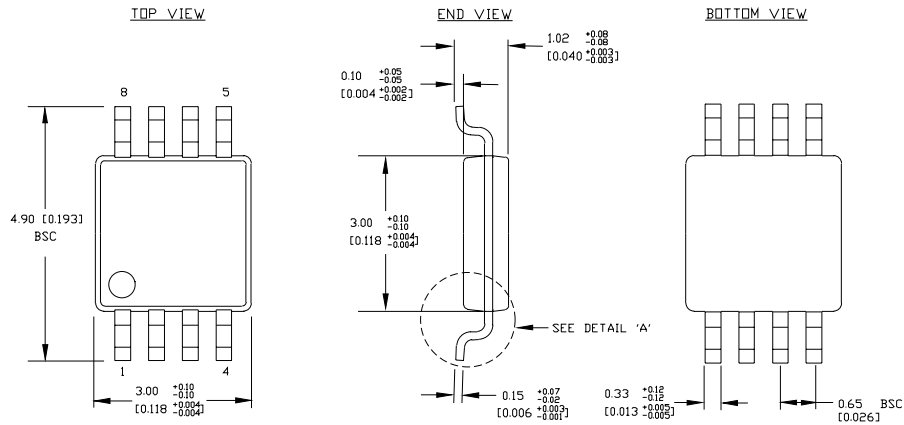


BOTTOM VIEW



- NOTES:
1. DIMENSIONS ARE IN INCHES[MM].
 2. CONTROLLING DIMENSION: INCHES.
 3. DIMENSION DOES NOT INCLUDE MOLD FLASH OR PROTRUSIONS, EITHER OF WHICH SHALL NOT EXCEED 0.006[0.152] PER SIDE.

8 LEAD MSOP (K8-1)



NOTES:
 1. DIMENSIONS ARE IN MM [INCHES].
 2. CONTROLLING DIMENSION: MM
 3. DIMENSION DOES NOT INCLUDE MOLD FLASH OR PROTRUSIONS, EITHER OF WHICH SHALL NOT EXCEED 0.20 [0.008] PER SIDE.

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