查询MAX4714供应商

19-1982; Rev 0; 4/01

Λ Λ Χ Λ 0.8Ω, Low-Voltage, Single-Supply SPDT Analog Switch in SC70

捷多邦,专业PCB打样工厂,24小时加急出货

General Description

The MAX4714 is a low on-resistance, low-voltage single-pole/double-throw (SPDT) analog switch that operates from a single +1.6V to +3.6V supply. The MAX4714 has break-before-make switching. This device also has fast switching speeds (t_{ON} = 18ns max, t_{OFF} = 12ns max).

When powered from a +3V supply, the MAX4714 features 0.8Ω max on-resistance (R_{ON}), with 0.18Ω max R_{ON} matching and flatness. The digital logic input is 1.8V CMOS compatible when using a single +3V supply.

The MAX4714 is pin compatible with the MAX4599 and is available in a 6-pin SC70 package.

Low Ron 0.8Ω max (+3V Supply) 2.5Ω max (+1.8V Supply)

- 0.18Ω max Ron Flatness (+3V Supply)
- ♦ +1.6V to +3.6V Single-Supply Operation
- Available in 6-Pin SC70 Package
- Fast Switching: ton = 18ns max, torr = 12ns max
- 1.8V CMOS Logic Compatible (+3V Supply)
- Pin Compatible with MAX4599
- Guaranteed Break-Before-Make

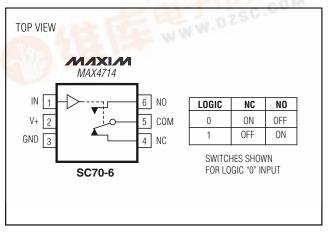
Applications

Power Routing Battery-Operated Equipment Audio and Video Signal Routing Low-Voltage Data-Acquisition Systems Communications Circuits PCMCIA Cards Cellular Phones Modems Hard Drives

PART TEMP. RANGE PACKAGE MAR			3	
	PART	TEMP. RANGE		TOP MARK
IVIAA4/14EAT-1 -40 C (0 +85°C 6 5C/0-6 AA	MAX4714EXT-T	-40°C to +85°C	6 SC70-6	AAY

Ordering Information

Pin Configuration/ Functional Diagram/Truth Table





Maxim Integrated Products 1

For pricing, delivery, and ordering information, please contact Maxim/Dallas Direct! at

Features

0.8 Ω , Low-Voltage, Single-Supply SPDT Analog Switch in SC70

ABSOLUTE MAXIMUM RATINGS

Voltages Referenced to GND

V+, IN	0.3V to +4V
COM, NC, NO (Note 1)	0.3V to (V+ + 0.3V)
Continuous Current NO, NC to COM	±150mÁ
Peak Current NO, NC to COM	
(pulsed at 1ms, 10% duty cycle max)	±300mA

Continuous Power Dissipation	
6-Pin SC70 (derate 3.1mW/°C above +70°C	C)247mW
Operating Temperature Range	
MAX4714EXT	40°C to +85°C
Junction Temperature	+150°C
Storage Temperature Range	
Lead Temperature (soldering, 10s)	+300°C

Note 1: Signals on NC, NO, and COM exceeding V+ or GND are clamped by internal diodes.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

ELECTRICAL CHARACTERISTICS—Single +3V Supply

(V+ = +2.7V to +3.6V, V_{IH} = +1.4V, V_{IL} = +0.5V, T_A = T_{MIN} to T_{MAX}, unless otherwise noted. Typical values are at V+ = +3.0V and T_A = +25°C.) (Notes 2, 3)

PARAMETER	SYMBOL	CONDITIONS	TA	MIN	ТҮР	МАХ	UNITS
ANALOG SWITCH			· · · · ·				
Analog Signal Range	V _{COM} , V _{NO} , V _{NC}			0		V+	V
On-Resistance	Ron	V+ = 2.7V, I _{COM} = 100mA,	+25°C		0.6	0.8	Ω
On-mesistance	NON	$V_{NO} \text{ or } V_{NC} = 1.5 V$	$T_{\mbox{MIN}}$ to $T_{\mbox{MAX}}$			0.9	52
On-Resistance Match	ΔRon	$V + = 2.7V, I_{COM} = 100mA,$	+25°C		0.03	0.06	Ω
Between Channels (Note 4)		$V_{NO} \text{ or } V_{NC} = 1.5 V$	$T_{\mbox{MIN}}$ to $T_{\mbox{MAX}}$			0.08	
On-Resistance Flatness	R _{FLAT} (ON)	$V + = 2.7V, I_{COM} = 100mA,$	+25°C		0.1	0.18	Ω
(Note 5)	TFLAT(ON)	$V_{\rm NO} \text{ or } V_{\rm NC} = 0.6V, 1.5V, 2.1V$	$T_{\mbox{MIN}}$ to $T_{\mbox{MAX}}$			0.2	
NO or NC Off-Leakage	INO(OFF),	$V + = 3.3V, V_{COM} = 0.3V, 3V,$	+25°C	-1		1	nA
Current	INC(OFF)	$V_{NO} \text{ or } V_{NC} = 3V, 0.3V$	$T_{\mbox{MIN}}$ to $T_{\mbox{MAX}}$	-5		5	10.0
COM On-Leakage Current	ICOM(ON)	$\label{eq:V+} \begin{array}{l} V+ = 3.3V, \ V_{COM} = 0.3V, \ 3V, \\ V_{NO} \ \text{or} \ V_{NC} = 0.3V, \ 3V \ \text{or} \ \text{floating} \end{array}$	+25°C	-2		2	nA
Com on Ecalage Canon			$T_{\mbox{MIN}}$ to $T_{\mbox{MAX}}$	-10		10	
DYNAMIC	1						
Turn-On Time	ton	$V_{\rm NO}$ or $V_{\rm NC}$ = 1.5V, R _L = 50 Ω ,	+25°C		13	18	ns
	UN	$C_L = 35 pF$, Figure 1	$T_{\mbox{MIN}}$ to $T_{\mbox{MAX}}$			20	110
Turn-Off Time	toff	$V_{NO} \text{ or } V_{NC} = 1.5 \text{V}, \text{ R}_{L} = 50 \Omega,$	+25°C		6	12	ns
	UFF	$C_L = 35 pF$, Figure 1	$T_{\mbox{MIN}}$ to $T_{\mbox{MAX}}$			15	110
Break-Before-Make Delay	t BBM	$V_{NO} \text{ or } V_{NC} = 1.5 \text{V}, \text{ R}_{L} = 50 \Omega,$	+25°C	1	9		ns
(Note 6)	UDIVI	C _L = 35pF, Figure 2	$T_{\mbox{MIN}}$ to $T_{\mbox{MAX}}$	1			110
Charge Injection	Q	V_{GEN} , R_{GEN} , $C_L = 1.0nF$, Figure 3	+25°C		22		рС
Off-Isolation (Note 7)	V _{ISO}	f = 1MHz, V_{COM} = 1 V_{RMS} , R _L = 50 Ω , C _L = 5pF, Figure 4	+25°C		-54		dB
Crosstalk (Note 8)		f = 1MHz, V _{COM} = 1V _{RMS} , R _L = 50Ω, C _L = 5pF, Figure 4	+25°C		-54		dB
Total Harmonic Distortion	THD	f = 20Hz to 20kHz, V_{COM} = 2V_{P-P}, R_L = 32\Omega	+25°C		0.01		%

0.8Ω, Low-Voltage, Single-Supply SPDT Analog Switch in SC70

ELECTRICAL CHARACTERISTICS—Single +3V Supply (continued)

 $(V + = +2.7V \text{ to } +3.6V, V_{IH} = +1.4V, V_{IL} = +0.5V, T_A = T_{MIN} \text{ to } T_{MAX}$, unless otherwise noted. Typical values are at V + = +3.0V and $T_A = +25^{\circ}C$.) (Notes 2, 3)

PARAMETER	SYMBOL	CONDITIONS	TA	MIN	ТҮР	MAX	UNITS
NC or NO Off-Capacitance	C _{NO(OFF),} C _{NC(OFF)}	f = 1MHz, Figure 5	+25°C		30		рF
COM On-Capacitance	C _{COM(ON)}	f = 1MHz, Figure 5	+25°C		65		рF
LOGIC INPUT							
Input Voltage Low	VIL					0.5	V
Input Voltage High	VIH			1.4			V
Input Leakage Current	lin	$V_{IN} = 0 \text{ or } V+$		-1		1	μΑ
SUPPLY							
Power-Supply Range	V+			1.6		3.6	V
Positive Supply Current I+		$V_{+} = +3.6V, V_{1N} = 0 \text{ or } V_{+}$	+25°C		0.04	0.2	
	1+		$T_{\mbox{MIN}}$ to $T_{\mbox{MAX}}$			2	μA

ELECTRICAL CHARACTERISTICS—Single +1.8V Supply

(V+ = +1.8V, V_{IH} = +1V, V_{IL} = +0.4V, T_A = T_{MIN} to T_{MAX}, unless otherwise noted. Typical values are at T_A = +25°C.) (Notes 2, 3)

PARAMETER	SYMBOL	CONDITIONS	TA	MIN	ТҮР	MAX	UNITS	
ANALOG SWITCH	•						•	
Analog Signal Range	V _{COM} , V _{NO} , V _{NC}			0		V+	V	
On-Resistance	R _{ON}	I _{COM} = 10mA,	+25°C		1.2	2.5	Ω	
On-nesistance	NON	$V_{NO} \text{ or } V_{NC} = +0.9V$	$T_{\mbox{MIN}}$ to $T_{\mbox{MAX}}$			5	52	
NO or NC Off-Leakage	INO(OFF),	V _{COM} = 0.3V, 1.5V,	+25°C	-1	0.01	1	n۸	
Current	INC(OFF)	$V_{NO} \text{ or } V_{NC} = 1.5V, 0.3V$	$T_{\mbox{MIN}}$ to $T_{\mbox{MAX}}$	-5		5	nA	
		$\begin{array}{c} V_{COM} = 0.3V, \ 1.5V, \ V_{NO} \ or \\ V_{NC} = 0.3V, \ 1.5V \ or \ floating \end{array}$	+25°C	-2		2	nA	
COM On-Leakage Current	ICOM(ON)		T _{MIN} to T _{MAX}	-10		10		
DYNAMIC								
		V_{NO} or V_{NC} = 1.5V, R_L = 50 Ω , C_L = 35pF, Figure 1	+25°C		18	25	ns	
Turn-On Time	ton		$T_{\mbox{MIN}}$ to $T_{\mbox{MAX}}$			30		
Turn Off Times	$V_{NO} \text{ or } V_{NC} = 1.5 \text{V}, \text{ R}_{\text{L}} = 50 \Omega,$	+25°C		9	15			
Turn-Off Time	toff	$C_L = 35 pF$, Figure 1	$T_{\mbox{MIN}}$ to $T_{\mbox{MAX}}$			18	ns	
Break-Before-Make Delay	t _{BBM}	ak-Before-Make Delay V_{NO} or $V_{NC} = 1$.	V_{NO} or $V_{NC} = 1.5V$, $R_L = 50\Omega$,	+25°C	2			
(Note 6)		$C_L = 35 pF$, Figure 2	$T_{\mbox{MIN}}$ to $T_{\mbox{MAX}}$	2			ns	
Charge Injection	Q	$V_{GEN} = 0$, $R_{GEN} = 0$, $C_L = 1$ nF, Figure 3	+25°C		12		рС	

0.8 Ω , Low-Voltage, Single-Supply SPDT Analog Switch in SC70

ELECTRICAL CHARACTERISTICS—Single +1.8V Supply (continued)

 $(V + = +1.8V, V_{IH} = +1V, V_{IL} = +0.4V, T_A = T_{MIN}$ to T_{MAX} , unless otherwise noted. Typical values are at $T_A = +25^{\circ}C$.) (Notes 2, 3)

PARAMETER	SYMBOL	CONDITIONS	TA	MIN	TYP	MAX	UNITS
LOGIC INPUT							
Input Voltage Low	VIL					0.4	V
Input Voltage High	VIH			1			V
Input Leakage Current	I _{IN}	$V_{IN} = 0 \text{ or } V+$				1	μA
SUPPLY							
Desitive Supply Current	1.		+25°C		0.04	0.2	۸
Positive Supply Current	e Supply Current I+ V _{IN} = 0 or V+	$T_{\mbox{MIN}}$ to $T_{\mbox{MAX}}$			2	μA	

Note 2: The algebraic convention, where the most negative value is a minimum and the most positive value is a maximum, is used in this data sheet.

Note 3: SC70-packaged parts are 100% tested at +25°C. Limits across the full temperature range are guaranteed by design and correlation.

Note 4: $\Delta R_{ON} = R_{ON}(MAX) - R_{ON}(MIN)$.

Note 5: Flatness is defined as the difference between the maximum and minimum values of on-resistance as measured over the specified analog signal range.

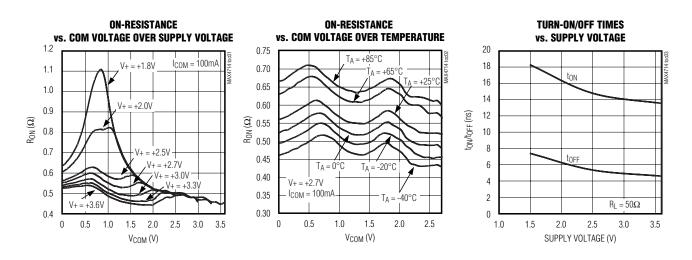
Note 6: Guaranteed by design.

Note 7: Off-Isolation = 20log₁₀ [V_{COM} / (V_{NC} or V_{NO})], V_{COM} = output, V_{NC} or V_{NO} = input to off switch.

Note 8: Between the two switches.

Typical Operating Characteristics

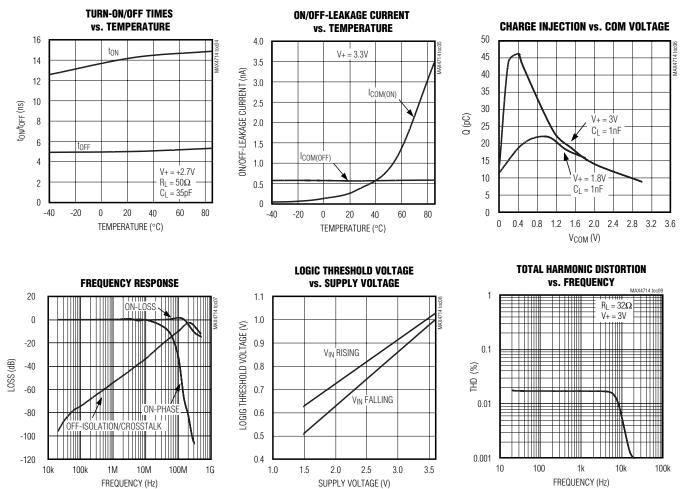
 $(T_A = +25^{\circ}C, unless otherwise noted.)$



0.8Ω, Low-Voltage, Single-Supply SPDT Analog Switch in SC70

Typical Operating Characteristics (continued)

 $(T_A = +25^{\circ}C, unless otherwise noted.)$



Pin Description

PIN	NAME	FUNCTION
1	IN	Digital Control Input
2	V+	Positive Supply Voltage Input
3	GND	Ground
4	NC	Analog Switch—Normally Closed
5	COM	Analog Switch—Common
6	NO	Analog Switch—Normally Open

MAX4714

M/IXI/M

0.8Ω, Low-Voltage, Single-Supply SPDT Analog Switch in SC70

MAX4714

Detailed Description

The MAX4714 is a low-on-resistance (R_{ON}), low-voltage, single-pole/double-throw (SPDT) analog switch that operates from a +1.6V to +3.6V supply. The MAX4714 has break-before-make switching. This device also has fast switching speeds (t_{ON} = 18ns max, t_{OFF} = 12ns max).

When powered from a +3V supply, the 0.8 Ω max R_{ON} allows high continuous currents to be switched in a variety of applications.

Applications Information

Logic Inputs

The MAX4714 logic input can be driven up to +3.6V regardless of the supply voltage. For example, with a

Rail-to-Rail is a registered trademark of Nippon Motorola, Ltd.

+3.3V supply, IN may be driven low to GND and high to +3.6V. Driving IN Rail-to-Rail[®] minimizes power consumption.

Analog Signal Levels

Analog signals that range over the entire supply voltage (V+ to GND) can be passed with very little change in on-resistance (see *Typical Operating Characteristics*). The switches are bidirectional, so the NO, NC, and COM pins can be used as either inputs or outputs.

Chip Information

/N/IXI/N

TRANSISTOR COUNT: 135 PROCESS: CMOS

_Test Circuits/Timing Diagrams

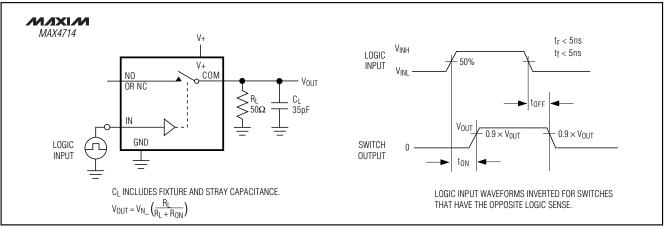


Figure 1. Switching Time

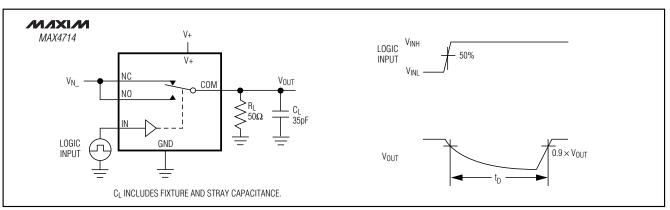


Figure 2. Break-Before-Make Interval

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0.8Ω, Low-Voltage, Single-Supply SPDT Analog Switch in SC70



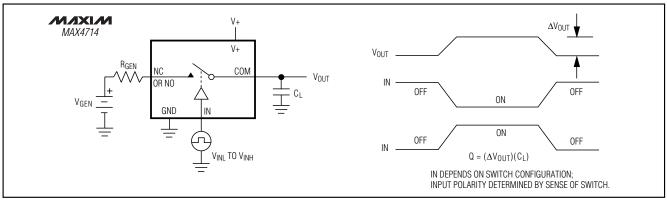


Figure 3. Charge Injection

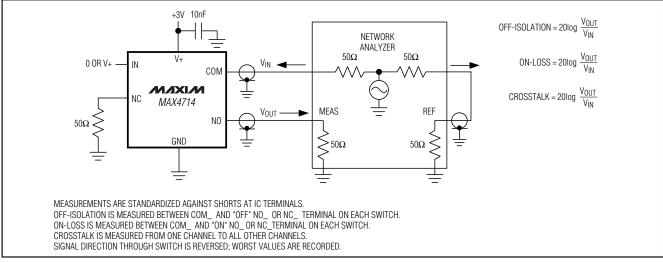


Figure 4. On-Loss, Off-Isolation, and Crosstalk

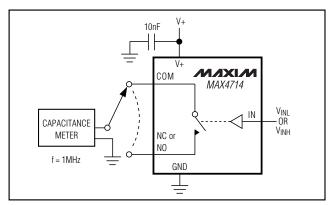
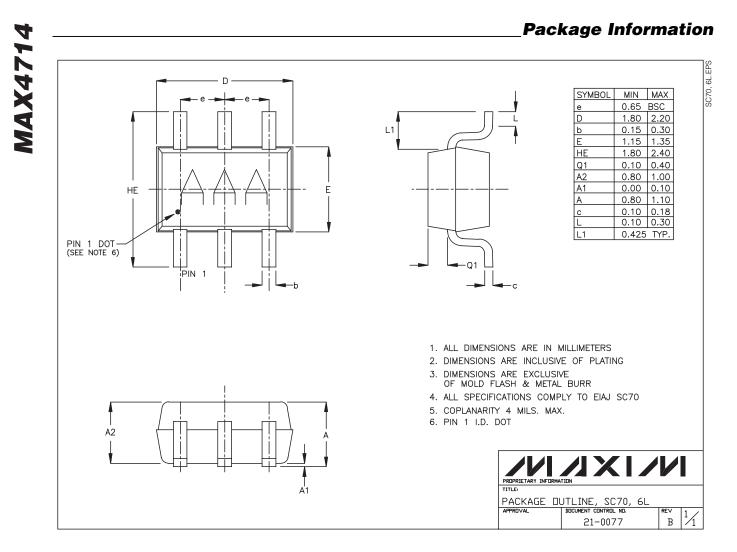


Figure 5. Channel Off/On-Capacitance



MAX4714

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