

NPN SILICON EPITAXIAL TWIN TRANSISTOR

UPA839TF

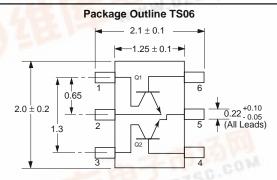
FEATURES

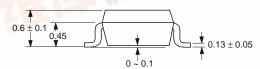
- SMALL PACKAGE OUTLINE: SOT-363 package measures just 2.0 mm x 1.25 mm
- LOW HEIGHT PROFILE: Just 0.60 mm high
- TWO DIFFERENT DIE TYPES:
 - Q1 Ideal oscillator transistor
 - Q2 Ideal buffer amplifier transistor

DESCRIPTION

The UPA839TF contains one NE680 and one NE856 NPN high frequency silicon bipolar chip. NEC's new low profile TF package is ideal for all portable wireless applications where reducing component height is a prime consideration. Each transistor chip is independently mounted and easily configured for oscillator/buffer amplifier and other applications.

OUTLINE DIMENSIONS (Units in mm)





PIN CONNECTIONS

- Collector (Q1)
 Emitter (Q1)
- 4. Base (Q2)
- 3. Collector (Q2)
- 5. Emitter (Q2) 6. Base (Q1)

Note: Pin 1 is the lower left most pin as the package lettering is oriented and read left to right.

ELECTRICAL CHARACTERISTICS (TA = 25°C)

PART NUMBER PACKAGE OUTLINE			44	UPA839TF TS06			
	SYMBOLS	PARAMETERS AND CONDITIONS	UNITS	MIN	TYP	MAX	
۵1	Ісво	Collector Cutoff Current at VcB = 10 V, IE = 0	μА			1.0	
	ІЕВО	Emitter Cutoff Current at VEB = 1 V, IC = 0	μА			1.0	
	hFE	DC Current Gain ¹ at VcE = 3 V, Ic = 5 mA		80	120	200	
	fT	Gain Bandwidth at VcE = 3 V, Ic = 5 mA	GHz	5.5	8.0		
	Cre	Feedback Capacitance ² at VcB = 3 V, IE = 0, f = 1 MHz	pF		0.3	0.7	
	S21E ²	Insertion Power Gain at VcE = 3 V, Ic =5 mA, f = 2 GHz	dB	5.5	7.5	(U)	
	NF	Noise Figure at VcE = 3 V, Ic = 5 mA, f = 2 GHz	dB	100	1.9	3.2	
	Ісво	Collector Cutoff Current at VcB = 10 V, IE = 0	μА		W.OZS	1.0	
	ІЕВО	Emitter Cutoff Current at VEB = 1 V, Ic = 0	μΑ	A A	491	1.0	
Q2	hFE	DC Current Gain ¹ at VcE = 3 V, Ic = 7 mA		100		145	
	fτ	Gain Bandwidth at VcE = 3 V, Ic = 7 mA, f = 1 GHz	GHz	3.0	4.5		
	Cre	Feedback Capacitance ² at VcB = 3 V, IE = 0, f = 1 MHz	pF		0.7	1.5	
	S21E ²	Insertion Power Gain at VcE = 3 V, Ic = 7 mA, f = 1 GHz	dB	7	9		
	NF	Noise Figure at VcE = 3 V, Ic = 7 mA, f = 1 GHz	dB		1.2	2.5	

Notes: 1. Pulsed measurement, pulse width \leq 350 μ s, duty cycle \leq 2 %.

Collector to base capacitance when measured with capacitance meter (automatic balanced bridge method), with emitter connected to guard pin of capacitances meter.



ABSOLUTE MAXIMUM RATINGS¹ (TA = 25°C)

SYMBOLS	PARAMETERS	UNITS	JNITS RATINGS	
			Q1	Q2
Vсво	Collector to Base Voltage	V	20	20
VCEO	Collector to Emitter Voltage	V	10	12
VEBO	Emitter to Base Voltage	V	1.5	3
Ic	Collector Current	mA	35	100
Рт	Total Power Dissipation	mW	110	110
			20	0
TJ	Junction Temperature	°C	150	150
Тѕтс	Storage Temperature	°C	-65 to	+150

Note: 1. Operation in excess of any one of these parameters may result in permanent damage.

ORDERING INFORMATION

PART NUMBER	QUANTITY	PACKAGING
UPA839TF-T1	3000	Tape & Reel