

## **Preliminary**

**RF2320** 

LINEAR GENERAL PURPOSE AMPLIFIER

### Typical Applications

- CATV Distribution Amplifiers
- Cable Modems
- Broadband Gain Blocks

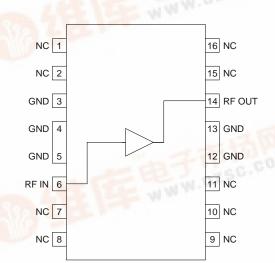
- Laser Diode Driver
- Return Channel Amplifier
- Base Stations

### **Product Description**

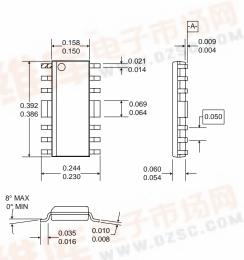
The RF2320 is a general purpose, low-cost, high-linearity RF amplifier IC. The device is manufactured on a Gallium Arsenide process and is featured in an SOP-16 batwing package. It has been designed for use as an easily cascadable  $75\Omega$  gain block with a noise figure of less than 2dB. Gain flatness better than 0.5dB from 5MHz to  $1000\,\mathrm{MHz}$ , and high linearity make this part ideal for cable TV applications. Other applications include IF and RF amplification in wireless voice and data communication products operating in frequency bands up to  $2500\,\mathrm{MHz}$ . The device is self-contained with  $75\Omega$  input and output impedances providing  $2:1\,\mathrm{VSWR}$  matching. For higher input and output return losses, see the evaluation schematic.

Optimum Technology Matching® Applied

- Si BJT Si Bi-CMOS
- ☐ GaAs HBT ☐ SiGe HBT
- GaAs MESFET
  ☐ Si CMOS



Functional Block Diagram



Package Style: Standard Batwing

#### **Features**

- 5MHz to 2500MHz Operation
- Internally Matched Input and Output
- 16dB Small Signal Gain
- 1.6dB Noise Figure
- +22dBm Output Power
- Single 6V to 9V Positive Power Supply

#### Ordering Information

RF2320 Linear General Purpose Amplifier RF2320 PCBA Fully Assembled Evaluation Board

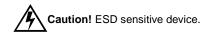
RF Micro Devices, Inc. 7625 Thorndike Road Greensboro, NC 27409, USA Tel (336) 664 1233 Fax (336) 664 0454 http://www.rfmd.com



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### **Absolute Maximum Ratings**

Parameter	Rating	Unit
Device Current	175	mA
Device Voltage	9	V
Input RF Power	+10	dBm
Output Load VSWR	20:1	
Ambient Operating Temperature	-40 to +85	°C
Storage Temperature	-40 to +150	°C



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Parameter	Specification		Unit	Condition		
Farameter	Min.	Тур.	Max.	Unit	Condition	
Overall (50Ω)					T=25 °C, V <sub>DD</sub> =7V, 50Ω System, P <sub>IN</sub> =-	
					10dBm	
Frequency Range		5 to 2500		MHz	3dB Bandwidth	
Input VSWR		2:1			Appropriate values for the DC blocking capacitor and bias inductor are required to maintain this VSWR at the intended operating frequency range.	
Output VSWR		1.3:1			See note for Input VSWR.	
Gain		15		dB	At 100MHz	
		16		dB	At 2000MHz	
Gain Flatness		+/-1		dB	From 5MHz to 2500MHz	
Noise Figure		1.8		dB	From 5MHz to 900MHz	
		2.6		dB	From 900MHz to 2500MHz,	
Output IP <sub>3</sub>		35		dBm	At 100MHz	
		36		dBm	At 1000MHz	
Output IP <sub>2</sub>		39.6		dBm	At 1000MHz	
Output P <sub>1dB</sub>		21.5		dBm	At 100MHz	
		22.5		dBm	At 1000MHz	
		18		dBm	At 2000MHz	
Reverse Isolation		20		dB		
					T=25 °C, $V_{DD}$ =9V, 50Ω System, $P_{IN}$ =-10dBm	
Gain		16.5		dB	At 100MHz	
		16.7		dB	At 1000MHz	
Noise Figure		1.8		dB	From 5MHz to 900MHz,	
		2.6		dB	From 900MHz to 2500MHz,	
Output IP <sub>3</sub>		36		dBm	At 100MHz	
		36.3		dBm	At 1000MHz	
Output IP <sub>2</sub>		39.8		dBm	At 1000MHz	
Output P <sub>1dB</sub>		23		dBm	At 100MHz	
1		24.7		dBm	At 1000MHz	
		19.5		dBm	At 2000MHz	
Power Supply						
Supply Voltage (V <sub>DD</sub> )	6	7	9	V		
Operating Current Range	75	85	100	mA		

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# Preliminary

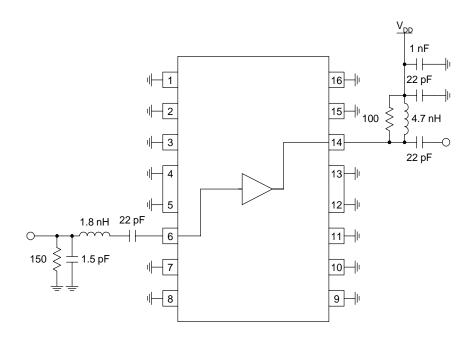
# RF2320

Parameter	Specification		Unit	Condition		
Parameter	Min.	Тур.	Max.	Unit	Condition	
Overall (75Ω)					T=25°C, V <sub>DD</sub> =7V, 75Ω System, P <sub>IN</sub> =-8dBm	
Frequency Range		5 to 2500		MHz	3dB Bandwidth	
Input VSWR		1.6:1			Appropriate values for the output DC blocking capacitor and bias inductor are required to maintain this VSWR over the intended operating frequency range.	
Output VSWR		1.3:1			See note for input VSWR.	
Gain		15		dB	At 500MHz	
Gain Flatness		+1-0.5		dB	5MHz to 1000MHz	
Output IP <sub>3</sub>		34.6		dBm	At 10MHz, Delta F1 and F2 = 1MHz	
		36.1		dBm	At 500MHz	
		33.1		dBm	At 1000MHz	
Output IP <sub>2</sub>		49.9		dBm	At 100MHz, Delta F1 and F2 = 156MHz	
		48.5		dBm	At 1000MHz	
Output IP <sub>1dB</sub>		21		dBm	At 10MHz	
•		23		dBm	At 500MHz	
		22		dBm	At 1000MHz	
110 Channels					10dBmV per channel, flat, at the input of the amplifier; V <sub>CC</sub> =7V, I <sub>CC</sub> =75mA	
XMOD		-71		dBc	At 55.25MHz	
		-72		dBc	At 331.25MHz	
		-72		dBc	At 547.25MHz	
СТВ		-79		dBc	At 55.25MHz	
		-78		dBc	At 331.25MHz	
		-78		dBc	At 547.25MHz	
CSO+1.25MHz		-91		dBc	At 55.25MHz	
		-58		dBc	At 331.25MHz	
		-56		dBc	At 547.25MHz	
CSO-1.25MHz		-51		dBc	At 55.25MHz	
		-53		dBc	At 331.25MHz	
		-58		dBc	At 547.25MHz	
CNR		+65		dB	At 55.25MHz	
		+64		dB	At 331.25MHz	
		+64		dB	At 547.25MHz	
Power Supply						
Supply Voltage (V <sub>DD</sub> )	6	7	9	V		
Operating Current Range	75	85	100	mA		

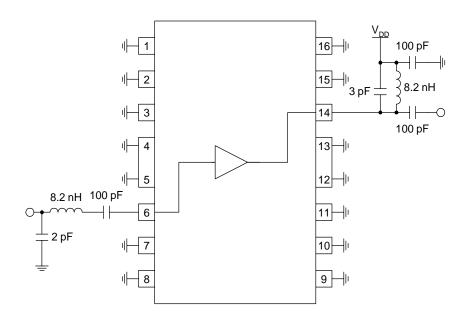
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Pin	Function	Description	Interface Schematic
1	NC	No connection. This pin should be connected to the ground plane.	
2	NC	Same as pin 1.	
3	GND	Ground connection. Keep traces physically short and connect immediately to ground plane for best performance. Each ground pin should have a via to the ground plane.	
4	GND	Same as pin 3.	
5	GND	Same as pin 3.	
6	RF IN	RF input pin. This pin is not internally DC blocked. A DC blocking capacitor suitable for the frequency of operation is required if DC is present from the previous stage. The gate voltage is nominally 0V but can be raised externally to increase the bias level. This will increase the current drain but improve linearity.	
7	NC	Same as pin 1.	
8	NC	Same as pin 1.	
9	NC	Same as pin 1.	
10	NC	Same as pin 1.	
11	NC	Same as pin 1.	
12	GND	Same as pin 3.	
13	GND	Same as pin 3.	
14	RF OUT	RF output and bias pin. Because DC is present on this pin, a DC blocking capacitor, suitable for the frequency of operation, should be used in most applications. For biasing, only an RF choke is needed.	RF IN O
15	NC	Same as pin 1.	
16	NC	Same as pin 1.	

## Application Schematic 1930-1990MHz Narrowband Operation



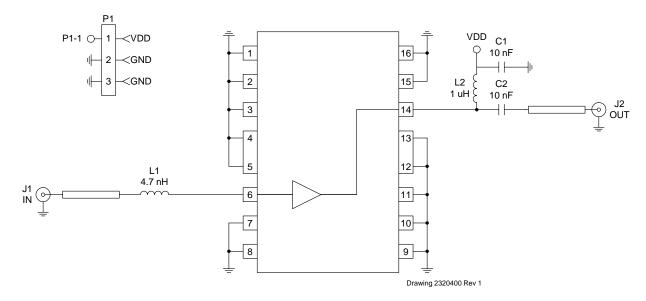
## Application Schematic 869-894MHz Narrowband Operation



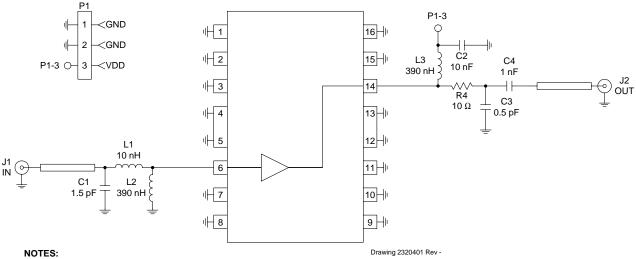
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### Evaluation Board Schematic - $50\Omega$

(Download Bill of Materials from www.rfmd.com.)



## Evaluation Board Schematic - $75\Omega$

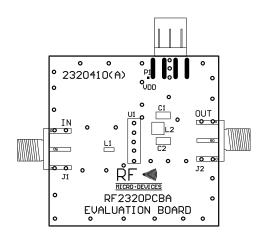


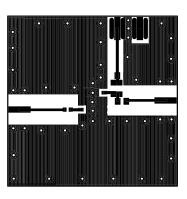
**NOTES:** J1 and J2 are 75  $\Omega$ F connectors.

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## Evaluation Board Layout - $50\Omega$ Board Size 1.5" x 1.5"

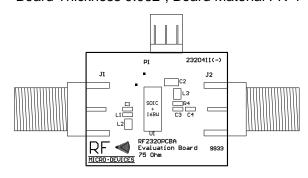
Board Thickness 0.031", Board Material FR-4

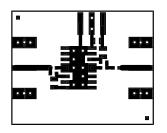




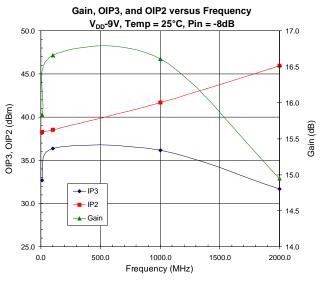
## Evaluation Board Layout - $75\Omega$

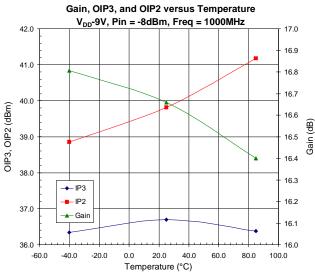
Board Thickness 0.062", Board Material FR-4

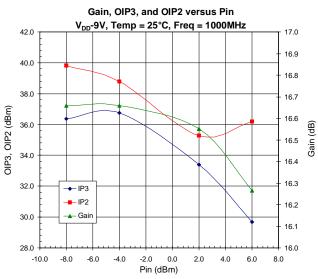


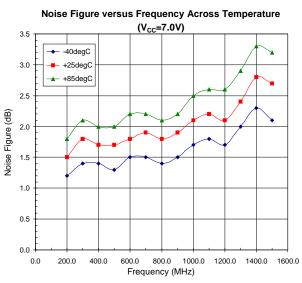


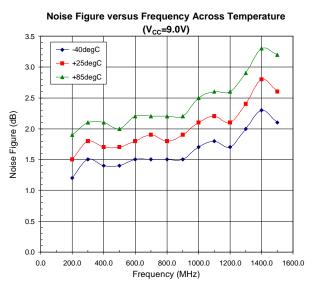
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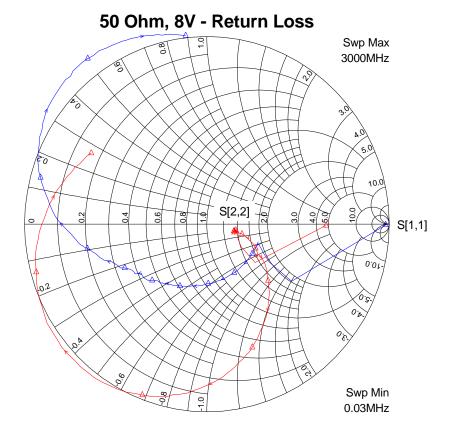












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