## MOTORの上入9供应商 SEMICONDUCTOR TECHNICAL DATA

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by MRF899/D

# The RF Line NPN Silicon RF Power Transistor

Designed for 26 Volt UHF large–signal, common emitter, Class AB linear amplifier applications in industrial and commercial FM/AM equipment operating in the range 800–960 MHz.

- Specified 26 Volt, 900 MHz Characteristics Output Power = 150 Watts (PEP) Minimum Gain = 8.0 dB @ 900 MHz, Class AB Minimum Efficiency = 35% @ 900 MHz, 150 Watts (PEP) Maximum Intermodulation Distortion –28 dBc @ 150 Watts (PEP)
- Characterized with Series Equivalent Large–Signal Parameters from 800
  to 960 MHz
- Silicon Nitride Passivated
- 100% Tested for Load Mismatch Stress at all Phase Angles with 5:1 VSWR
   @ 26 Vdc, and Rated Output Power
- Gold Metallized, Emitter Ballasted for Long Life and Resistance to Metal Migration
- Circuit board photomaster available upon request by contacting RF Tactical Marketing in Phoenix, AZ.



150 W, 900 MHz RF POWER TRANSISTOR NPN SILICON



### **MAXIMUM RATINGS**

Rating	Symbo	Value	Unit
Collector–Emitter Voltage	VCEC	28	Vdc
Collector–Emitter Voltage	VCES	60	Vdc
Emitter-Base Voltage	VEBC	4.0	Vdc
Collector-Current — Continuous	IC	25	Adc
Total Device Dissipation @ T <sub>C</sub> = 25°C	PD	230 1.33	Watts W/°C
Storage Temperature Range	T <sub>stg</sub>	-65 to +150	°C

THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction to Case		0.75	°C/W

ELECTRICAL CHARACTERISTICS (T<sub>C</sub> = 25°C unless otherwise noted.)

Characteristic	Symbol	Min	Тур	Max	Unit
OFF CHARACTERISTICS	188 4 4			•	•
Collector–Emitter Breakdown Voltage (I <sub>C</sub> = 100 mAdc, I <sub>B</sub> = 0)	V <sub>(BR)</sub> CEO	28	37	-	Vdc
Collector–Emitter Breakdown Voltage (I <sub>C</sub> = 50 mAdc, V <sub>BE</sub> = 0)	V(BR)CES	60	85	-	Vdc
Emitter-Base Breakdown Voltage (I <sub>E</sub> = 10 mAdc, I <sub>C</sub> = 0)	V <sub>(BR)EBO</sub>	4.0	4.9	-	Vdc
Collector Cutoff Current (V <sub>CE</sub> = 30 Vdc, V <sub>BE</sub> = 0)	ICES	_	—	10	mAdo
ON CHARACTERISTICS				·	
DC Current Gain (I <sub>CE</sub> = 1.0 Adc, V <sub>CE</sub> = 5.0 Vdc)	hFE	30	75	120	_
DYNAMIC CHARACTERISTICS	· ·		<u>.</u>	•	-
Output Capacitance ( $V_{CB}$ = 26 Vdc, I <sub>E</sub> = 0, f = 1.0 MHz) (1)	C <sub>ob</sub>	—	75	—	pF
Propinformation only. This part is collector matched					(contin

For information only. This part is collector matched.

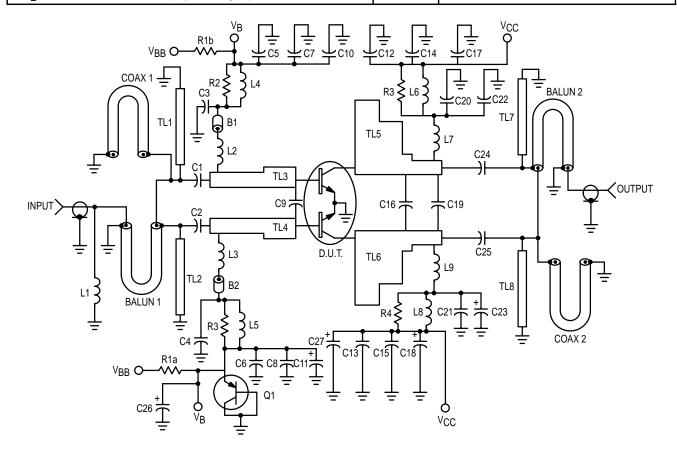
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(continued)



## **ELECTRICAL CHARACTERISTICS** — continued ( $T_C = 25^{\circ}C$ unless otherwise noted.)

Characteristic	Symbol	Min	Тур	Max	Unit
FUNCTIONAL CHARACTERISTICS					
Common–Emitter Amplifier Power Gain $V_{CC}$ = 26 Vdc, P <sub>out</sub> = 150 Watts (PEP), I <sub>CQ</sub> = 300 mA, f <sub>1</sub> = 900 MHz, f <sub>2</sub> = 900.1 MHz	G <sub>pe</sub>	8.0	9.0	_	dB
Collector Efficiency $V_{CC} = 26$ Vdc, $P_{out} = 150$ Watts (PEP), $I_{Cq} = 300$ mA, $f_1 = 900$ MHz, $f_2 = 900.1$ MHz	η	30	40	_	%
3rd Order Intermodulation Distortion V <sub>CC</sub> = 26 Vdc, P <sub>out</sub> = 150 Watts (PEP), I <sub>Cq</sub> = 300 mA, f <sub>1</sub> = 900 MHz, $f_2$ = 900.1 MHz		_	-32	-28	dBc
Output Mismatch Stress $V_{CC} = 26$ Vdc, $P_{Out} = 150$ Watts (PEP), $I_{Cq} = 300$ mA, $f_1 = 900$ MHz, $f_2 = 900.1$ MHz, VSWR = 5:1 (all phase angles)	Ψ	No Degradation in Output Power Before and After Test			



- B1, B2 Ferrite Bead, Ferroxcube #56–590–65–3B C1, C2, C24, C25 — 43 pF, B Case, ATC Chip Capacitor C3, C4, C20, C21 — 100 pF, B Case, ATC Chip Capacitor C5, C6, C12, C13 — 1000 pF, B Case, ATC Chip Capacitor C7, C8, C14, C15 — 1800 pF, AVX Chip Capacitor C9 — 9.1 pF, A Case, ATC Chip Capacitor C10, C11, C17, C18, C22, C23 — 10  $\mu$ F, Electrolytic Capacitor Panasonic C16 — 3.9 pF, B Case, ATC Chip Capacitor
- C19 0.8 pF, B Case, ATC Chip Capacitor
- $\text{C26}-\text{200}\,\mu\text{F},$  Electrolytic Capacitor Mallory Sprague
- C27 500  $\mu\text{F}$  Electrolytic Capacitor

- L1 5 Turns 24 AWG IDIA 0.059" Choke, 19.8 nH L2, L3, L7, L9 — 4 Turns 20 AWG IDIA 0.163" Choke L4, L5, L6, L8 — 12 Turns 22 AWG IDIA 0.140" Choke N1, N2 — Type N Flange Mount, Omni Spectra
- Q1 Bias Transistor BD136 PNP

R2, R3, R4, R5 - 4.0 x 39 Ohm 1/8 W Chips in Parallel

- R1a, R1b 56 Ohm 1.0 W
- TL1-TL8 See Photomaster

Balun1, Balun2, Coax 1, Coax 2 - 2.20" 50 Ohm 0.088" o.d.

Semi-rigid Coax, Micro Coax Board — 1/32" Glass Teflon,  $\varepsilon_r = 2.55$ " Arlon (GX-0300–55–22)

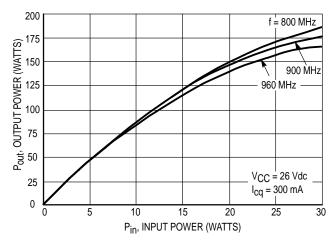


Figure 2. Output Power versus Input Power

Pin = 24 W

16 W

8 W

30

f = 900 MHz

I<sub>CQ</sub> = 300 mA

28

200

175

25

0 L

16

18

20

22

V<sub>CC</sub>, COLLECTOR VOLTAGE (VOLTS)

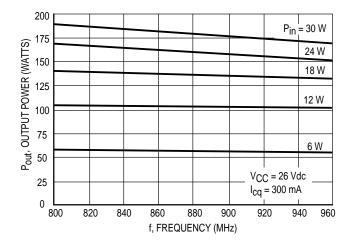


Figure 3. Output Power versus Frequency

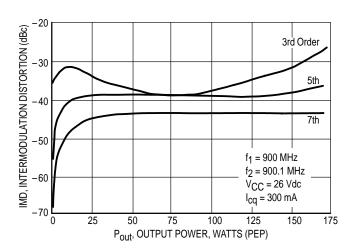


Figure 4. Output Power versus Supply Voltage

24

26

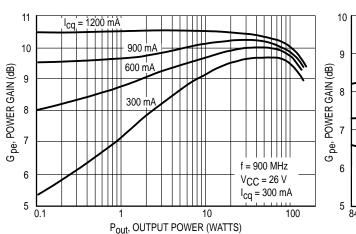


Figure 6. Power Gain versus Output Power

Figure 5. Intermodulation versus Output Power

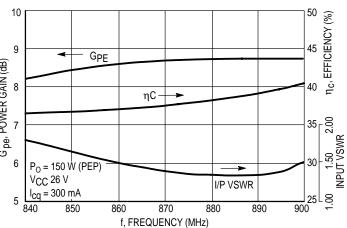
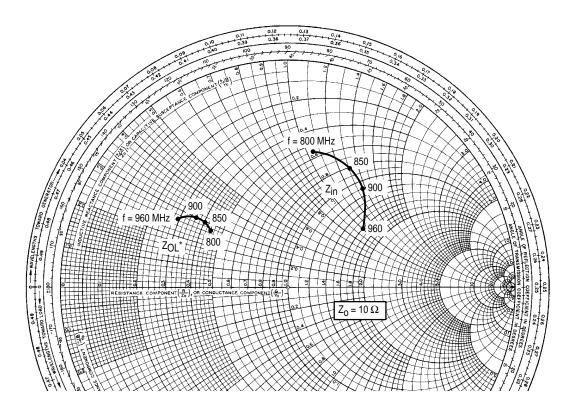


Figure 7. Broadband Test Fixture Performance



f MHz	Z <sub>in</sub> Ohms	Z <sub>OL</sub> * Ohms
800	5.51 + j10.6	4.52 + j2.64
850	8.17 + j13.2	4.21 + j2.98
900	11.2 + j13.8	3.68 + j2.97
960	16.8 + j10.1	2.98 + j2.71

NOTE: Z<sub>in</sub> & Z<sub>OL</sub>\* are given from base-to-base and collector-to-collector respectively

Z<sub>OL</sub>\* = Conjugate of optimum load impedance into which the device operates at a given output power, voltage and frequency.

Figure 8. Input and Output Impedances with Circuit Tuned for Maximum Gain @  $P_O$  = 150 W (PEP), V<sub>CC</sub> = 26 V

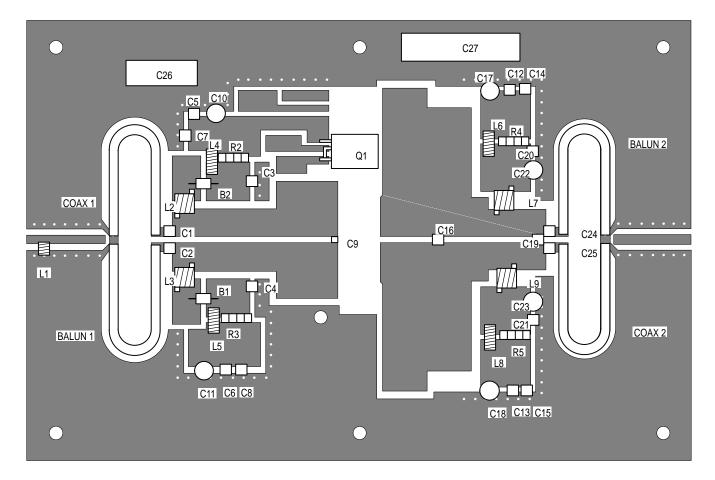
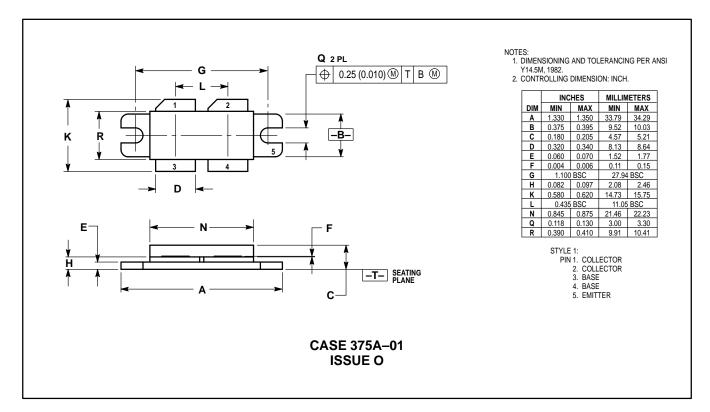


Figure 9. MRF899 Test Fixture Component Layout

#### PACKAGE DIMENSIONS



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