

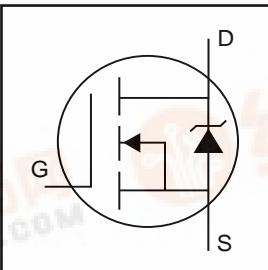
International  
**IR** Rectifier

## **PRELIMINARY**

# IRFBF20S/L

## HEXFET® Power MOSFET

- Surface Mount (IRFBF20S)
  - Low-profile through-hole (IRFBF20L)
  - Available in Tape & Reel (IRFBF20S)
  - Dynamic dv/dt Rating
  - 150°C Operating Temperature
  - Fast Switching
  - Fully Avalanche Rated



$$V_{DSS} = 900V$$

$$R_{DS(on)} = 8.0\Omega$$

$$I_D = 1.7A$$

## Description

Third generation HEXFETs from international Rectifier provide the designer with the best combination of fast switching, ruggedized device design, low on-resistance and cost-effectiveness.

The D<sup>2</sup>Pak is a surface mount power package capable of accommodating die sizes up to HEX-4. It provides the highest power capability and the lowest possible on-resistance in any existing surface mount package. The D<sup>2</sup>Pak is suitable for high current applications because of its low internal connection resistance and can dissipate up to 2.0W in a typical surface mount application. The through-hole version (IRFBF20L) is available for low-profile applications.



## Absolute Maximum Ratings

|                           | Parameter                                  | Max.                   | Units |
|---------------------------|--|------------------------|-------|
| $I_D @ T_C = 25^\circ C$  | Continuous Drain Current, $V_{GS} @ 10V$ ⑤ | 1.7                    | A     |
| $I_D @ T_C = 100^\circ C$ | Continuous Drain Current, $V_{GS} @ 10V$ ⑤ | 1.1                    |       |
| $I_{DM}$                  | Pulsed Drain Current ①⑤                    | 6.8                    |       |
| $P_D @ T_A = 25^\circ C$  | Power Dissipation                          | 3.1                    | W     |
| $P_D @ T_C = 25^\circ C$  | Power Dissipation                          | 54                     | W     |
|                           | Linear Derating Factor                     | 0.43                   | W/°C  |
| $V_{GS}$                  | Gate-to-Source Voltage                     | $\pm 20$               | V     |
| $E_{AS}$                  | Single Pulse Avalanche Energy②⑤            | 180                    | mJ    |
| $I_{AR}$                  | Avalanche Current①                         | 1.7                    | A     |
| $E_{AR}$                  | Repetitive Avalanche Energy①               | 5.4                    | mJ    |
| $dv/dt$                   | Peak Diode Recovery $dv/dt$ ③⑤             | 1.5                    | V/ns  |
| $T_J$                     | Operating Junction and                     | -55 to + 150           | °C    |
| $T_{STG}$                 | Storage Temperature Range                  |                        |       |
|                           | Soldering Temperature, for 10 seconds      | 300 (1.6mm from case.) |       |

找 PDF Thermal Resistance

|                 | Parameter   | Typ. | Max. | Units |
|-----------------|---|------|------|-------|
| R <sub>JC</sub> | Junction-to-Case                                  | —    | 2.3  | °C/W  |
| R <sub>JA</sub> | Junction-to-Ambient ( PCB Mounted steady-state)** | —    | 40   | °C/W  |

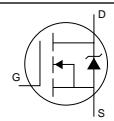
# IRFBF20S/L

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## Electrical Characteristics @ $T_J = 25^\circ\text{C}$ (unless otherwise specified)

|   | Parameter                            | Min. | Typ. | Max. | Units               | Conditions  |
|---|--------------------------------------|------|------|------|---------------------|---|
| $V_{(\text{BR})\text{DSS}}$                   | Drain-to-Source Breakdown Voltage    | 900  | —    | —    | V                   | $V_{\text{GS}} = 0\text{V}$ , $I_D = 250\mu\text{A}$                                    |
| $\Delta V_{(\text{BR})\text{DSS}/\Delta T_J}$ | Breakdown Voltage Temp. Coefficient  | —    | 1.1  | —    | V/ $^\circ\text{C}$ | Reference to $25^\circ\text{C}$ , $I_D = 1\text{mA}$ ⑤                                  |
| $R_{\text{DS}(\text{on})}$                    | Static Drain-to-Source On-Resistance | —    | —    | 8.0  | $\Omega$            | $V_{\text{GS}} = 10\text{V}$ , $I_D = 1.0\text{A}$ ④                                    |
| $V_{\text{GS}(\text{th})}$                    | Gate Threshold Voltage               | 2.0  | —    | 4.0  | V                   | $V_{\text{DS}} = V_{\text{GS}}$ , $I_D = 250\mu\text{A}$                                |
| $g_{\text{fs}}$                               | Forward Transconductance             | 0.60 | —    | —    | S                   | $V_{\text{DS}} = 50\text{V}$ , $I_D = 1.0\text{A}$ ⑤                                    |
| $I_{\text{DSS}}$                              | Drain-to-Source Leakage Current      | —    | —    | 100  | $\mu\text{A}$       | $V_{\text{DS}} = 900\text{V}$ , $V_{\text{GS}} = 0\text{V}$                             |
|   |                                      | —    | —    | 500  |                     | $V_{\text{DS}} = 720\text{V}$ , $V_{\text{GS}} = 0\text{V}$ , $T_J = 125^\circ\text{C}$ |
| $I_{\text{GSS}}$                              | Gate-to-Source Forward Leakage       | —    | —    | 100  | nA                  | $V_{\text{GS}} = 20\text{V}$  |
|   | Gate-to-Source Reverse Leakage       | —    | —    | -100 |                     | $V_{\text{GS}} = -20\text{V}$   |
| $Q_g$   | Total Gate Charge                    | —    | —    | 38   | nC                  | $I_D = 1.7\text{A}$   |
| $Q_{\text{gs}}$                               | Gate-to-Source Charge                | —    | —    | 4.7  |                     | $V_{\text{DS}} = 360\text{V}$   |
| $Q_{\text{gd}}$                               | Gate-to-Drain ("Miller") Charge      | —    | —    | 21   |                     | $V_{\text{GS}} = 10\text{V}$ , See Fig. 6 and 13 ④⑤                                     |
| $t_{\text{d}(\text{on})}$                     | Turn-On Delay Time                   | —    | 8.0  | —    | ns                  | $V_{\text{DD}} = 450\text{V}$   |
| $t_r$   | Rise Time                            | —    | 21   | —    |                     | $I_D = 1.7\text{A}$   |
| $t_{\text{d}(\text{off})}$                    | Turn-Off Delay Time                  | —    | 56   | —    |                     | $R_G = 18\Omega$  |
| $t_f$   | Fall Time                            | —    | 32   | —    |                     | $R_D = 280\Omega$ , See Fig. 10 ④⑤  |
| $L_s$   | Internal Source Inductance           | —    | 7.5  | —    | nH                  | Between lead,<br>and center of die contact  |
| $C_{\text{iss}}$                              | Input Capacitance                    | —    | 490  | —    | pF                  | $V_{\text{GS}} = 0\text{V}$   |
| $C_{\text{oss}}$                              | Output Capacitance                   | —    | 55   | —    |                     | $V_{\text{DS}} = 25\text{V}$  |
| $C_{\text{rss}}$                              | Reverse Transfer Capacitance         | —    | 18   | —    |                     | $f = 1.0\text{MHz}$ , See Fig. 5⑤   |

## Source-Drain Ratings and Characteristics

|                 | Parameter                                 | Min.  | Typ. | Max. | Units         | Conditions   |
|-----------------|---|---|------|------|---------------|--|
| $I_S$           | Continuous Source Current<br>(Body Diode) | —   | —    | 1.7  | A             | MOSFET symbol<br>showing the<br>integral reverse<br>p-n junction diode.<br> |
| $I_{\text{SM}}$ | Pulsed Source Current<br>(Body Diode) ①   | —   | —    | 6.8  |               |  |
| $V_{\text{SD}}$ | Diode Forward Voltage                     | —   | —    | 1.5  |               | $T_J = 25^\circ\text{C}$ , $I_S = 1.7\text{A}$ , $V_{\text{GS}} = 0\text{V}$ ④   |
| $t_{\text{rr}}$ | Reverse Recovery Time                     | —   | 350  | 530  | ns            | $T_J = 25^\circ\text{C}$ , $I_F = 1.7\text{A}$   |
| $Q_{\text{rr}}$ | Reverse Recovery Charge                   | —   | 0.85 | 1.3  | $\mu\text{C}$ | $dI/dt = 100\text{A}/\mu\text{s}$ ④⑤   |
| $t_{\text{on}}$ | Forward Turn-On Time                      | Intrinsic turn-on time is negligible (turn-on is dominated by $L_s+L_D$ ) |      |      |               |  |

### Notes:

① Repetitive rating; pulse width limited by max. junction temperature. ( See fig. 11 )

②  $V_{\text{DD}} = 50\text{V}$ , starting  $T_J = 25^\circ\text{C}$ ,  $L = 117\text{mH}$   
 $R_G = 25\Omega$ ,  $I_{AS} = 1.7\text{A}$ . (See Figure 11)

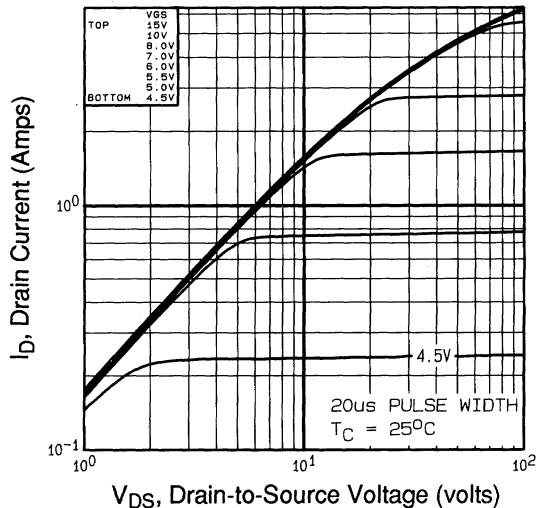
③  $I_{SD} \leq 1.7\text{A}$ ,  $dI/dt \leq 70\text{A}/\mu\text{s}$ ,  $V_{\text{DD}} \leq V_{(\text{BR})\text{DSS}}$ ,  
 $T_J \leq 150^\circ\text{C}$

④ Pulse width  $\leq 300\mu\text{s}$ ; duty cycle  $\leq 2\%$ .

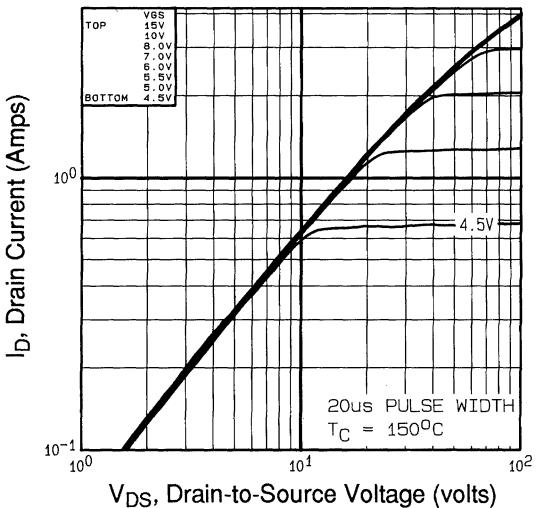
⑤ Uses IRFBF20 data and test conditions

\*\* When mounted on 1" square PCB (FR-4 or G-10 Material ).

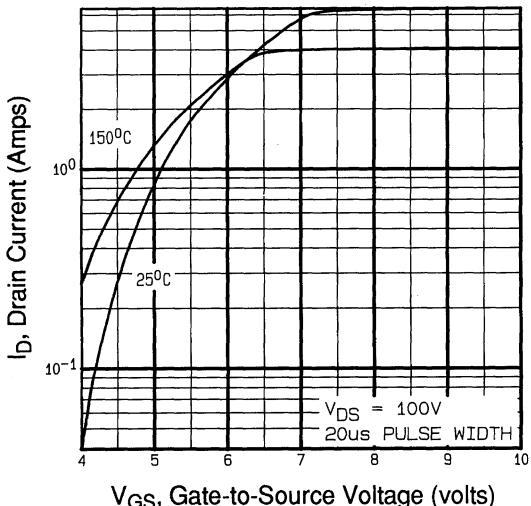
For recommended footprint and soldering techniques refer to application note #AN-994.



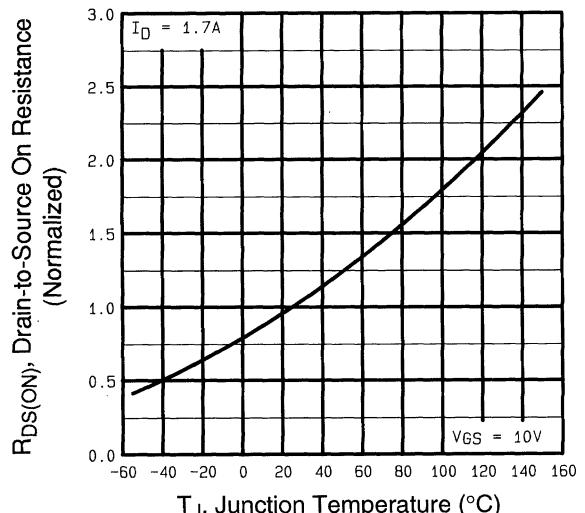
**Fig 1.** Typical Output Characteristics,



**Fig 2.** Typical Output Characteristics,



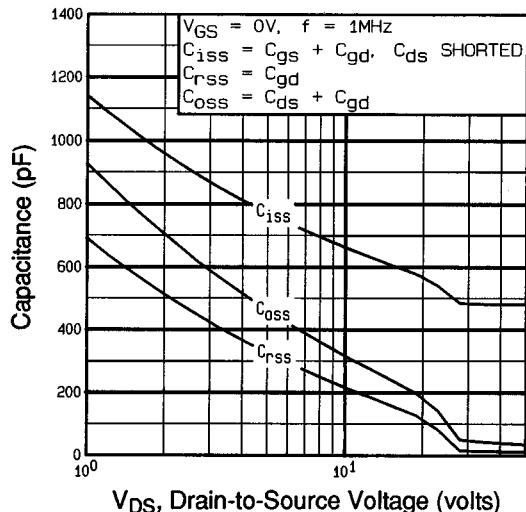
**Fig 3.** Typical Transfer Characteristics



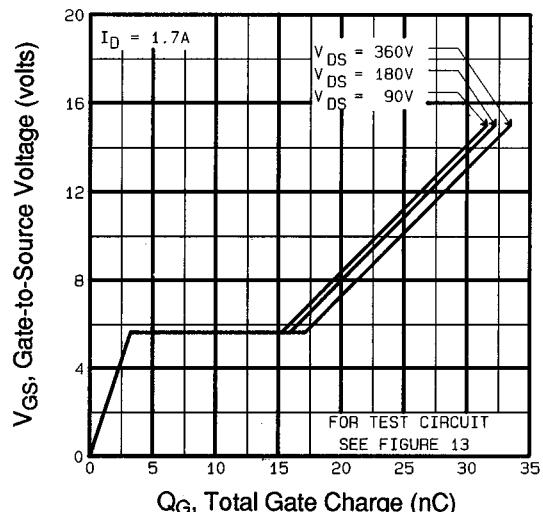
**Fig 4.** Normalized On-Resistance  
Vs. Temperature

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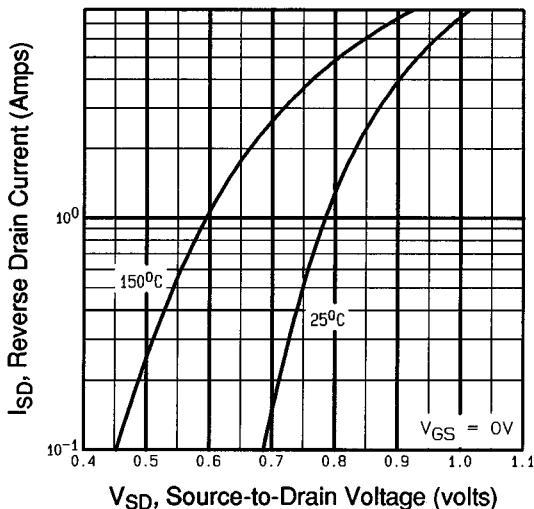
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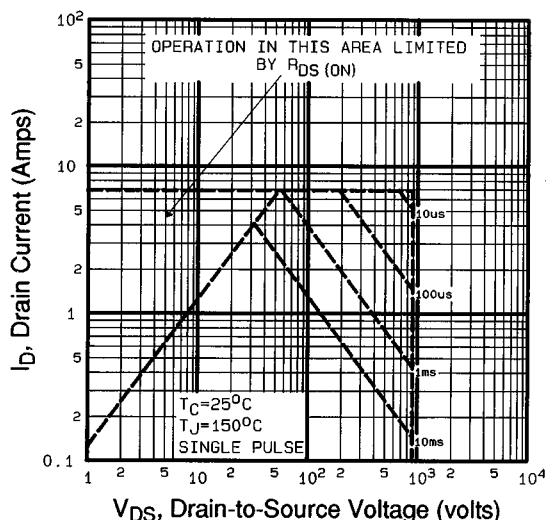
**Fig 5.** Typical Capacitance Vs.  
Drain-to-Source Voltage



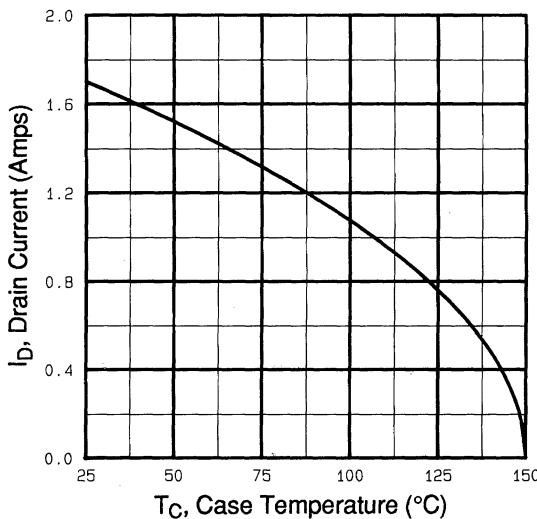
**Fig 6.** Typical Gate Charge Vs.  
Gate-to-Source Voltage



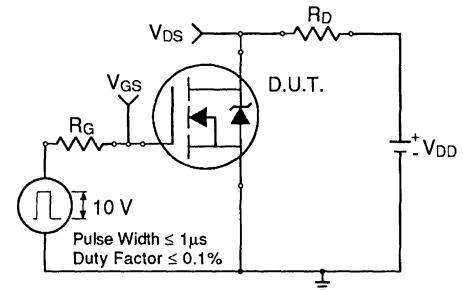
**Fig 7.** Typical Source-Drain Diode  
Forward Voltage



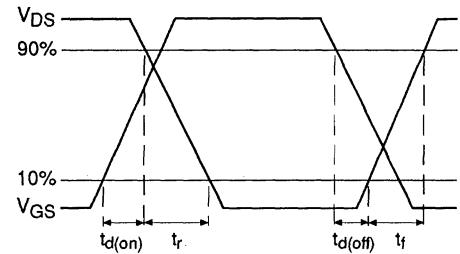
**Fig 8.** Maximum Safe Operating Area



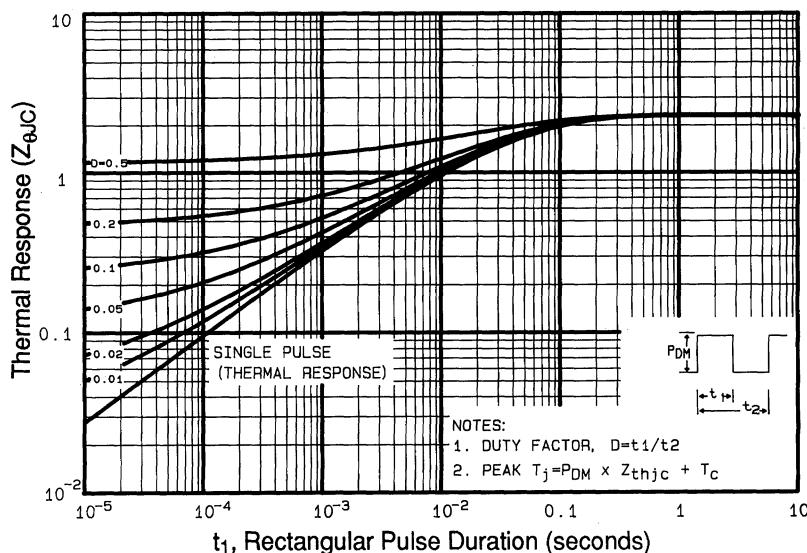
**Fig 9.** Maximum Drain Current Vs.  
Case Temperature



**Fig 10a.** Switching Time Test Circuit



**Fig 10b.** Switching Time Waveforms



**Fig 11.** Maximum Effective Transient Thermal Impedance, Junction-to-Case

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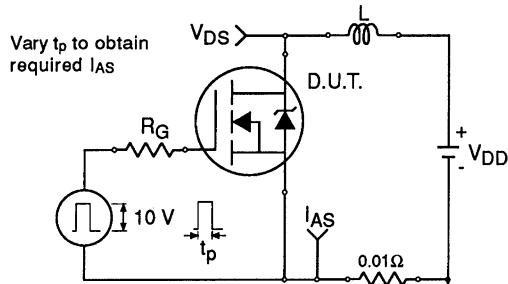


Fig 12a. Unclamped Inductive Test Circuit

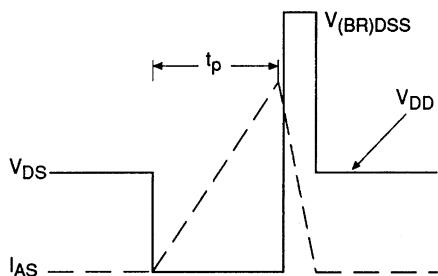


Fig 12b. Unclamped Inductive Waveforms

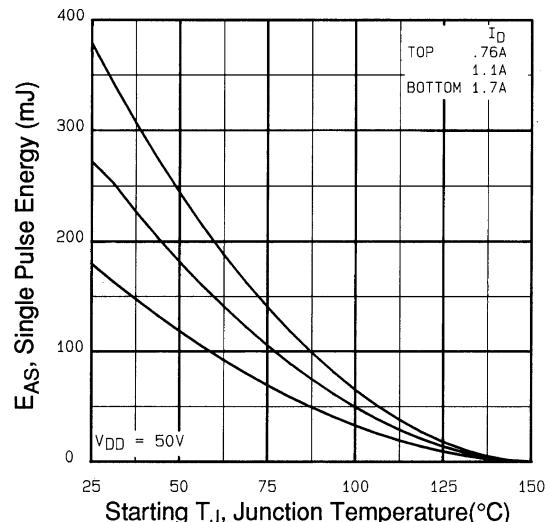


Fig 12c. Maximum Avalanche Energy Vs. Drain Current

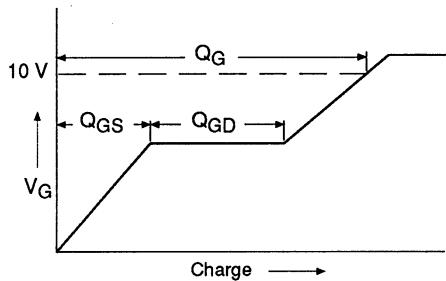


Fig 13a. Basic Gate Charge Waveform

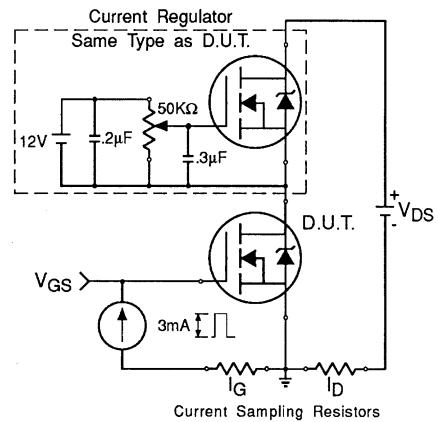
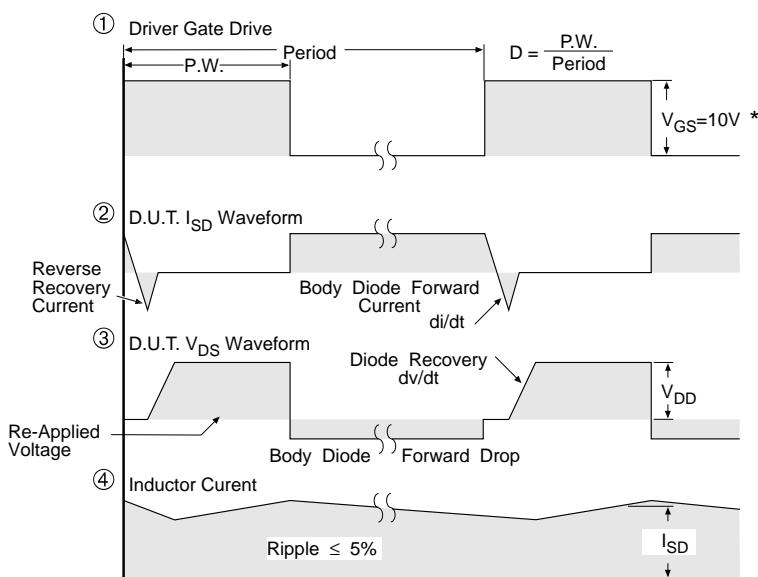
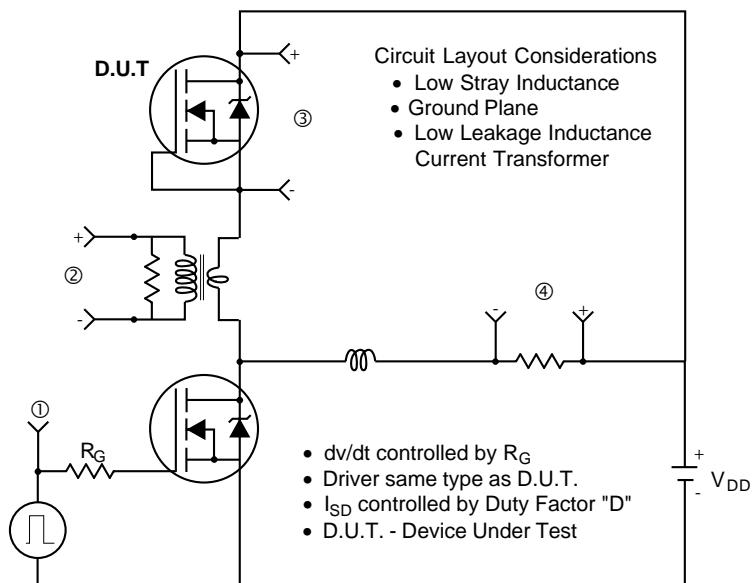


Fig 13b. Gate Charge Test Circuit

### Peak Diode Recovery dv/dt Test Circuit



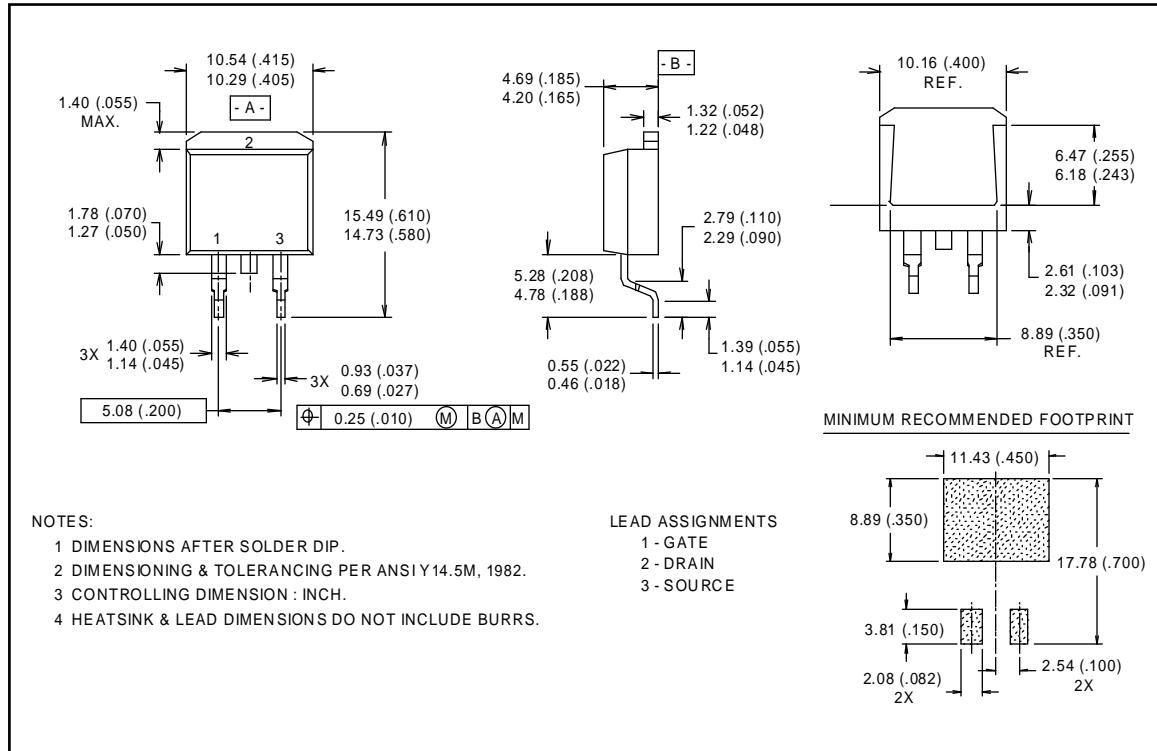
\*  $V_{GS} = 5V$  for Logic Level Devices

**Fig.14** For N-Channel HEXFETs

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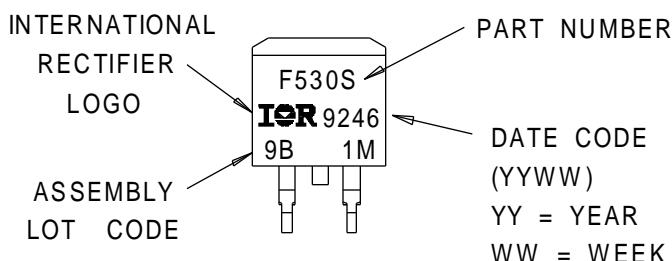
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## D<sup>2</sup>Pak Package Outline



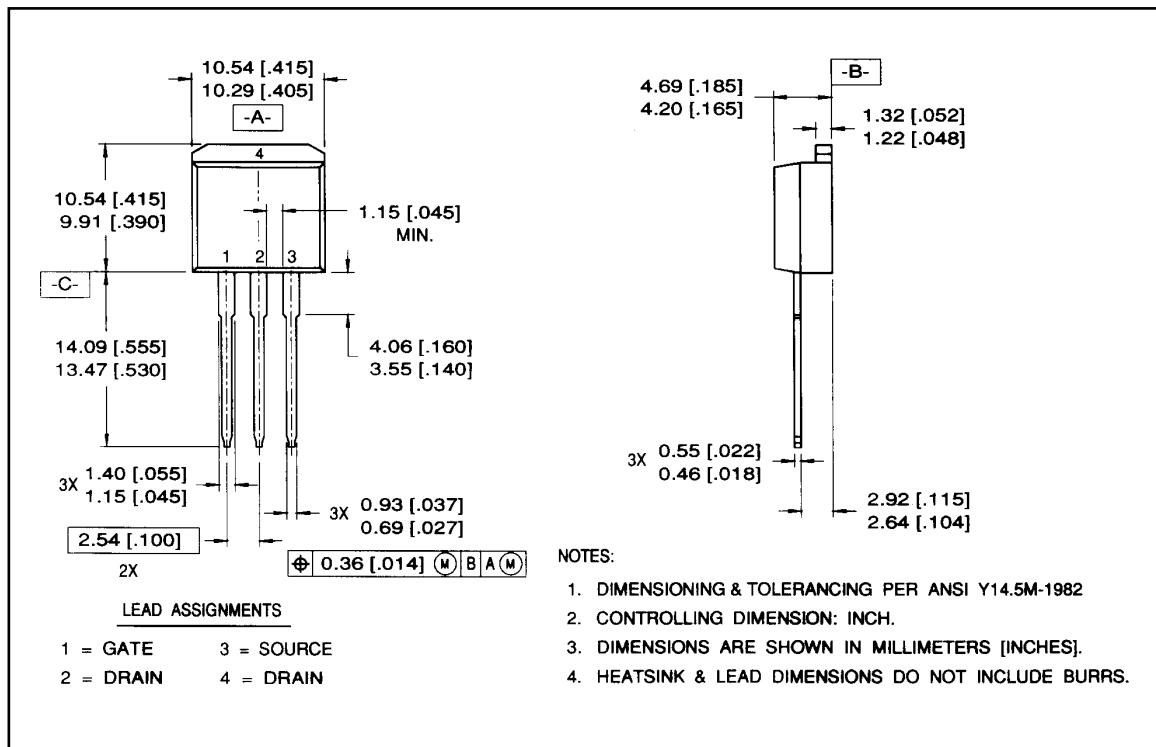
## Part Marking Information

D<sup>2</sup>Pak



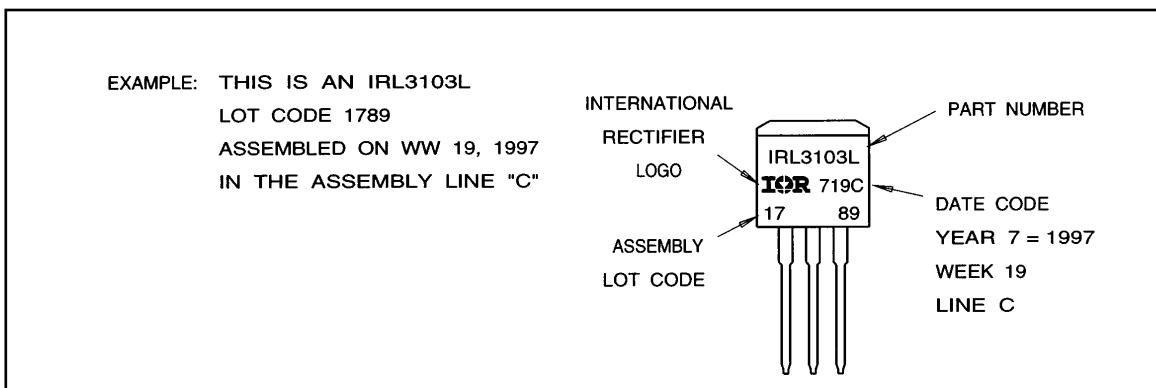
## Package Outline

### TO-262 Outline



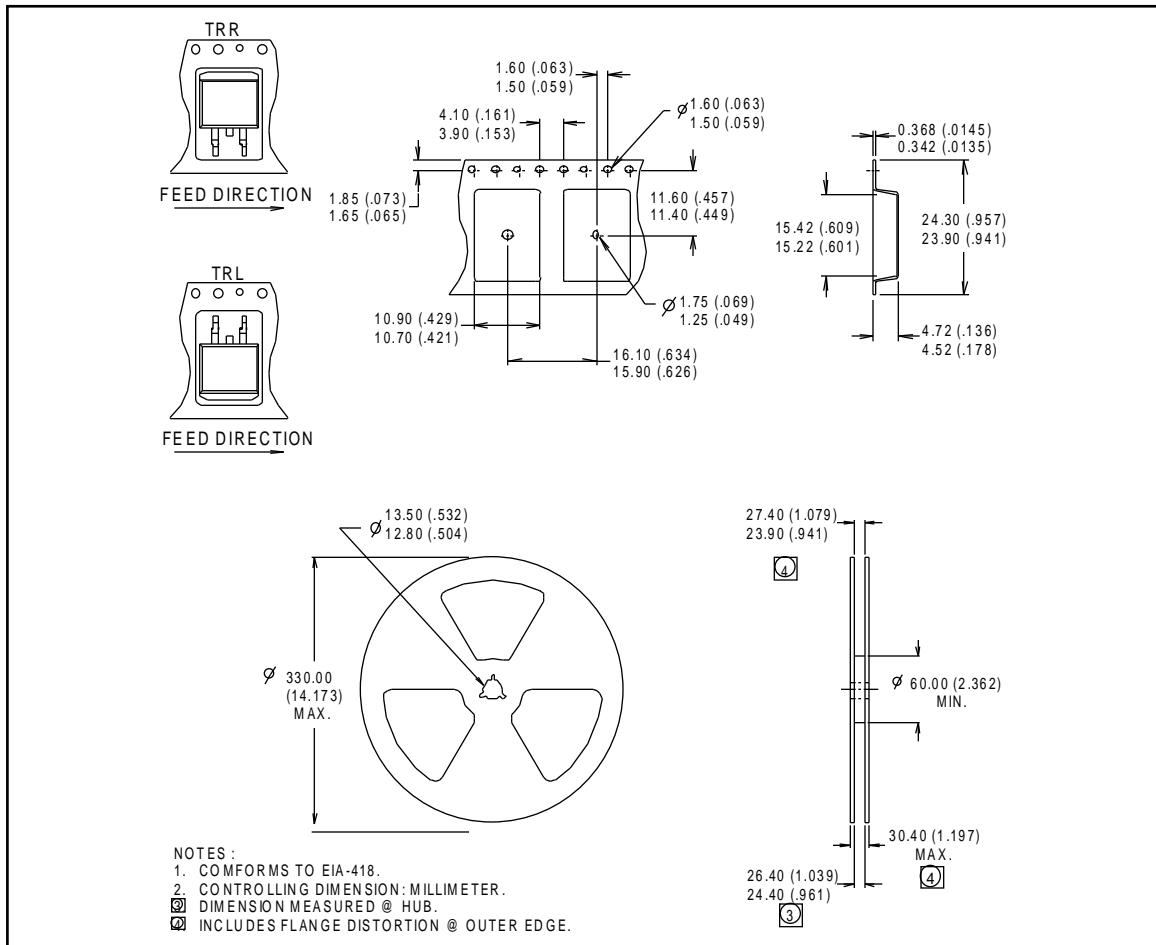
## Part Marking Information

### TO-262



## Tape & Reel Information

D<sup>2</sup>Pak



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