

FJN3302R

Switching Application (Bias Resistor Built In)

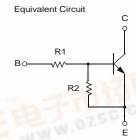
- Switching circuit, Inverter, Interface circuit, Driver Circuit
- Built in bias Resistor ($R_1=10K\Omega$, $R_2=10K\Omega$)
- Complement to FJN4302R



NPN Epitaxial Silicon Transistor

Absolute Maximum Ratings Ta=25°C unless otherwise noted

Parameter	Value	Units
Collector-Base Voltage	50	V
Collector-Emitter Voltage	50	V
Emitter-Base Voltage	10	V
Collector Current	100	mA
Collector Power Dissipation	300	mW
Junction Temperature	150	°C
Storage Temperature	-55 ~ 150	°C
	Collector-Base Voltage Collector-Emitter Voltage Emitter-Base Voltage Collector Current Collector Power Dissipation Junction Temperature	Collector-Base Voltage 50 Collector-Emitter Voltage 50 Emitter-Base Voltage 10 Collector Current 100 Collector Power Dissipation 300 Junction Temperature 150



Electrical Characteristics Ta=25°C unless otherwise noted

Symbol	Parameter	Test Condition	Min.	Тур.	Max.	Units
BV _{CBO}	Collector-Base Breakdown Voltage	I _C =10μA, I _E =0	50			V
BV _{CEO}	Collector-Emitter Breakdown Voltage I _C =100μA, I _B =0		50			V
I _{CBO}	Collector Cut-off Current	V _{CB} =40V, I _E =0			0.1	μΑ
h _{FE}	DC Current Gain	V _{CE} =5V, I _C =5mA	30			
V _{CE} (sat)	Collector-Emitter Saturation Voltage	I _C =10mA, I _B =0.5mA			0.3	V
f _T	Current Gain Bandwidth Product	V _{CE} =10V, I _C =5mA		250		MHz
C _{ob}	Output Capacitance	V _{CB} =10V, I _E =0 f=1.0MHz		3.7	1 0Z	pF
V _I (off)	Input Off Voltage	V _{CE} =5V, I _C =100μA	0.5	W.W.		V
V _I (on)	Input On Voltage	V _{CE} =0.3V, I _C =10mA	I BEEN		3	V
R ₁	Input Resistor	EA\((1)	7	10	13	ΚΩ
R_1/R_2	Resistor Ratio		0.9	1	1.1	

Typical Characteristics

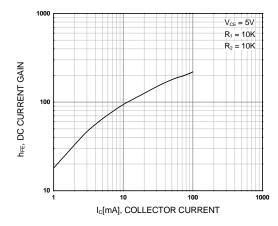


Figure 1. DC current Gain

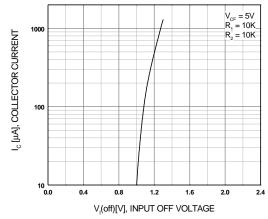


Figure 3. Input Off Voltage

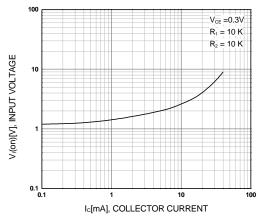


Figure 2. Input On Voltage

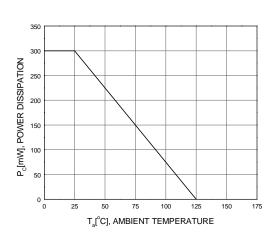
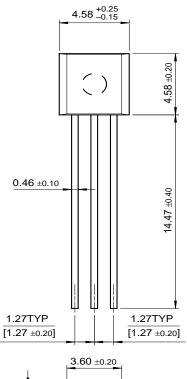


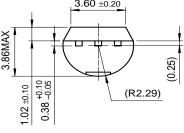
Figure 4. Power Derating

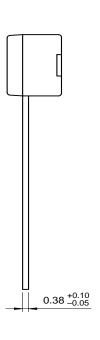
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Package Dimensions

TO-92







Dimensions in Millimeters

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