

FJN3313R

Switching Application (Bias Resistor Built In)

- Switching circuit, Inverter, Interface circuit, Driver Circuit
- Built in bias Resistor ($R_1 = 2.2K\Omega$, $R_2 = 47K\Omega$) WWW.DZSG
- Complement to FJN4313R

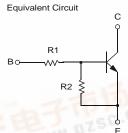


1. Emitter 2. Collector 3. Base

NPN Epitaxial Silicon Transistor

Absolute Maximum Ratings Ta=25°C unless otherwise noted

Symbol	Parameter	Value	Units
V _{CBO}	Collector-Base Voltage	50	V
V _{CEO}	Collector-Emitter Voltage	50	V
V _{EBO}	Emitter-Base Voltage	10	V
Ic	Collector Current	100	mA
P _C	Collector Power Dissipation	300	mW
T _J	Junction Temperature	150	°C
T _{STG}	Storage Temperature	-55 ~ 150	°C

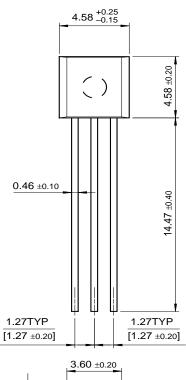


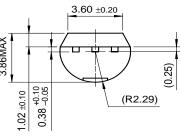
Electrical Characteristics Ta=25°C unless otherwise noted

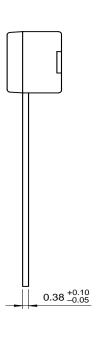
ODO		Test Condition	Min.	Тур.	Max.	Units
	follector-Base Breakdown Voltage	$I_{C}=10\mu A, I_{E}=0$	50			V
BV _{CEO} C	ollector-Emitter Breakdown Voltage	I _C =100μA, I _B =0	50			V
I _{CBO} Co	ollector Cut-off Current	V_{CB} =40V, I_E =0			0.1	μΑ
h _{FE} D	C Current Gain	V_{CE} =5V, I_{C} =5mA	68			
V _{CE} (sat) C	collector-Emitter Saturation Voltage	I _C =10mA, I _B =0.5mA			0.3	V
f _T C	urrent Gain Bandwidth Product	V_{CE} =10V, I_{C} =5mA		250		MHz
	Output Capacitance	V _{CB} =10V, I _E =0 f=1.0MHz		3.7	W 07	pF
V _I (off) In	nput Off Voltage	V _{CE} =5V, I _C =100μA	0.5	WW		V
V _I (on) In	nput On Voltage	V _{CE} =0.2V, I _C =5mA			1.1	V
R ₁ In	put Resistor	FW/(63 ===	1.5	2.2	2.9	ΚΩ
R ₁ /R ₂ R	esistor Ratio		0.042	0.047	0.052	

Package Dimensions

TO-92







Dimensions in Millimeters

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DOME™	GlobalOptoisolator™	MICROWIRE™	QS™	SyncFET™
EcoSPARK™	GTO™	MSX™	QT Optoelectronics™	TinyLogic™
E ² CMOS™	HiSeC™	MSXPro™	Quiet Series™	TruTranslation™
EnSigna™	I^2C^{TM}	OCX^{TM}	RapidConfigure™	UHC™
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Programmable Active Droop™		OPTOPLANAR™	SMART START™	

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