

FJN4308R

Switching Application (Bias Resistor Built In)

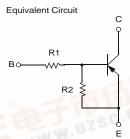
- Switching circuit, Inverter, Interface circuit, Driver Circuit
- Built in bias Resistor (R₁=47KΩ, R₂=22KΩ)
- Complement to FJN3308R



PNP Epitaxial Silicon Transistor

Absolute Maximum Ratings Ta=25°C unless otherwise noted

Symbol	Parameter	Value	Units
V _{CBO}	Collector-Base Voltage	-50	V
V _{CEO}	Collector-Emitter Voltage	-50	V
V _{EBO}	Emitter-Base Voltage	-10	V
Ic	Collector Current	-100	mA
P _C	Collector Power Dissipation	300	mW
T _J	Junction Temperature	150	°C
T _{STG}	Storage Temperature	-55 ~ 150	°C



Electrical Characteristics Ta=25°C unless otherwise noted

000		Test Condition	Min.	Тур.	Max.	Units
	ollector-Base Breakdown Voltage	I_{C} = -10 μ A, I_{E} =0	-50			V
BV _{CEO} Co	ollector-Emitter Breakdown Voltage	$I_C = -100 \mu A, I_B = 0$	-50			V
I _{CBO} Co	ollector Cut-off Current	V _{CB} = -40V, I _E =0			-0.1	μΑ
h _{FE} DO	C Current Gain	V_{CE} = -5V, I_{C} = -5mA	56			
V _{CE} (sat) Co	ollector-Emitter Saturation Voltage	I _C = -10mA, I _B = -0.5mA			-0.3	V
f _T Cu	urrent Gain Bandwidth Product	V_{CE} = -10V, I_{C} = -5mA		200	- 45	MHz
	utput Capacitance	V _{CB} = -10V, I _E =0 f=1.0MHz		5.5	W DZ	pF
V _I (off) In	put Off Voltage	$V_{CE} = -5V, I_{C} = -100\mu A$	-0.8	W.W.		V
V _I (on) In	put On Voltage	$V_{CE} = -0.3V, I_{C} = -2mA$			-4	V
R ₁ In	put Resistor	EA/((2) ===	32	47	62	ΚΩ
R ₁ /R ₂ Re	esistor Ratio		1.9	2.1	2.4	

Typical Characteristics

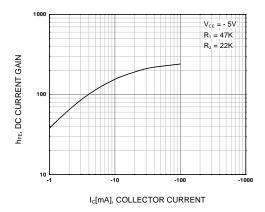


Figure 1. DC current Gain

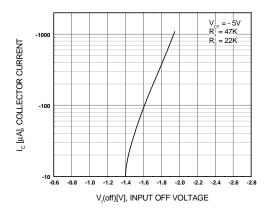


Figure 3. Input Off Voltage

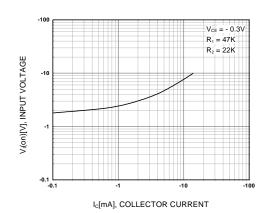


Figure 2. Input On Voltage

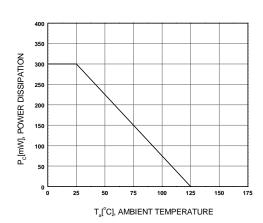
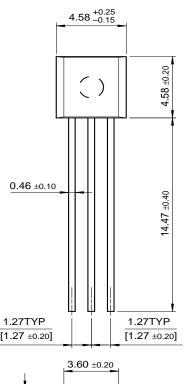


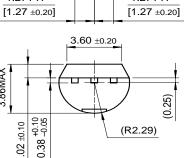
Figure 4. Power Derating

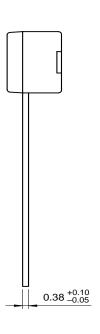
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Package Dimensions

TO-92







Dimensions in Millimeters

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