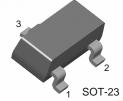


FJV3114R

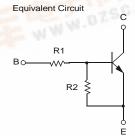
Switching Application (Bias Resistor Built In)

- Switching circuit, Inverter, Interface circuit, Driver Circuit
- Built in bias Resistor (R₁ =4.7KΩ, R₂=47KΩ)
- Complement to FJV4114R



1. Base 2. Emitter 3. Collector





NPN Epitaxial Silicon Transistor

Absolute Maximum Ratings T_a=25°C unless otherwise noted

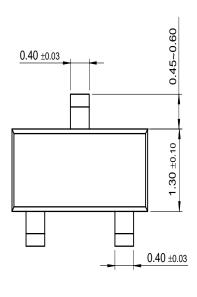
Symbol	Parameter	Value	Units	
V _{CBO}	Collector-Base Voltage	50	V	
V _{CEO}	Collector-Emitter Voltage	50	V	
√ _{EBO}	Emitter-Base Voltage	10	V	
С	Collector Current	100	mA	
°c	Collector Power Dissipation	200	mW	
Γ _J	Junction Temperature	150	°C	
T _{STG}	Storage Temperature	-55 ~ 150	°C	

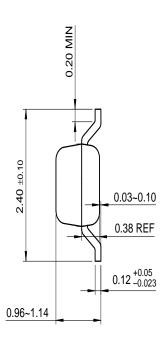
Electrical Characteristics T_a=25°C unless otherwise noted

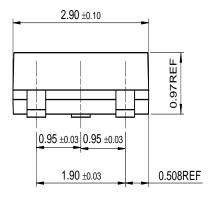
Symbol	Parameter	Test Condition	Min.	Тур.	Max.	Units
BV _{CBO}	Collector-Base Breakdown Voltage	$I_{C}=10\mu A, I_{E}=0$	50			V
BV _{CEO}	Collector-Emitter Breakdown Voltage	I _C =100μA, I _B =0	50		-11	V
I _{CBO}	Collector Cut-off Current	V _{CB} =40V, I _E =0			0.1	μΑ
h _{FE}	DC Current Gain	V _{CE} =5V, I _C =5mA	68	W.A.		
V _{CE} (sat)	Collector-Emitter Saturation Voltage	I _C =10mA, I _B =0.5mA	1000		0.3	V
f _T	Current Gain Bandwidth Product	V _{CE} =10V, I _C =5mA		250		MHz
C _{ob}	Output Capacitance	V _{CB} =10V, I _E =0 f=1.0MHz		3.7		pF
V _I (off)	Input Off Voltage	V _{CE} =5V, I _C =100μA	0.5			V
V _I (on)	Input On Voltage	V_{CE} =0.2V, I_{C} =5mA			1.3	V
R ₁	Input Resistor		3.2	4.7	6.2	ΚΩ
R ₁ /R ₂	Resistor Ratio		0.09	0.1	0.11	

Package Dimensions

SOT-23







Dimensions in Millimeters

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E ² CMOS™	HiSeC™	MSXPro™	Quiet Series™	TruTranslation™
EnSigna™	I^2C^{TM}	OCX^{TM}	RapidConfigure™	UHC™
Across the board.	. Around the world.™	OCXPro™	RapidConnect™	UltraFET [®]
The Power Franc	hise™	OPTOLOGIC [®]	SILENT SWITCHER®	VCX^{TM}
Programmable Ad	ctive Droop™	OPTOPLANAR™	SMART START™	

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