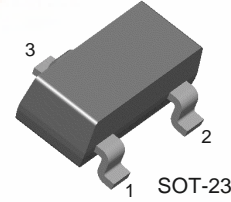


FAIRCHILD
SEMICONDUCTOR®

FJV4101R

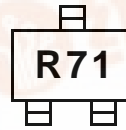
Switching Application (Bias Resistor Built In)

- Switching circuit, Inverter, Interface circuit, Driver Circuit
- Built in bias Resistor ($R_1=4.7K\Omega$, $R_2=4.7K\Omega$)
- Complement to FJV3101R

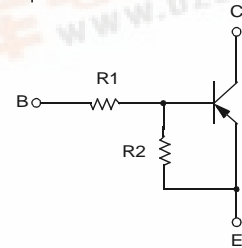


1. Base 2. Emitter 3. Collector

Marking



Equivalent Circuit



PNP Epitaxial Silicon Transistor

Absolute Maximum Ratings $T_a=25^\circ\text{C}$ unless otherwise noted

| Symbol | Parameter | Value | Units |
|-----------|-----------------------------|-----------|------------------|
| V_{CBO} | Collector-Base Voltage | -50 | V |
| V_{CEO} | Collector-Emitter Voltage | -50 | V |
| V_{EBO} | Emitter-Base Voltage | -10 | V |
| I_C | Collector Current | -100 | mA |
| P_C | Collector Power Dissipation | 200 | mW |
| T_J | Junction Temperature | 150 | $^\circ\text{C}$ |
| T_{STG} | Storage Temperature | -55 ~ 150 | $^\circ\text{C}$ |

Electrical Characteristics $T_a=25^\circ\text{C}$ unless otherwise noted

| Symbol | Parameter | Test Condition | Min. | Typ. | Max. | Units |
|---------------|--------------------------------------|---|------|------|------|---------------|
| BV_{CBO} | Collector-Base Breakdown Voltage | $I_C = -10\mu\text{A}$, $I_E = 0$ | -50 | | | V |
| BV_{CEO} | Collector-Emitter Breakdown Voltage | $I_C = -100\mu\text{A}$, $I_B = 0$ | -50 | | | V |
| I_{CBO} | Collector Cut-off Current | $V_{CB} = -40\text{V}$, $I_E = 0$ | | | -0.1 | μA |
| h_{FE} | DC Current Gain | $V_{CE} = -5\text{V}$, $I_C = -10\text{mA}$ | 20 | | | |
| $V_{CE(sat)}$ | Collector-Emitter Saturation Voltage | $I_C = -10\text{mA}$, $I_B = -0.5\text{mA}$ | | | -0.3 | V |
| f_T | Current Gain Bandwidth Product | $V_{CE} = -10\text{V}$, $I_C = -5\text{mA}$ | | 200 | | MHz |
| C_{ob} | Output Capacitance | $V_{CB} = -10\text{V}$, $I_E = 0$ $f = 1.0\text{MHz}$ | | 5.5 | | pF |
| $V_{I(off)}$ | Input Off Voltage | $V_{CE} = -5\text{V}$, $I_C = -100\mu\text{A}$ | -0.5 | | | V |
| $V_{I(on)}$ | Input On Voltage | $V_{CE} = -0.3\text{V}$, $I_C = -20\text{mA}$ | | | -3 | V |
| R_1 | Input Resistor | | 3.2 | 4.7 | 6.2 | $K\Omega$ |
| R_1/R_2 | Resistor Ratio | | 0.9 | 1 | 1.1 | |

Typical Characteristics

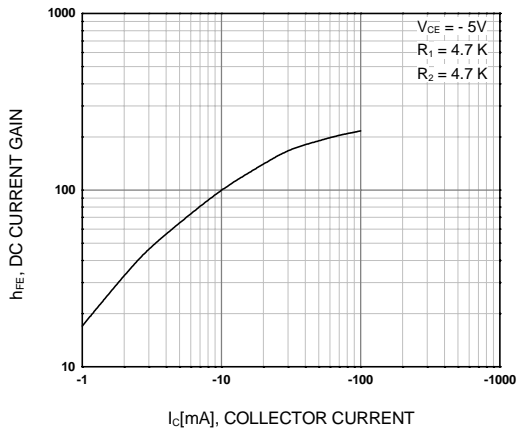


Figure 1. DC current Gain

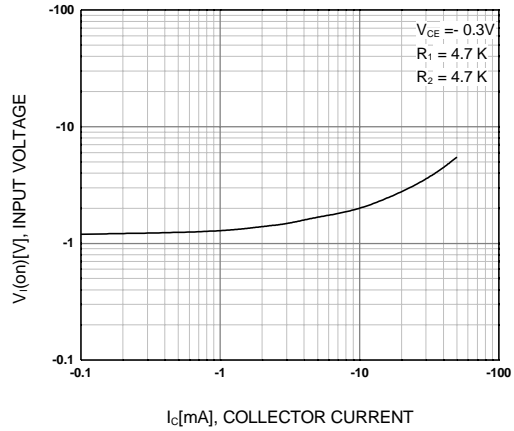


Figure 2. Input On Voltage

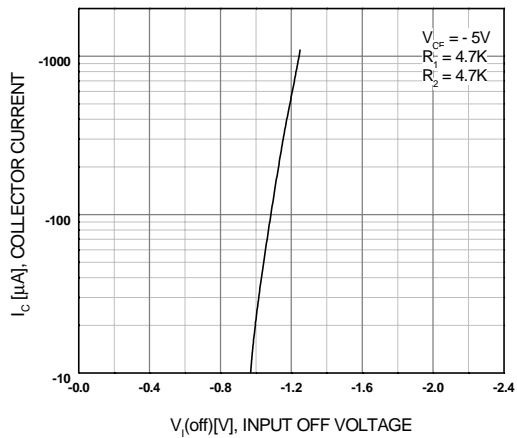


Figure 3. Input Off Voltage

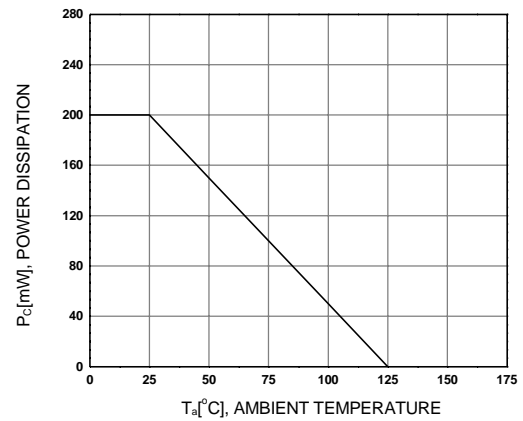
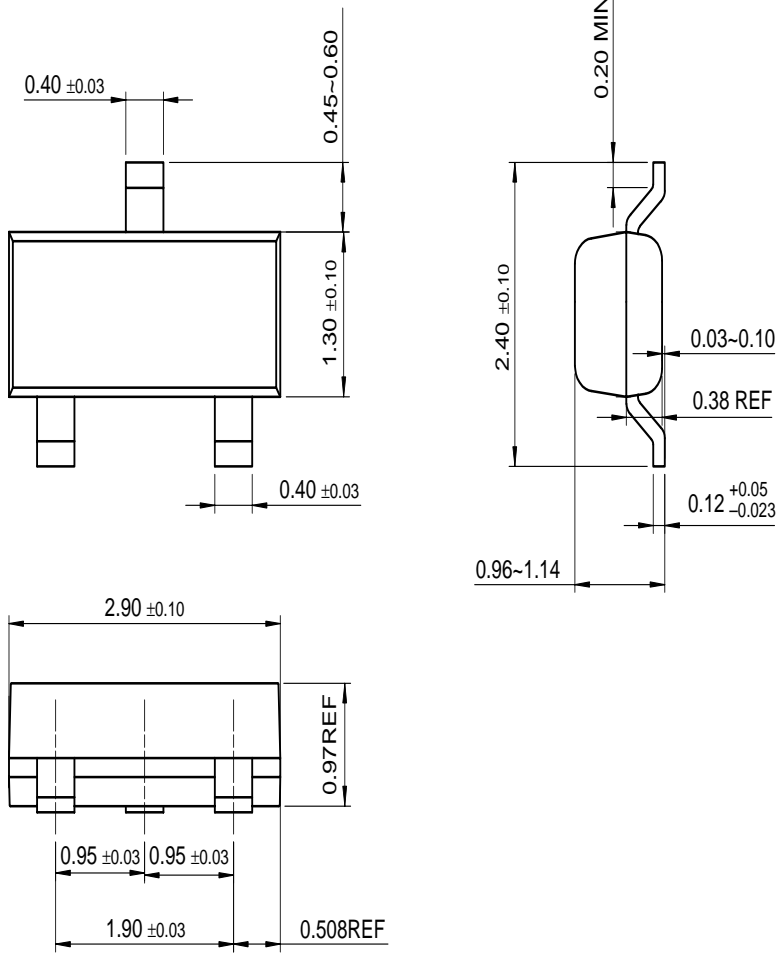


Figure 4. Power Derating

Package Demensions

SOT-23



Dimensions in Millimeters

TRADEMARKS

The following are registered and unregistered trademarks Fairchild Semiconductor owns or is authorized to use and is not intended to be an exhaustive list of all such trademarks.

| | | | | |
|----------------------|---------------------|---------------------|-----------------|------|
| ACEx™ | FASTr™ | OPTOLOGIC® | SMART START™ | VCX™ |
| Bottomless™ | FRFET™ | OPTOPLANAR™ | SPM™ | |
| CoolFET™ | GlobalOptoisolator™ | PACMAN™ | Stealth™ | |
| CROSSVOLT™ | GTO™ | POP™ | SuperSOT™-3 | |
| DOME™ | HiSeC™ | Power247™ | SuperSOT™-6 | |
| EcoSPARK™ | I ² C™ | PowerTrench® | SuperSOT™-8 | |
| E ² CMOS™ | ISOPLANAR™ | QFET™ | SyncFET™ | |
| EnSigna™ | LittleFET™ | QS™ | TinyLogic™ | |
| FACT™ | MicroFET™ | QT Optoelectronics™ | TruTranslation™ | |
| FACT Quiet series™ | MicroPak™ | Quiet Series™ | UHC™ | |
| FAST® | MICROWIRE™ | SLIENT SWITCHER® | UltraFET® | |

DISCLAIMER

FAIRCHILD SEMICONDUCTOR RESERVES THE RIGHT TO MAKE CHANGES WITHOUT FURTHER NOTICE TO ANY PRODUCTS HEREIN TO IMPROVE RELIABILITY, FUNCTION OR DESIGN. FAIRCHILD DOES NOT ASSUME ANY LIABILITY ARISING OUT OF THE APPLICATION OR USE OF ANY PRODUCT OR CIRCUIT DESCRIBED HEREIN; NEITHER DOES IT CONVEY ANY LICENSE UNDER ITS PATENT RIGHTS, NOR THE RIGHTS OF OTHERS.

LIFE SUPPORT POLICY

FAIRCHILD'S PRODUCTS ARE NOT AUTHORIZED FOR USE AS CRITICAL COMPONENTS IN LIFE SUPPORT DEVICES OR SYSTEMS WITHOUT THE EXPRESS WRITTEN APPROVAL OF FAIRCHILD SEMICONDUCTOR CORPORATION.

As used herein:

1. Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body, or (b) support or sustain life, or (c) whose failure to perform when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in significant injury to the user.
2. A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

PRODUCT STATUS DEFINITIONS

Definition of Terms

| Datasheet Identification | Product Status | Definition |
|--------------------------|------------------------|---|
| Advance Information | Formative or In Design | This datasheet contains the design specifications for product development. Specifications may change in any manner without notice. |
| Preliminary | First Production | This datasheet contains preliminary data, and supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design. |
| No Identification Needed | Full Production | This datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design. |
| Obsolete | Not In Production | This datasheet contains specifications on a product that has been discontinued by Fairchild semiconductor. The datasheet is printed for reference information only. |