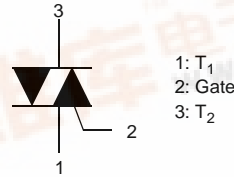
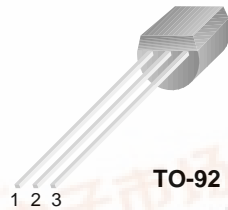


FAIRCHILD
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FKN2L60

Application Explanation

- Switching mode power supply, light dimmer, electric flasher unit, hair drier
- TV sets, stereo, refrigerator, washing machine
- Electric blanket, solenoid driver, small motor control
- Photo copier, electric tool



Bi-Directional Triode Thyristor Planar Silicon

Absolute Maximum Ratings $T_C=25^\circ\text{C}$ unless otherwise noted

| Symbol | Parameter | Rating | Units |
|------------------|--|--------|-------|
| V_{DRM} | Repetitive Peak Off-State Voltage (Note 1) | 600 | V |

| Symbol | Parameter | Conditions | Rating | Units | |
|----------------------|---|---|------------|------------------------|---|
| $I_{\text{T (RMS)}}$ | RMS On-State Current | Commercial frequency, sine full wave 360° conduction, $T_C=65^\circ\text{C}$ | 1.5 | A | |
| I_{TSM} | Surge On-State Current | Sinewave 1 full cycle, peak value, non-repetitive | 50Hz | 9 | A |
| | | | 60Hz | 10 | A |
| I^2t | I^2t for Fusing | Value corresponding to 1 cycle of halfwave, surge on-state current, $t_p=10\text{ms}$ | 0.4 | A^2s | |
| di/dt | Critical Rate of Rise of On-State Current | $I_G = 2 \times I_{\text{GT}}$, $t_r \leq 100\text{ns}$ | 50 | $\text{A}/\mu\text{s}$ | |
| P_{GM} | Peak Gate Power Dissipation | | 1 | W | |
| $P_{\text{G (AV)}}$ | Average Gate Power Dissipation | | 0.1 | W | |
| V_{GM} | Peak Gate Voltage | | 6 | V | |
| I_{GM} | Peak Gate Current | | 0.5 | A | |
| T_{J} | Junction Temperature | | - 40 ~ 125 | $^\circ\text{C}$ | |
| T_{STG} | Storage Temperature | | - 40 ~ 125 | $^\circ\text{C}$ | |

Thermal Characteristic

| Symbol | Parameter | Test Condition | Min. | Typ. | Max. | Units |
|----------------------|--------------------|---------------------------|------|------|------|---------------------------|
| $R_{\text{th(J-C)}}$ | Thermal Resistance | Junction to case (Note 4) | - | - | 40 | $^\circ\text{C}/\text{W}$ |



Electrical Characteristics $T_C=25^\circ\text{C}$ unless otherwise noted

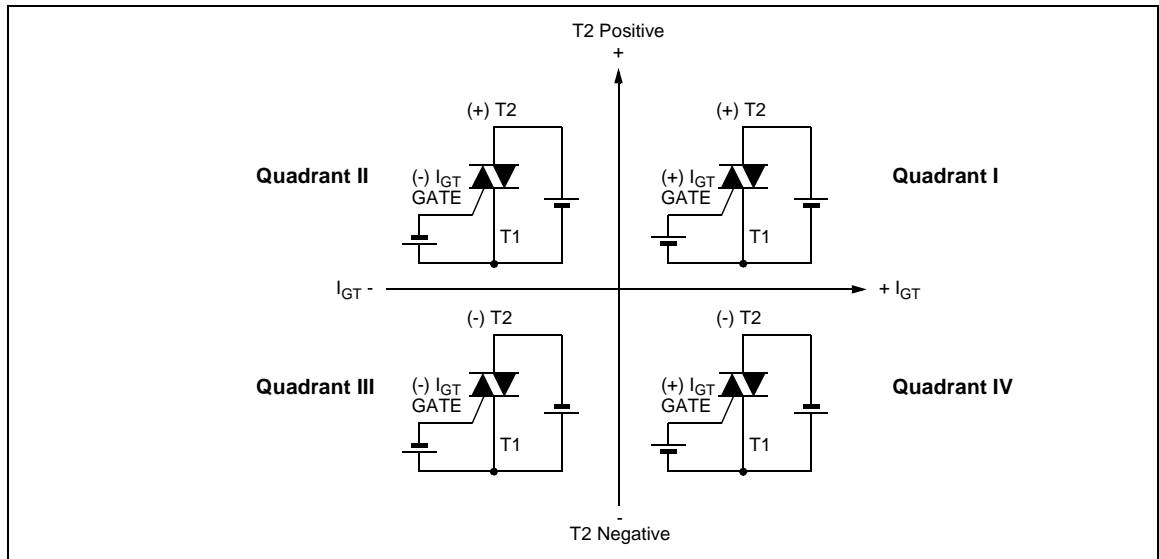
| Symbol | Parameter | Test Condition | Min. | Typ. | Max. | Units | |
|------------------|---|---|-----------------|------|------|------------------|----|
| I_{DRM} | Repetitive Peak Off-State Current | V_{DRM} applied | - | - | 20 | μA | |
| V_{TM} | On-State Voltage | $T_C=25^\circ\text{C}$, $I_{\text{TM}}=3\text{A}$ Instantaneous measurement | - | - | 1.6 | V | |
| V_{GT} | Gate Trigger Voltage (Note 2) | $V_{\text{D}}=12\text{V}$, $R_{\text{L}}=20\Omega$ | T2(+), Gate (+) | - | - | 1.5 | V |
| | | | T2(+), Gate (-) | - | - | 1.5 | V |
| | | | T2(-), Gate (-) | - | - | 1.5 | V |
| I_{GT} | Gate Trigger Current (Note 2) | $V_{\text{D}}=12\text{V}$, $R_{\text{L}}=20\Omega$ | T2(+), Gate (+) | - | - | 5 | mA |
| | | | T2(+), Gate (-) | - | - | 5 | mA |
| | | | T2(-), Gate (-) | - | - | 5 | mA |
| V_{GD} | Gate Non-Trigger Voltage | $T_J=125^\circ\text{C}$, $V_{\text{D}}=1/2V_{\text{DRM}}$ | 0.2 | - | - | V | |
| I_{H} | Holding Current | $V_{\text{D}}=12\text{V}$, $I_{\text{TM}}=1\text{A}$ | - | - | 10 | mA | |
| I_{L} | Latching Current | $V_{\text{D}}=12\text{V}$, $I_{\text{G}}=1.2I_{\text{GT}}$ | I, III | - | - | 10 | mA |
| | | | II | - | - | 10 | mA |
| dv/dt | Critical Rate of Rise of Off-State Voltage | $V_{\text{DRM}} = \text{Rated}$, $T_J = 125^\circ\text{C}$, Exponential Rise | 500 | - | - | V/ μs | |
| $(dv/dt)_C$ | Critical-Rate of Rise of Off-State Commutating Voltage (Note 3) | | 5 | - | - | V/ μs | |

Notes:

- Gate Open
- Measurement using the gate trigger characteristics measurement circuit
- The critical-rate of rise of the off-state commutating voltage is shown in the table below
- Case temperature is measured at the T2 terminal 1.5mm away from the molded case.

| V_{DRM} (V) | Test Condition | Commutating voltage and current waveforms (inductive load) |
|-------------------------|---|---|
| FKN2L60 | <ol style="list-style-type: none"> Junction Temperature $T_J=125^\circ\text{C}$ Rate of decay of on-state commutating current $(di/dt)_C = -0.5\text{A/ms}$ Peak off-state voltage $V_{\text{D}} = 400\text{V}$ | |

Quadrant Definitions for a Triac



Typical Curves

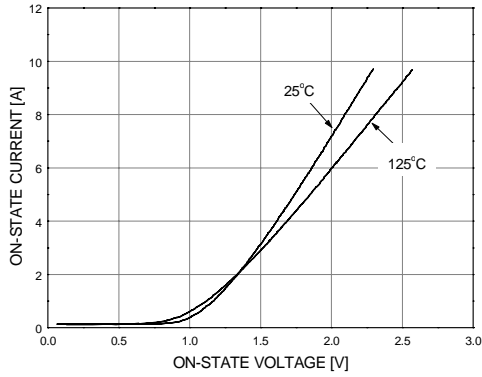


Figure 1. Maximum On-state Characteristics

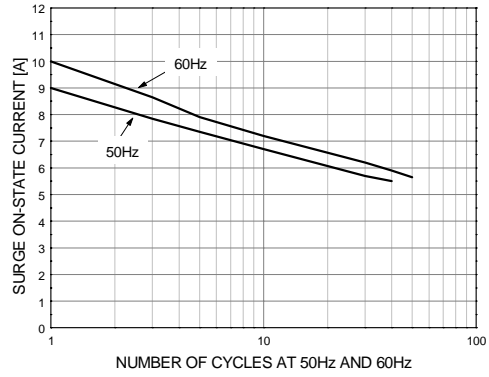


Figure 2. Rated Surge On-state Current

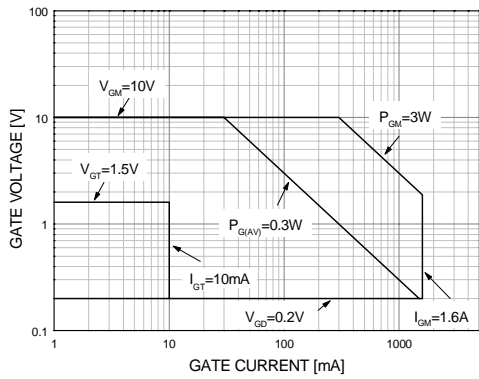


Figure 3. Gate Characteristics

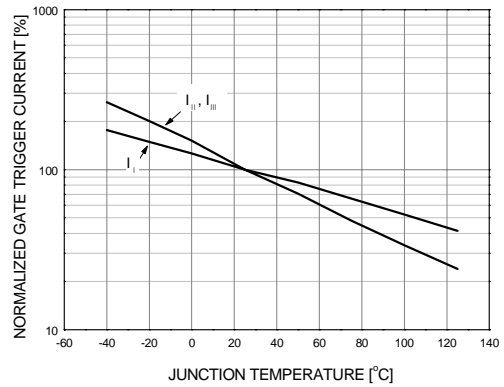


Figure 4. Gate Trigger Current vs T_j

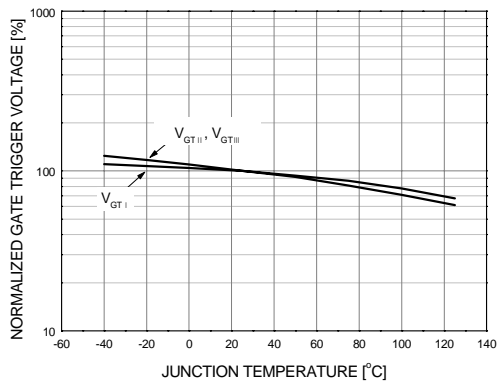


Figure 5. Gate Trigger Voltage vs T_j

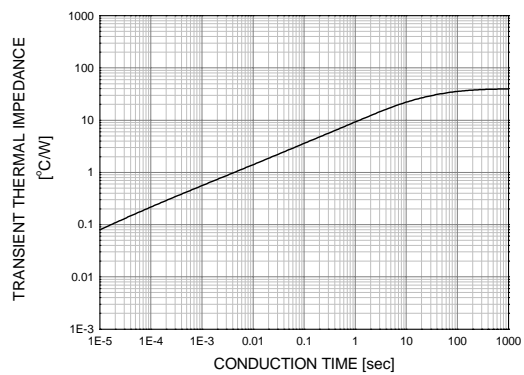


Figure 6. Transient Thermal Impedance

Typical Curves (Continues)

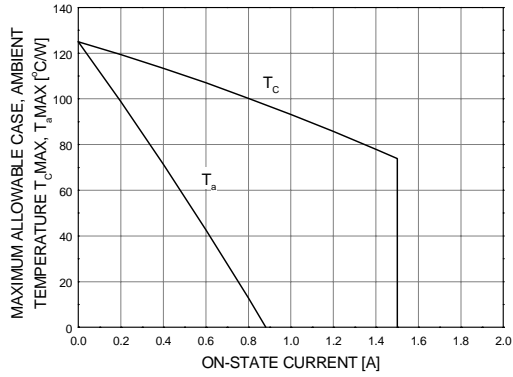


Figure 7. Allowable Case, Ambient Temperature vs Rms On-state Current

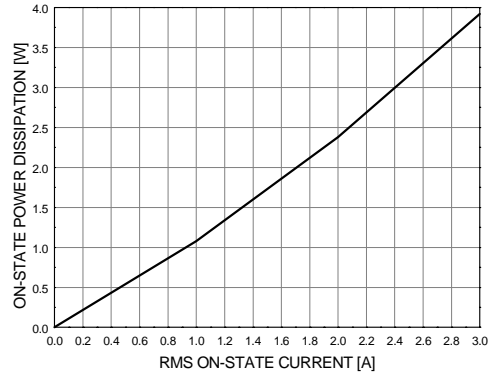


Figure 8. Maximum On-state Power Dissipation vs Rms On-state Current

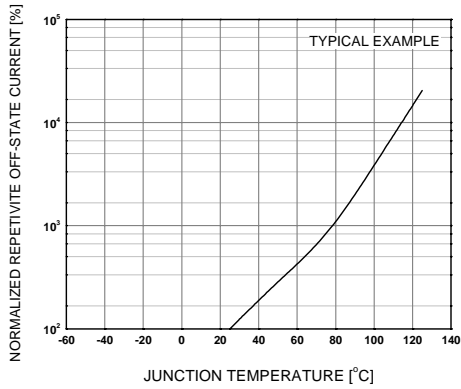


Figure 9. Repetitive Peak Off-state Current vs Junction Temperature

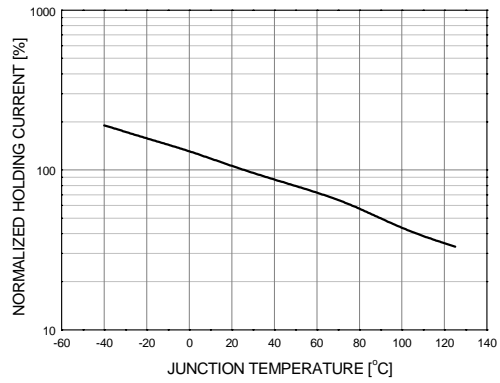


Figure 10. Holding Current vs Junction Temperature

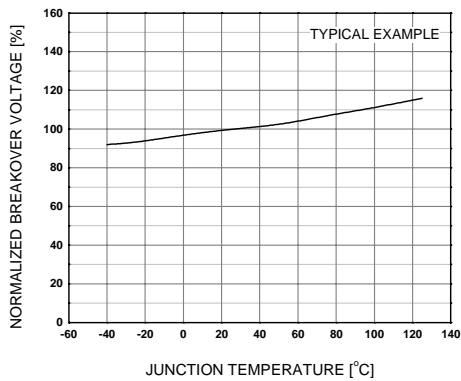


Figure 11. Breakover Voltage vs Junction Temperature

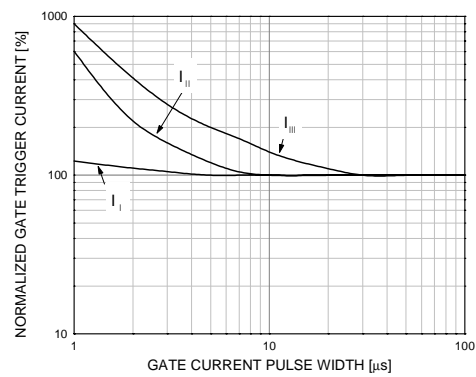


Figure 12. Gate Trigger Current vs Gate Current Pulse Width

Typical Curves (Continues)

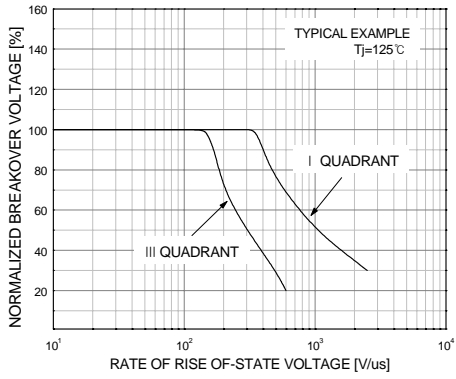
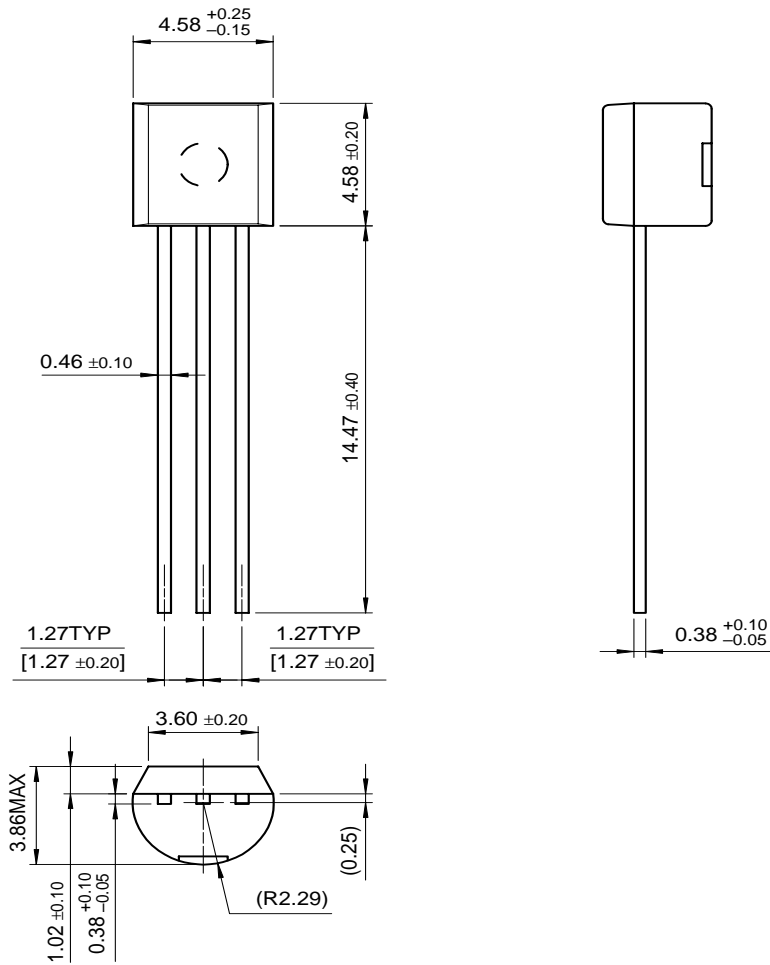


Figure 13. Breakover Voltage vs Rate of Rise of Off-state Voltage

Package Dimension

TO-92

FKN2L60



Dimensions in Millimeters

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| Bottomless™ | FASTr™ | LittleFET™ | Power247™ | SuperFET™ |
| CoolFET™ | FPST™ | MICROCOUPLER™ | PowerSaver™ | SuperSOT™-3 |
| CROSSVOLT™ | FRFET™ | MicroFET™ | PowerTrench® | SuperSOT™-6 |
| DOME™ | GlobalOptoisolator™ | MicroPak™ | QFET® | SuperSOT™-8 |
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