

3.3V / 5.0V, SINE, TCXO



Model: FOX801LF Series

RoHS Compliant

Rev. 9/19/2006

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FEATURES

- 2.0mm Height Max
- Low Cost
- Clipped Sine Output
- Tape and Reel (2,000 pcs. STD)

OPTIONS

- 'ALF' Version - 5.0V
- 'BLF' Version - 3.0V
- VCTCXO - AELF/BELF Version
- VCTCXO - AHLF/BHLF Version (trimmerless)

• PART NUMBER SELECTION [Learn More](#) - Internet Required

Part Number	Model Number	Frequency Stability	Operating Temperature (°C)	Frequency Range (MHz)
250-Frequency-xxxxx	FOX801ALF	see table	-30 ~ +75	10.000 ~ 50.000
251-Frequency-xxxxx	FOX801AELF	see table	-30 ~ +75	10.000 ~ 50.000
252-Frequency-xxxxx	FOX801AHLF	see table	-30 ~ +75	10.000 ~ 50.000
253-Frequency-xxxxx	FOX801BLF	see table	-30 ~ +75	10.000 ~ 50.000
254-Frequency-xxxxx	FOX801BELF	see table	-30 ~ +75	10.000 ~ 50.000
255-Frequency-xxxxx	FOX801BHLF	see table	-30 ~ +75	10.000 ~ 50.000

• ELECTRICAL CHARACTERISTICS

PARAMETERS	MAX (unless otherwise noted)
Frequency Range (Fo)	10.000 ~ 50.000 MHz
Temperature Range	
Operating (T _{OPR})	-30°C ~ +75°C
Storage (T _{STG})	-35°C ~ +80°C
Initial Frequency Tolerance (@25°C)	
V _c = 2.5V (ALF Series) ¹ FOX801ALF/BLF/AELF/BELF	±0.5PPM
V _c = 1.5V (BLF Series) ¹ FOX801AHLF/BHLF	±2.0PPM
Supply Voltage (V _{DD})	
ALF Version	5.0V ± 5%
BLF Version	3.0V ± 5%
Input Current (I _{DD})	2.0mA
Frequency Stability	
Over Temperature Range	±2.5PPM
Over Supply Voltage Change (V _{DD} ± 5%)	±0.3PPM
Over Load Change (10Ω ± 10% // 10pF ± 10%)	±0.3PPM
Output Waveform (Clipped Sine)	
Peak-to-Peak Level (V _{p-p})	
ALF Version: 10.000 ~ 15.999 MHz	1.0V Min
ALF Version: 16.000 ~ 50.000 MHz	0.8V Min
BLF Version: 10.000 ~ 15.999 MHz	0.8V Min
BLF Version: 16.000 ~ 50.000 MHz	0.7V Min
Output Load	10KΩ // 10pF
Frequency Adjustment (Internal Trimmer)	
FOX801ALF/BLF/AELF/BELF	±3.0PPM
FOX801AHLF/BHLF	Trimmerless
Voltage Control Option (VCTCXO) ¹	
ALF Version (V _c =2.5±2.0V) FOX801ALF/BLF	None
BLF Version (V _c =1.5±1.0V) FOX801AELF/BELF	±5.0PPM Min
FOX801AHLF/BHLF	±8.0PPM Min
Aging per year	±0.8PPM

¹ For proper operation, a control voltage (V_c) must be applied to pin 1 on VCTCXOs. All specifications subject to change without notice.

