

SANYO

FP108

 TR:PNP Epitaxial Planar Silicon Transistor
 SBD:Schottky Barrier Diode

DC-DC Converter Applications

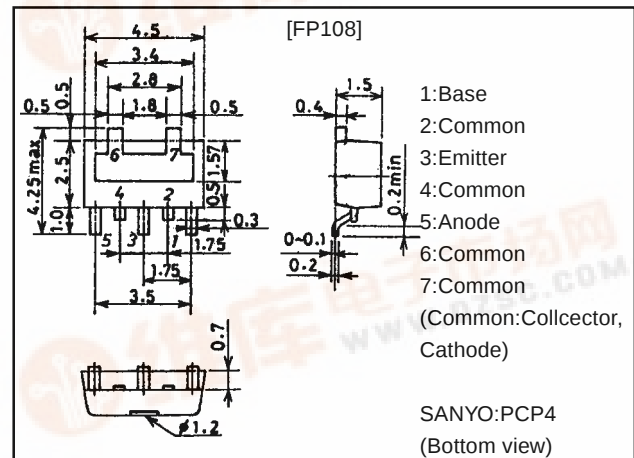
Features

- Composite type with a PNP transistor and a Schottky barrier diode contained in one package, facilitating high-density mounting.
- The FP108 is formed with 2 chips, one being equivalent to the 2SB1121 and the other the SB01-015CP, placed in one package.

Package Dimensions

unit:mm

2088A



Specifications

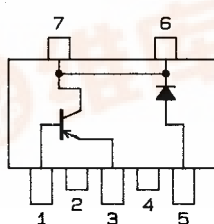
Absolute Maximum Ratings at Ta = 25°C

Parameter	Symbol	Conditions	Ratings	Unit
[TR]				
Collector-to-Base Voltage	V_{CBO}		-30	V
Collector-to-Emitter Voltage	V_{CEO}		-25	V
Emitter-to-Base Voltage	V_{EBO}		-6	V
Collector Current	I_C		-2	A
Collector Current (Pulse)	I_{CP}		-5	A
Base Current	I_B		-400	mA
Collector Dissipation	P_C	Mounted on ceramic board (250mm ² ×0.8mm)	1.3	W
Junction Temperature	T_J		150	°C
Storage Temperature	T_{stg}		-55 to +150	°C
[SBD]				
Repetitive Peak Reverse Voltage	V_{RRM}		15	V
Non-repetitive Peak Reverse Surge Voltage	V_{RSM}		17	V
Average Rectified Current	I_O		1	A
Surge Forward Current	I_{FSM}	50Hz sine wave, 1cycle	8	A
Junction Temperature	T_J		-55 to +125	°C
Storage Temperature	T_{stg}		-55 to +125	°C

Marking:108

Continued on next page.

Electrical Connection



1:Base
 2:Common
 3:Emitter
 4:Common
 5:Anode
 6:Common
 7:Common
 (Common:Collector, Cathode)

FP108

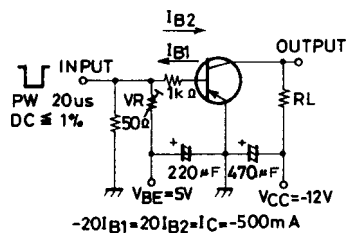
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Electrical Characteristics at Ta=25°C

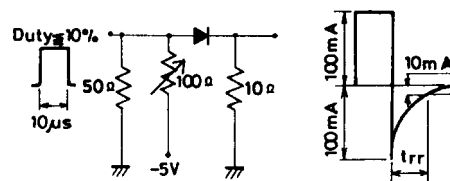
.Parameter	Symbol	Conditons	Ratings			Unit
			min	typ	max	
[TR]						
Collector Cutoff Current	I _{CBO}	V _{CB} =−20V, I _E =0			−0.1	μA
Emitter Cutoff Current	I _{EBO}	V _{EB} =−4V, I _C =0			−0.1	μA
DC Current Gain	h _{FE1}	V _{CE} =−2V, I _C =−100mA	140		400	
	h _{FE2}	V _{CE} =−2V, I _C =−1.5A	65			
Gain-Bandwidth Product	f _T	V _{CE} =−10V, I _C =−50mA		150		MHz
Output Capacitance	Cob	V _{CE} =−10V, f=1MHz		32		pF
C-E Saturation Voltage	V _{CE(sat)}	I _C =−1.5A, I _B =−75mA		−0.35	−0.6	V
B-E Saturation Voltage	V _{BE(sat)}	I _C =−1.5A, I _B =−75mA		−0.85	−1.2	V
C-B Breakdown Voltage	V _{(BR)CBO}	I _C =−10μA, I _E =0	−30			V
C-E Breakdown Voltage	V _{(BR)CEO}	I _C =−1mA, R _{BE} =∞	−25			V
E-B Breakdown Voltage	V _{(BR)EBO}	I _E =−10μA, I _C =0	−6			V
Turn-ON Time	t _{on}	See specified Test Circuit		60		ns
Storage Time	tstg	See specified Test Circuit		350		ns
Fall Time	tf	See specified Test Circuit		25		ns
[SBD]						
Reverse Voltage	V _R	I _R =300μA	15			V
Forward Voltage	V _F	I _F =1A			0.55	V
Reverse Current	I _R	V _R =7.5V			50	μA
Interterminal Capacitance	C	V _R =10V, f=1MHz		30		pF
Reverse Recovery Time	t _{rr}	I _F =I _R =100mA, See specified Test Circuit			10	ns
Thermal Resistance	Rthj-a	Mounted on ceramic board (250mm²×0.8mm)		120		°C/W

Switching Time Test Circuit

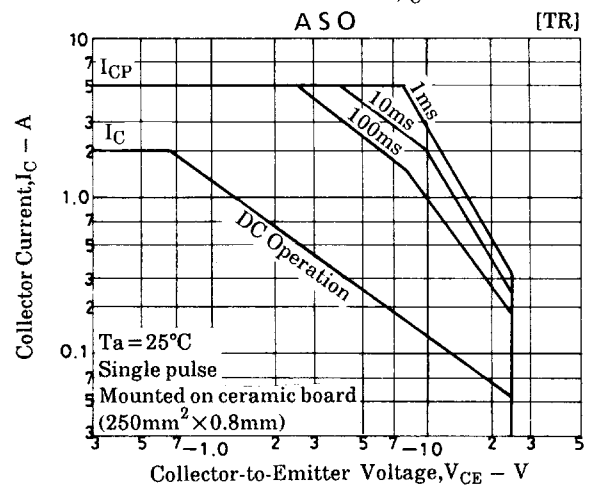
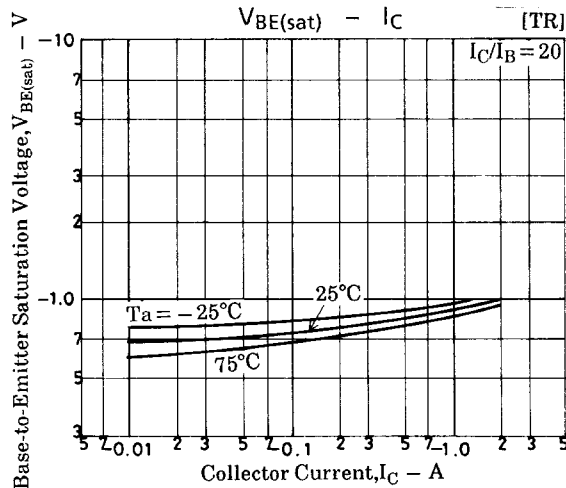
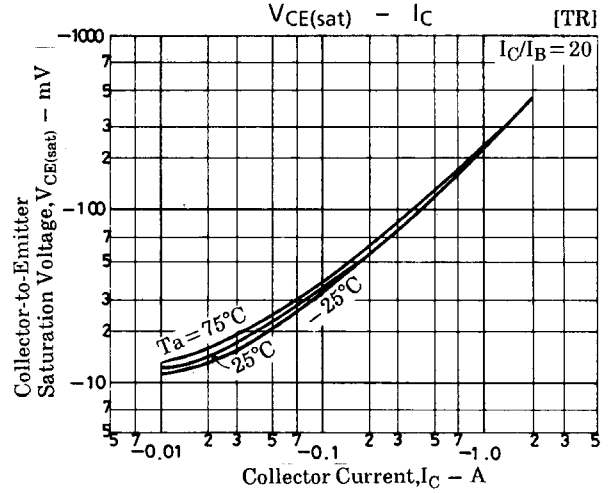
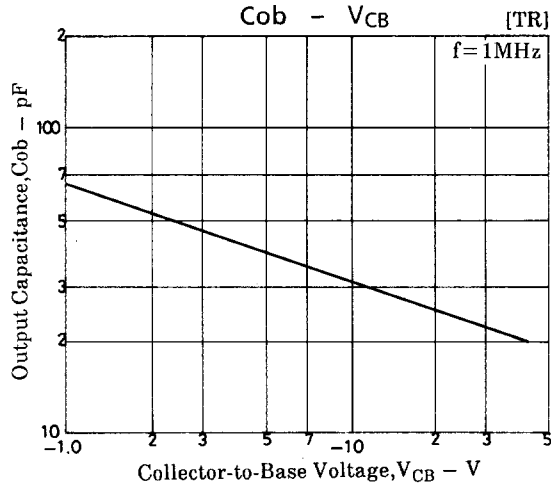
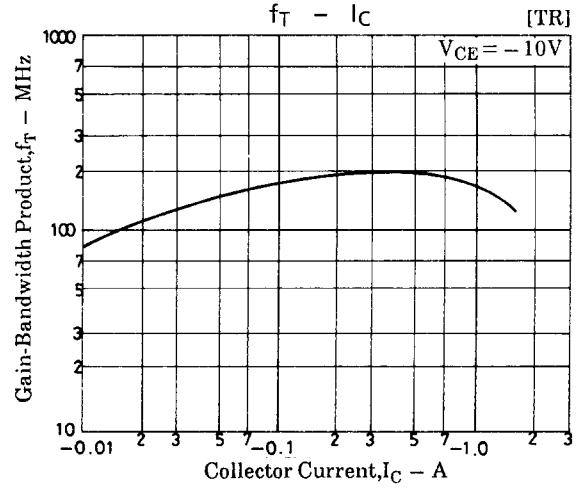
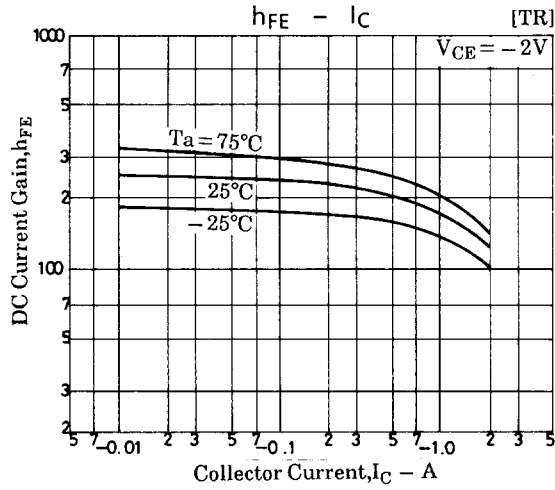
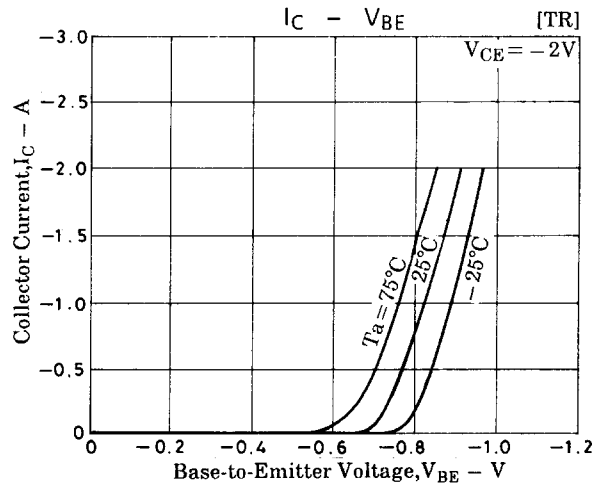
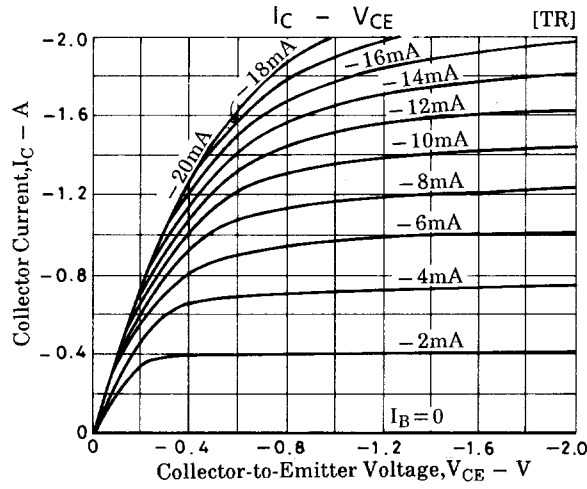
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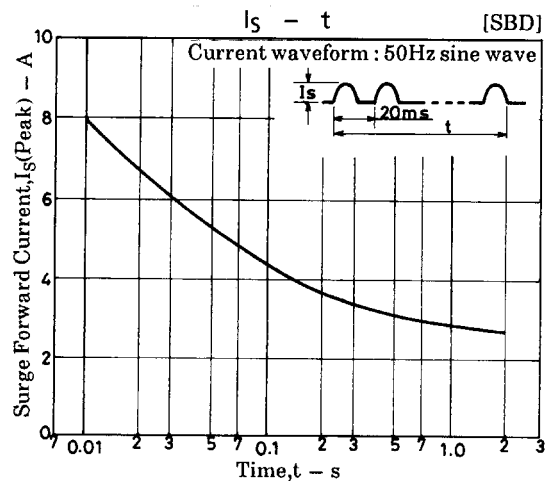
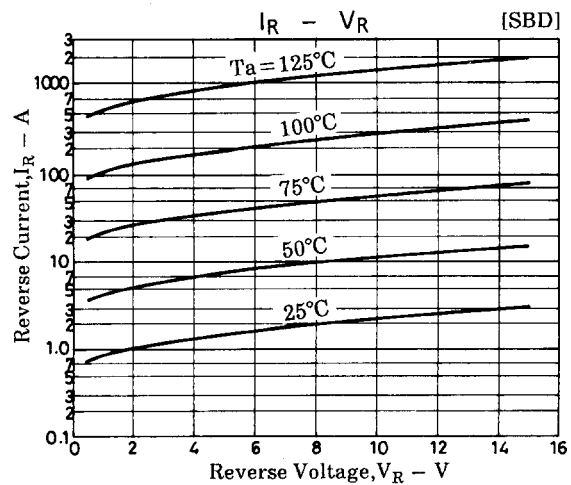
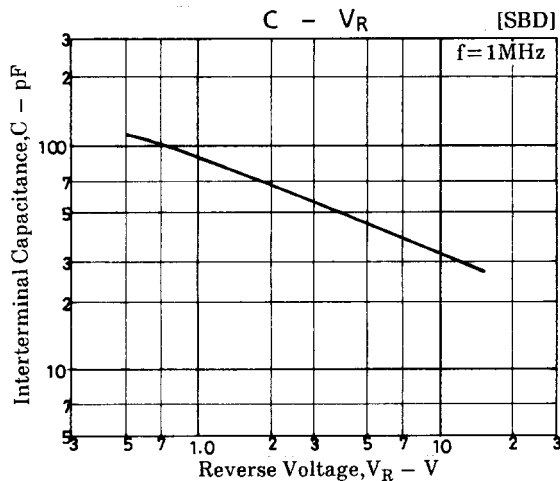
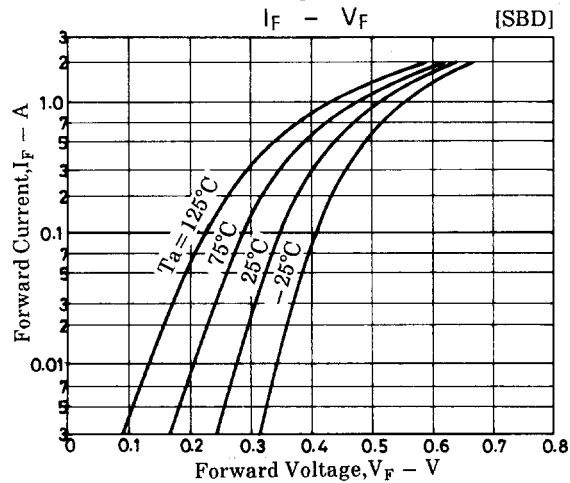
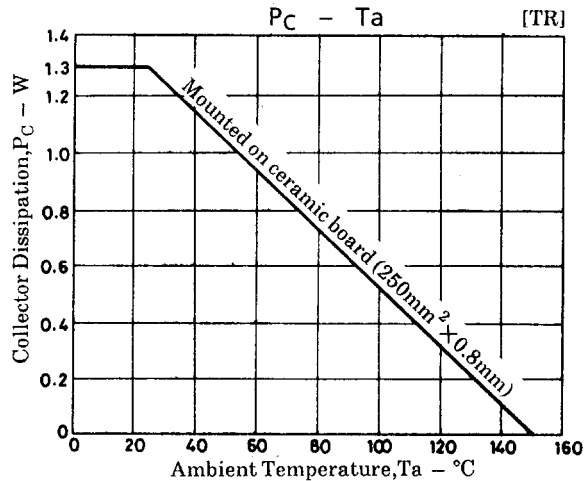


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