

MITSUBISHI Nch POWER MOSFET

# FS50KMJ-2

HIGH-SPEED SWITCHING USE

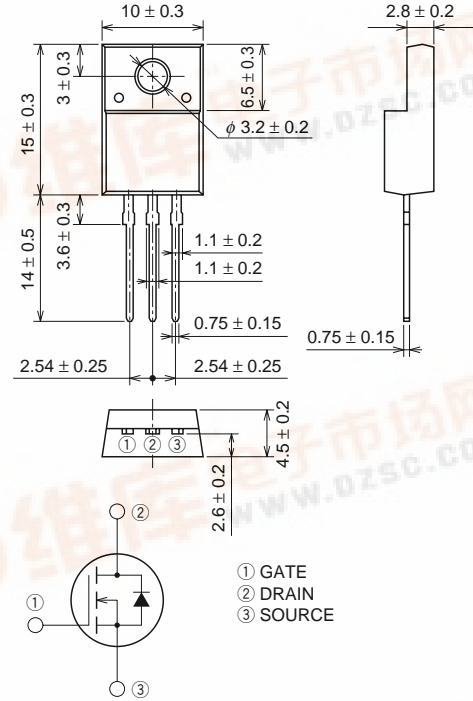
## FS50KMJ-2



- 4V DRIVE
- V<sub>DSS</sub> ..... 100V
- r<sub>DS (ON)</sub> (MAX) ..... 48mΩ
- I<sub>D</sub> ..... 50A
- Integrated Fast Recovery Diode (TYP.) ..... 90ns
- V<sub>iso</sub> ..... 2000V

## OUTLINE DRAWING

Dimensions in mm



TO-220FN

## APPLICATION

Motor control, Lamp control, Solenoid control  
DC-DC converter, etc.

## MAXIMUM RATINGS (T<sub>c</sub> = 25°C)

Symbol	Parameter	Conditions	Ratings	Unit
V <sub>DSS</sub>	Drain-source voltage	V <sub>GS</sub> = 0V	100	V
V <sub>GSS</sub>	Gate-source voltage	V <sub>DS</sub> = 0V	±20	V
I <sub>D</sub>	Drain current		50	A
I <sub>DM</sub>	Drain current (Pulsed)		200	A
I <sub>DA</sub>	Avalanche drain current (Pulsed)	L = 50μH	50	A
I <sub>S</sub>	Source current		50	A
I <sub>SM</sub>	Source current (Pulsed)		200	A
P <sub>D</sub>	Maximum power dissipation		30	W
T <sub>ch</sub>	Channel temperature		-55 ~ +150	°C
T <sub>stg</sub>	Storage temperature		-55 ~ +150	°C
V <sub>iso</sub>	Isolation voltage	AC for 1minute, Terminal to case	2000	V
—	Weight	Typical value	2.0	g



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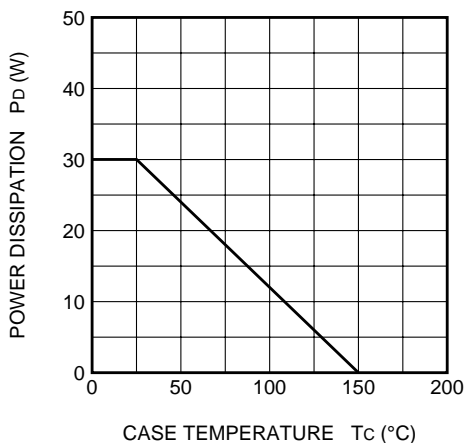
HIGH-SPEED SWITCHING USE

## ELECTRICAL CHARACTERISTICS (Tch = 25°C)

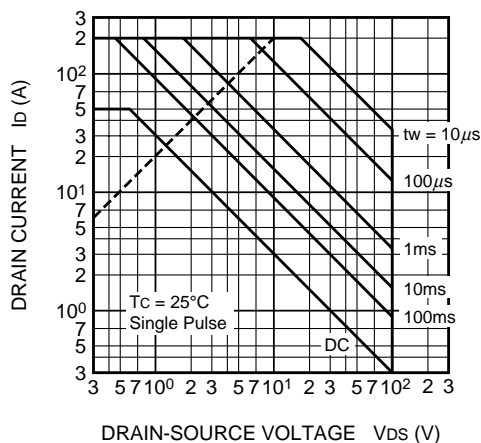
Symbol	Parameter	Test conditions	Limits			Unit
			Min.	Typ.	Max.	
V(BR)DSS	Drain-source breakdown voltage	Id = 1mA, VGS = 0V	100	—	—	V
IGSS	Gate-source leakage current	VGS = ±20V, VDS = 0V	—	—	±0.1	μA
IDSS	Drain-source leakage current	VDS = 100V, VGS = 0V	—	—	0.1	mA
VGS(th)	Gate-source threshold voltage	Id = 1mA, VDS = 10V	1.0	1.5	2.0	V
rDS(ON)	Drain-source on-state resistance	Id = 25A, VGS = 10V	—	37	48	mΩ
rDS(ON)	Drain-source on-state resistance	Id = 25A, VGS = 4V	—	40	52	mΩ
VDS(ON)	Drain-source on-state voltage	Id = 25A, VGS = 10V	—	0.93	1.20	V
yfs	Forward transfer admittance	Id = 25A, VDS = 10V	—	40	—	S
Ciss	Input capacitance	VDS = 10V, VGS = 0V, f = 1MHz	—	3000	—	pF
Coss	Output capacitance		—	410	—	pF
Crss	Reverse transfer capacitance		—	210	—	pF
td(on)	Turn-on delay time	VDD = 50V, Id = 25A, VGS = 10V, RGEN = RGS = 50Ω	—	22	—	ns
tr	Rise time		—	65	—	ns
td(off)	Turn-off delay time		—	270	—	ns
tf	Fall time		—	160	—	ns
VSD	Source-drain voltage	Is = 25A, VGS = 0V	—	1.0	1.5	V
Rth(ch-c)	Thermal resistance	Channel to case	—	—	4.17	°C/W
trr	Reverse recovery time	Is = 50A, dis/dt = -100A/μs	—	90	—	ns

## PERFORMANCE CURVES

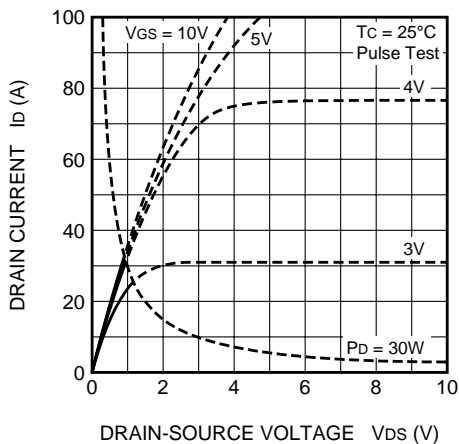
POWER DISSIPATION DERATING CURVE



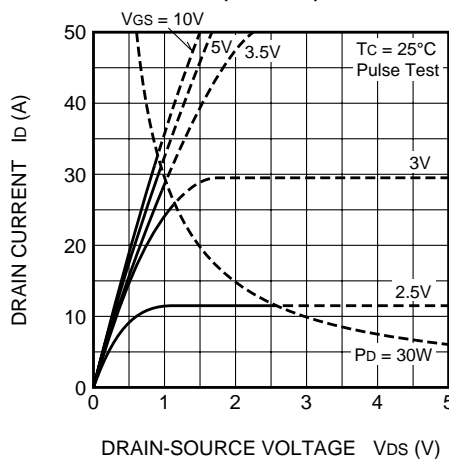
MAXIMUM SAFE OPERATING AREA



OUTPUT CHARACTERISTICS (TYPICAL)

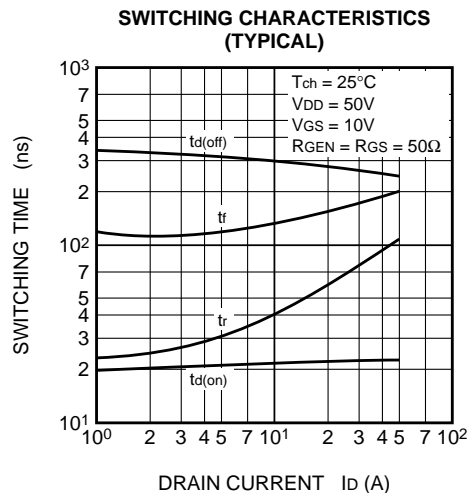
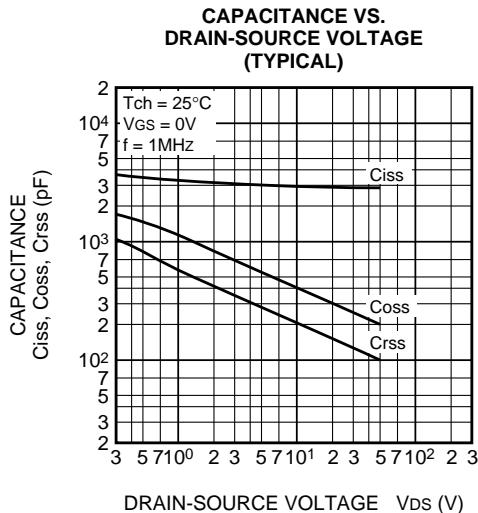
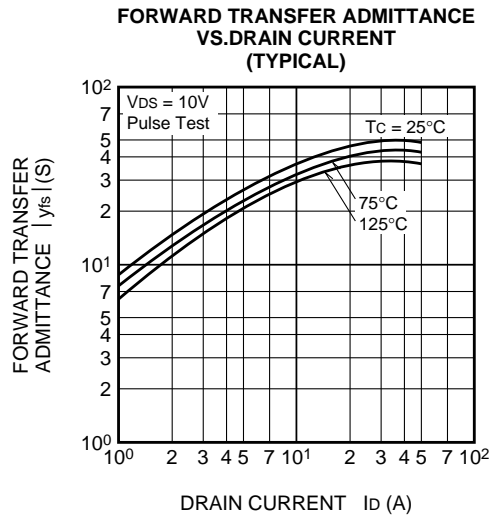
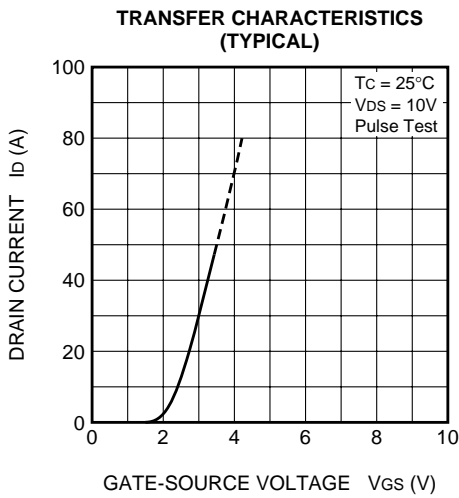
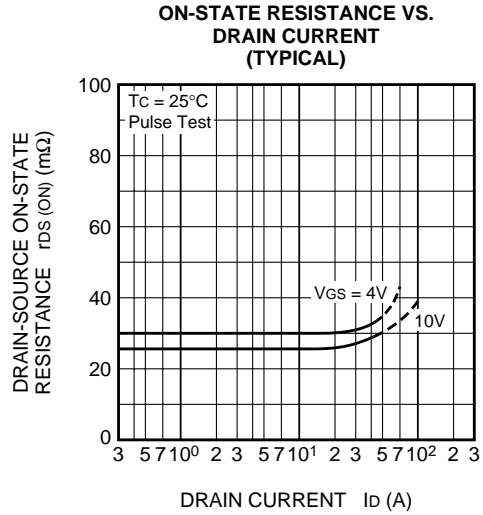
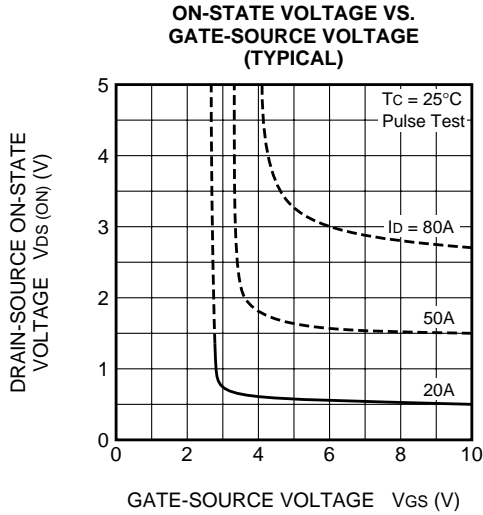


OUTPUT CHARACTERISTICS (TYPICAL)



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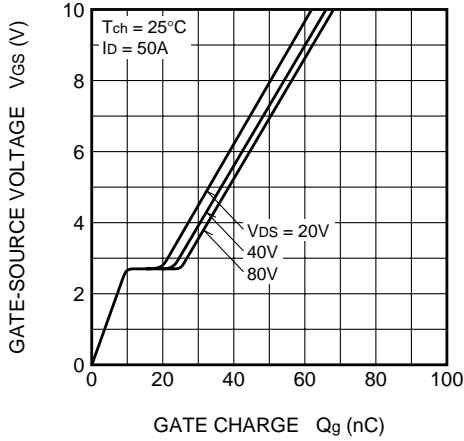
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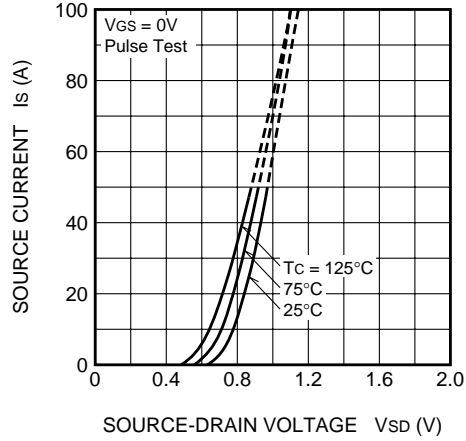
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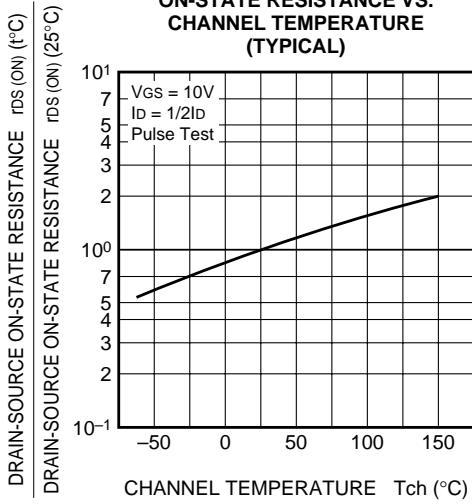
**GATE-SOURCE VOLTAGE VS. GATE CHARGE (TYPICAL)**



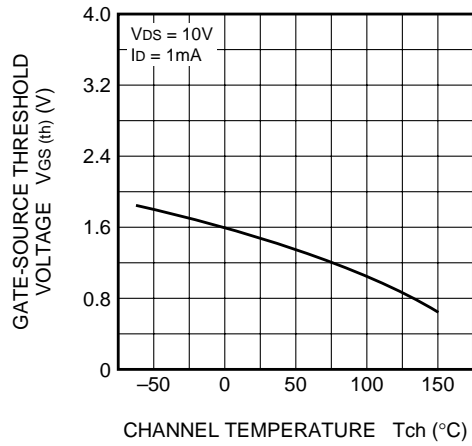
**SOURCE-DRAIN DIODE FORWARD CHARACTERISTICS (TYPICAL)**



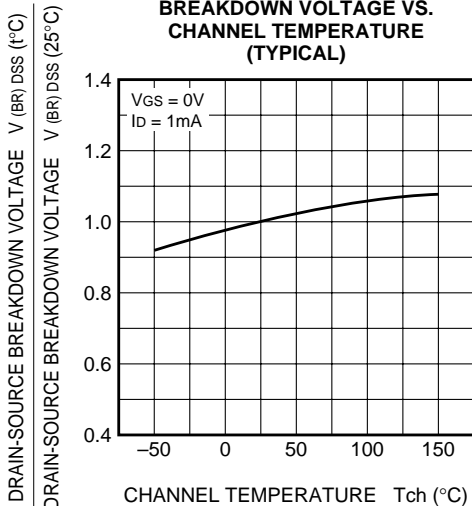
**ON-STATE RESISTANCE VS. CHANNEL TEMPERATURE (TYPICAL)**



**THRESHOLD VOLTAGE VS. CHANNEL TEMPERATURE (TYPICAL)**



**BREAKDOWN VOLTAGE VS. CHANNEL TEMPERATURE (TYPICAL)**



**TRANSIENT THERMAL IMPEDANCE CHARACTERISTICS**

