

THYRISTOR MODULE (SINGLE PHASE BRIDGE TYPE)**FSD20A30/60**

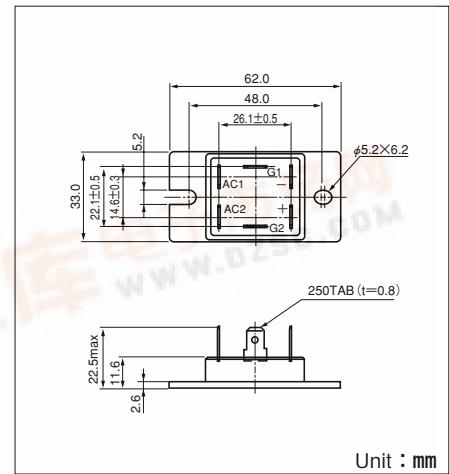
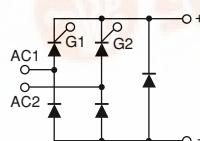
UL:E76102(M)

FSD20A is a single phase bridge module consist of thyristors and diodes

- $I_D=20A$, $V_{RRM}=600V$
- Easy Construction
- Highly reliable glass passivated chips

(Applications)

Rectification (Bridge)
Motor Drive



Unit : mm

(T_j=25°C unless otherwise specified)**■Maximum Ratings**

| Symbol | Item | Ratings | | Unit |
|------------------|-----------------------------------|----------|----------|------|
| | | FSD20A30 | FSD20A60 | |
| V _{RRM} | Repetitive Peak Reverse Voltage | 300 | 600 | V |
| V _{DRM} | Repetitive Peak Off-State Voltage | 300 | 600 | V |

| Symbol | Item | Conditions | Ratings | Unit |
|--------------------|--------------------------------------|--|-------------|------------------|
| I _{T(AV)} | Average On-State Current | Single phase, half wave, 180° conduction, T _c : 65°C | 20 | A |
| I _{TSM} | Surge On-State Current | ½cycle, 50Hz/60Hz, peak value, non-repetitive | 180/200 | A |
| I ² t | I ² t | | 165 | A ² S |
| P _{GM} | Peak Gate Power Dissipation | | 10 | W |
| P _{G(AV)} | Average Gate Power Dissipation | | 1 | W |
| I _{FGM} | Peak Gate Current | | 3 | A |
| V _{FGM} | Peak Gate Voltage (Forward) | | 10 | V |
| V _{RGM} | Peak Gate Voltage (Reverse) | | 5 | V |
| dI/dt | Critical Rate of On-State Current | I _G =100mA, T _j =25°C, V _D =½V _{DRM} , dI/dt=1A/μs | 100 | A/μs |
| V _{iso} | Isolation Breakdown Voltage (R.M.S.) | A.C.1minute | 2500 | V |
| T _j | Operating Junction Temperature | | -30 to +125 | °C |
| T _{stg} | Storage Temperature | | -30 to +125 | °C |
| | Mounting Torque (M5) | Recommended Value 1.5-2.5 (15-25) | 2.7 (28) | N·m (kgf·cm) |
| | Mass | | 66 | g |

■Electrical Characteristics

| Symbol | Item | Conditions | Ratings | Unit |
|----------------------------------|--|---|---------|------|
| I _{DRM} | Repetitive Peak Off-State Current, max. | at V _{DRM} , single phase, half wave, T _j =125°C | 5 | mA |
| I _{RRM} | Repetitive Peak Reverse Current, max. | at V _{DRM} , single phase, half wave, T _j =125°C | 5 | mA |
| V _{TM} | Peak On-State Voltage, max. | On-State Current 30A, T _j =25°C Inst. measurement | 1.5 | V |
| I _{GT} /V _{GT} | Gate Trigger Current/Voltage, max. | T _j =25°C, I _T =1A, V _D =6V | 40/1.2 | mA/V |
| V _{GD} | Non-Trigger Gate, Voltage. min. | T _j =125°C, V _D =½V _{DRM} | 0.2 | V |
| t _{gt} | Turn On Time, max. | I _T =10A, I _G =100mA, T _j =25°C, V _D =½V _{DRM} , dI/dt=1A/μs | 10 | μs |
| dV/dt | Critical Rate of Rise of Off-State Voltage, min. | T _j =125°C, V _D =¾V _{DRM} , Exponential wave. | 50 | V/μs |
| I _H | Holding Current, typ. | T _j =25°C | 30 | mA |
| R _{th(j-c)} | Thermal Impedance, max. | Junction to case | 1.0 | °C/W |

