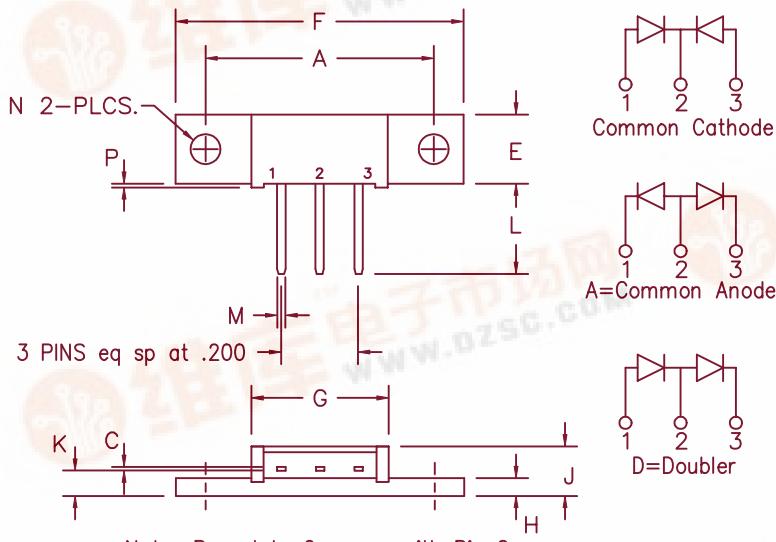


FST6435 — FST6450



Dim.	Inches		Millimeter		Notes
	Minimum	Maximum	Minimum	Maximum	
A	1.180	1.195	29.97	30.35	
C	.025	.035	0.64	0.89	
E	.350	.370	8.89	9.40	
F	1.490	1.510	37.85	38.35	
G	.695	.715	17.65	18.16	
H	.088	.098	2.24	2.49	
J	.240	.260	6.10	6.60	
K	.115	.135	2.92	3.43	
L	.460	.480	11.68	12.19	
M	.034	.046	0.86	1.17	
N	.151	.161	3.84	4.09	
P	.015	.025	0.38	0.64	Dia.

Note: Baseplate Common with Pin 2

Microsemi Catalog Number	Industry Part Number	Working Peak Reverse Voltage	Repetitive Peak Reverse Voltage
FST6435*	61CNQ035	35V	35V
FST6440*	61CNQ040	40V	40V
FST6445*	61CNQ045	45V	45V
FST6450*		50V	50V

*Add the Suffix A for Common Anode, D for Doubler

- Schottky Barrier Rectifier
- Guard Ring Protection
- 2X30 Amperes avg.
- 175°C Junction Temperature
- Reverse Energy Tested
- V_{RRM} – 35 to 50 Volts

Electrical Characteristics

Average forward current per pkg
 Average forward current per leg
 Maximum surge current per leg
 Max repetitive peak reverse current per leg
 Max peak forward voltage per leg
 Max peak forward voltage per leg
 Max peak reverse current per leg
 Max reverse current per leg
 Typical junction capacitance per leg

| F(AV) 60 Amps
 | F(AV) 30 Amps
 | FSM 800 Amps
 | R(OV) 2 Amps
 | VFM 0.46 Volts
 | VFM 0.65 Volts
 | RM 45 mA
 | RM 2 mA
 | CJ 1900 pF

T_C = 156°C, Square wave, R_{θJC} = 0.5°C/W
 T_C = 156°C, Square wave, R_{θJC} = 1.0°C/W
 8.3 ms, half sine, T_J = 175°C
 f = 1 KHZ, 25°C, 1 usec square wave
 | FM = 30A: T_J = 175°C*
 | FM = 30A: T_J = 25°C*
 V_{RRM}, T_J = 125°C*
 V_{RRM}, T_J = 25°C
 V_R = 5.0V, T_C = 25°C

*Pulse test: Pulse width 300 usec, Duty cycle 2%

Thermal and Mechanical Characteristics

Storage temp range	T _{STG}	-55°C to 175°C
Operating junction temp range	T _J	-55°C to 175°C
Max thermal resistance per leg	R _{θJC}	1.0°C/W Junction to case
Max thermal resistance per pkg	R _{θJC}	0.5°C/W Junction to case
Typical thermal resistance (greased)	R _{θCS}	0.3°C/W Case to sink
Mounting Base Torque		10 inch pounds maximum
Weight		0.3 ounce (8.4 grams) typical

FST6435 – FST6450

Figure 1
Typical Forward Characteristics – Per Leg

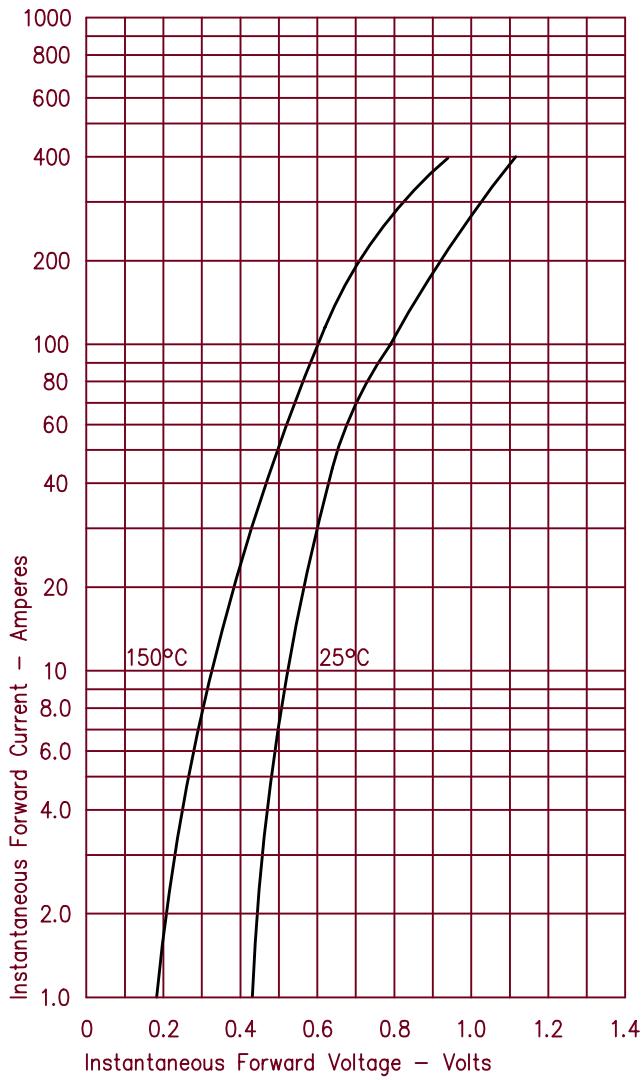


Figure 3
Typical Junction Capacitance – Per Leg

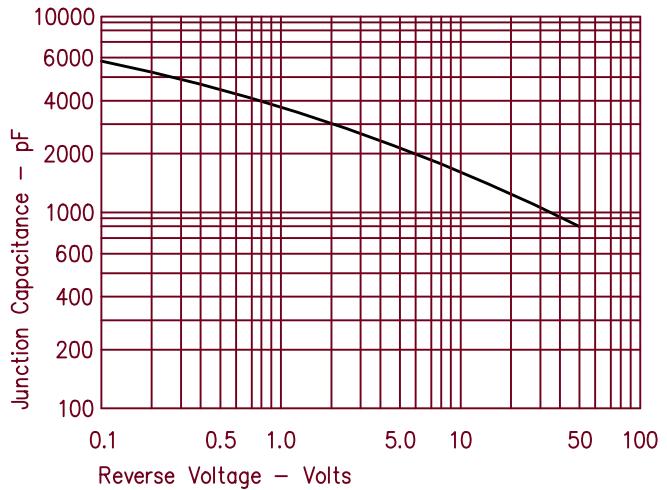


Figure 4
Forward Current Derating – Per Leg

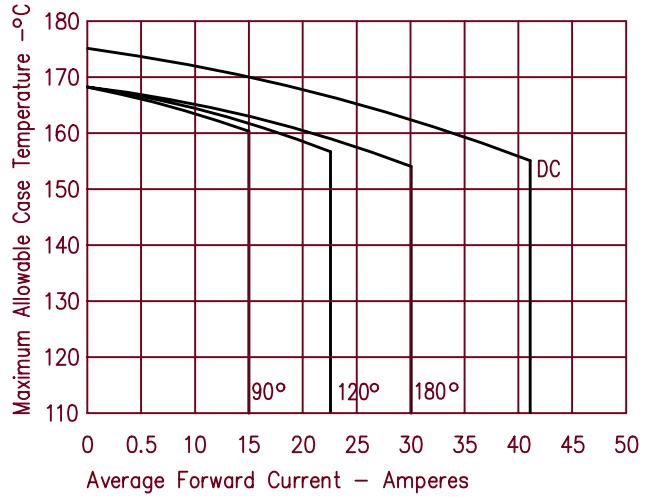


Figure 2
Typical Reverse Characteristics – Per Leg

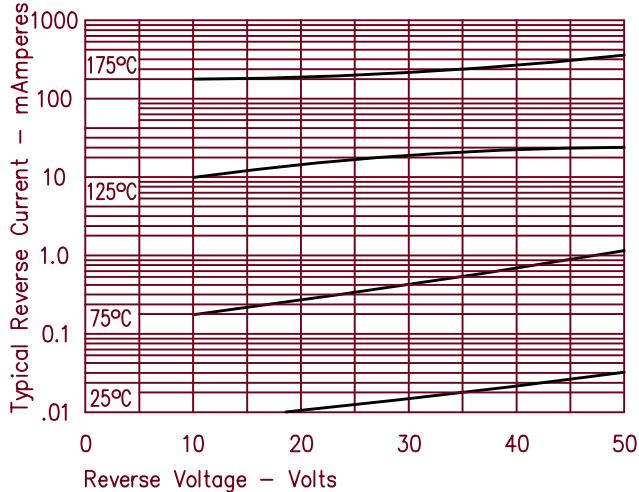


Figure 5
Maximum Forward Power Dissipation – Per Leg

