P-Channel Silicon MOSFET





## **Ultrahigh-Speed Switching Applications**

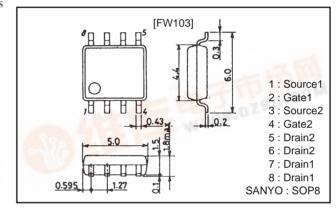
#### **Features**

- Low ON resistance
- Ultrahigh-speed switching.
- Composite type with two 4V-drive P-channel MOSFETs facilitating high-density mounting.
- Matched pair capability.

# Package Dimensions www.pzsc.com

unit: mm

### 2129-SOP8



## **Specifications**

## Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	VDSS		-30	V
Gate-to-Source Voltage	VGSS		±20	V
Drain Current (DC)	ID		-3	Α
Drain Current (pulse)	IDP	PW≤10µs, duty cycle≤1%	-32	Α
Allowable Power Dissipation	PD	Mounted on a ceramic board (1000mm <sup>2</sup> ×0.8mm) 1unit	1.7	W
Total Dissipation	PT	Mounted on a ceramic board (1000mm <sup>2</sup> ×0.8mm)	2.0	W
Channel Temperature	Tch		150	°C
Storage temperature	Tstg		-55 to +150	°C

### Electrical Characteristics at Ta=25°C

Parameter	Symbol	Conditions		Ratings		
			min	typ	max	Unit
D-S Breakdown Voltage	V(BR)DSS	ID=-1mA, VGS=0	-30	4		V
Zero-Gate-Voltage Drain Current	IDSS	V <sub>DS</sub> =-30V, V <sub>GS</sub> =0			-100	μΑ
Gate-to-Source Leakage Current	IGSS	VGS=±16V, VDS=0			±10	μA
Cutoff Voltage	VGS(off)	V <sub>DS</sub> =-10V, I <sub>D</sub> =-1mA	-1.0		-2.5	V
Forward Transfer Admittance	ly <sub>fs</sub>	V <sub>DS</sub> =-10V, I <sub>D</sub> =-3A	2	4		S
Static Drain-to-Source	RDS(on)	I <sub>D</sub> =-3A, V <sub>G</sub> S=-10V		95	125	mΩ
ON-State Resistance	RDS(on)	ID=-3A, VGS=-4V		150	205	mΩ
Input Capacitance	Ciss	V <sub>DS</sub> =-10V, f=1MHz		550		pF
Output Capacitance	Coss	V <sub>DS</sub> =-10V, f=1MHz		370		pF
Reverse Transfer Capacitance	Crss	V <sub>DS</sub> =-10V, f=1MHz		70		pF

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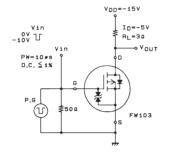
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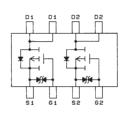
Parameter	Symbol	Conditions	Ratings			- Unit
	Symbol		min	typ	max	
Turn-ON Delay Time	td(on)	See specified Test Circuit.		20		ns
Rise Time	t <sub>r</sub>	"		110		ns
Turn-OFF Delay Time	td(off)	"		330		ns
Fall Time	tf	"		170		ns
Diode Forward Voltage	V <sub>SD</sub>	I <sub>S</sub> =-3A, V <sub>G</sub> S=0		-1.0	-1.2	V

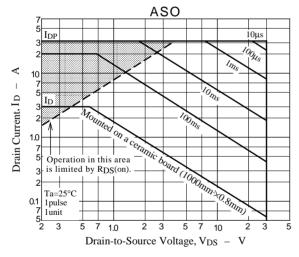
## **Switching Time Test Circuit**

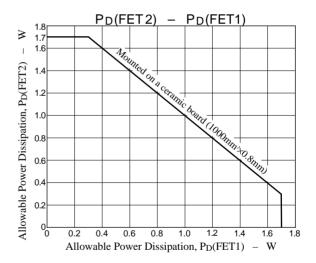
## **Electrical Connection**

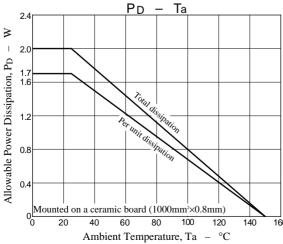
(Top view)











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