Ordering number : ENN6784

P-Channel Silicon MOSFET

**FW133** 



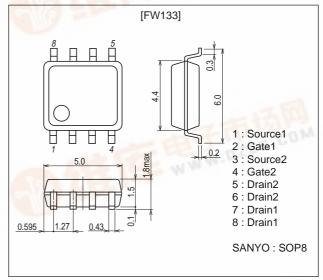
# **Load Switching Applications**

### **Features**

- · 4V drive.
- · Low ON-resistance.

## **Package Dimensions**

unit : mm 2129



## **Specifications**

Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	VDSS		-30	V
Gate-to-Source Voltage	VGSS		±20	V
Drain Current (DC)	ΙD	- 12 Table	7	Α
Drain Current (Pulse)	IDP	PW≤10μs, duty cycle≤1%	-32	А
Allowable Power Dissipation	PD	Mounted on a ceramic board (1000mm²X0.8mm) 1unit	1.7	W
Total Dissipation	PT	Mounted on a ceramic board (1000mm²X0.8mm)	2.0	W
Channel Temperature	Tch		150	°C
Storage Temperature	Tstg	COM	-55 to +150	°C

#### Electrical Characteristics at Ta=25°C

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	Offic
Drain-to-Source Breakdown Voltage	V(BR)DSS	ID=-1mA, VGS=0	-30		-55 17	V
Zero-Gate Voltage Drain Current	IDSS	V <sub>DS</sub> =-30V, V <sub>GS</sub> =0		2	-1	μΑ
Gate-to-Source Leakage Current	IGSS	V <sub>GS</sub> =±16V, V <sub>DS</sub> =0			±10	μΑ
Cutoff Voltage	VGS(off)	V <sub>DS</sub> =-10V, I <sub>D</sub> =-1mA	-1.0	NAL.	-2.4	V
Forward Transfer Admittance	yfs	V <sub>DS</sub> =-10V, I <sub>D</sub> =-7A	8.4	12		S

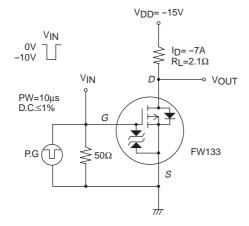
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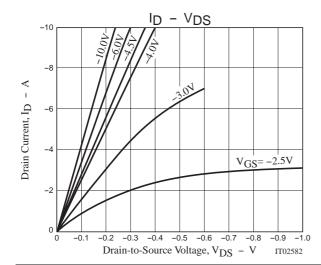
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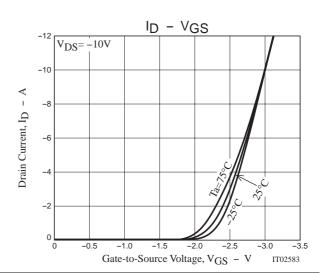
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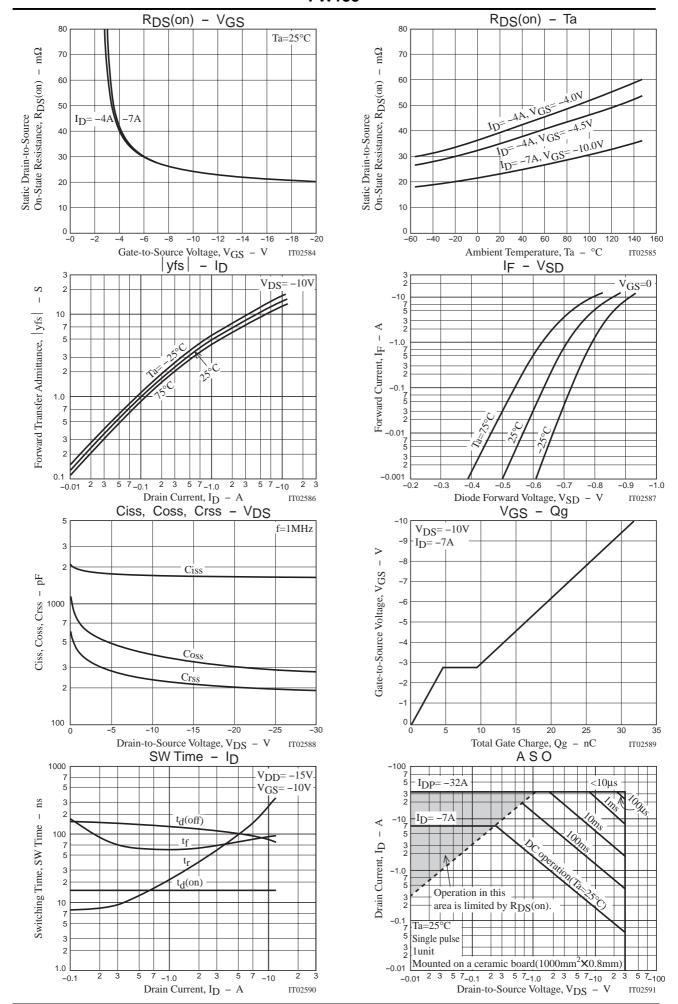
Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	Oill
Static Drain-to-Source On-State Resistance	RDS(on)1	ID=-7A, VGS=-10V		24	32	mΩ
	R <sub>DS</sub> (on)2	I <sub>D</sub> =-4A, V <sub>G</sub> S=-4.5V		36	51	mΩ
	RDS(on)3	I <sub>D</sub> =-4A, V <sub>G</sub> S=-4V		40	56	mΩ
Input Capacitance	Ciss	V <sub>DS</sub> =-10V, f=1MHz		1700		pF
Output Capacitance	Coss	V <sub>DS</sub> =-10V, f=1MHz		380		pF
Reverse Transfer Capacitance	Crss	V <sub>DS</sub> =-10V, f=1MHz		240		pF
Turn-ON Delay Time	t <sub>d</sub> (on)	See specified Test Circuit		15		ns
Rise Time	t <sub>r</sub>	See specified Test Circuit		150		ns
Turn-OFF Delay Time	t <sub>d</sub> (off)	See specified Test Circuit		85		ns
Fall Time	tf	See specified Test Circuit		90		ns
Total Gate Charge	Qg	V <sub>DS</sub> =-10V, V <sub>GS</sub> =-10V, I <sub>D</sub> =-7A		32		nC
Gate-to-Source Charge	Qgs	V <sub>DS</sub> =-10V, V <sub>GS</sub> =-10V, I <sub>D</sub> =-7A		4.5		nC
Gate-to-Drain "Miller" Charge	Qgd	V <sub>DS</sub> =-10V, V <sub>GS</sub> =-10V, I <sub>D</sub> =-7A		5		nC
Diode Forward Voltage	V <sub>SD</sub>	I <sub>S</sub> =-7A, V <sub>G</sub> S=0		-1.0	-1.5	V

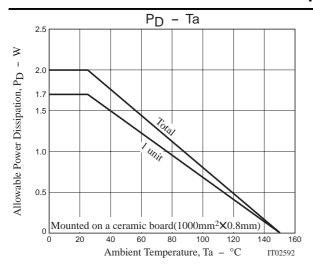
## **Switching Time Test Circuit**











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