SCRs

查询GB301A供应商

Commercial Nanosecond Switching

GB300 PCB GB300A 24小时加急出货 **GA300** 捷名≥300A GA301 GB301 GA301A **GB301A**

FEATURES

- Rise Time: 10ns
- Delay Time: 10ns
- Recovery Time: 0.5µs
- Pulse Current: to 100A
- · Turn-on with 20ns, 10mA gate pulse

DESCRIPTION

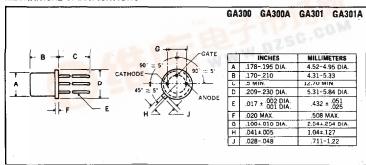
The Microsemi Nanosecond Thyristor Switch combines the turn-on speed of logic level transistors with the high current switching capability inherent in SCRs. With this device engineers can now design circuits capable of switching pulse currents of 1A in less than 10ns or up to 30A in less than 20ns.

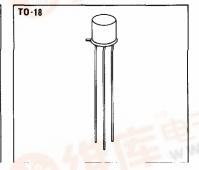
WWW.DZSG.COM The GA300, GB300 Series is specifically designed for use as switching element in high speed laser diode pulse drivers. Other applications include electronic crowbars, harmonic wave-form generators, line drivers and general purpose replacements for avalanche transistors. For applications requiring higher voltage levels, Microsemi has developed several "series string" circuits which allow the series connection of an unlimited number of devices for voltages as high as 2000V with no significant decrease in speed. The circuits are described in Microsemi's Design Note #14.

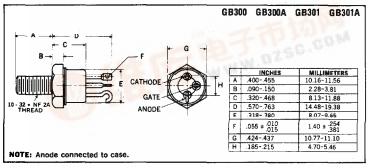
ABSOLUTE MAXIMUM RATINGS

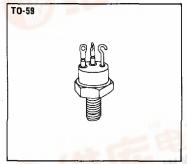
The circ	cuits are described in Mid	crosemi's Design Note #	14.	
ABSOLUTE MAXIMUM RATINGS				
	GA300	GA301 GA301A	GB300 CB300A	GB301 GD301A
Repetitive Peak Off-State Voltage, VDRM	60V	100V	60V	100V
Repetitive Peak On-State Current, ITRM	up to 100A		up to 100A	
Peak Gate Current, I _{GM}		0mA	250mA	
Average Gate Current, IGIAVI	25	mA	50mA	
Reverse Gate Current, I _{GR}	3 <i>i</i>	πA	3mA	
Reverse Gate Voltage, V _{GR}		SV		5V
Storage Temperature Range		—65°C t	o +150°C	
Operating Temperature Range	0°C to +125°C			

MECHANICAL SPECIFICATIONS









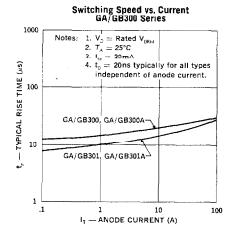


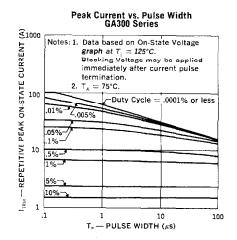




ELECTRICAL SPECIFICATIONS (at 25°C unless noted)

Test	Symbol	Min.	Typical	Max.	Units	Test Conditions
Nelay Time	t _a	_	20 10	30	ns	$I_{\rm G}=20$ mA, $I_{\rm T}=1$ A $I_{\rm G}=3$ UmA, $I_{\rm T}=1$ A
Rise Time (Note 1) GA300, 300A, GB300, 300A	t,	-	15 25	25 —	ns	$V_D = 60V, I_T = 1A$ $V_D = 60V, I_T = 30A$ (Note 1)
Rise Time (Note 1) GA301, 301A, GB301, 301A	t,	_	10 20	20	ns	$V_D = 100V, I_T = 1A$ (Note 1)
Circuit Commutated Turn-off Time GA300, 301, GB300, 301	t _q		0.8	2.0	μS	$I_T = IA$, $I_R = IA$, $R_{GK} = IK$
GA300A, 301A, GB300A, 301A	` q		0.3	0.5	ριC	$I_{T} = 1A$, $I_{R} = 1A$, $R_{GK} = 1K$
Gate Trigger-on Pulse Width	t _{pg (on)}		0.02	0.05	μS	$I_G = 10$ mA, $I_T = 1$ A
Off-state Current	I _{DRM}	=	0.01 20	0.1 100	μ Α μ Α	V_{DRM} = Rating, R_{GK} = 1K, T = 25°C V_{DRM} = Rating, R_{GK} = 1K, T = 125°C
Reverse Current (Note 2)	I _{RRM}	_	1.0	10	mA	$V_{RRM} = 30V, R_{GK} = 1K$ (Note 2)
Gate Trigger Voltage	V _{GT}	0.4 0.10	0.6 0.2	0.75 —	V V	$V_D = 5V, R_{GS} = 100\Omega, T = 25^{\circ}C$ $V_D = 5V, R_{GS} = 100\Omega, T = 125^{\circ}C$
Gate Trigger Current	I _{GT}	-	10	200	μΑ	$V_D = 5V$, $R_{GS} = 10K$
On-state Voltage	V _T		1.1	1.5	V	$I_{\uparrow} = 2A$
Off-state Voltage — Critical Rate of Rise	dv/dt	15	30	_	V/µs	$V_D = 30V, R_{GK} = 1K$
Reverse Gate Current	I_{GR}	=	0.01	0.1	mA	$V_{GR} = 5V$
Holding Current	I _H	0.3 0.05	2.0 0.4	5.0 —	mA mA	$V_D = 5V$, $R_{GK} = 1K$, $T = 25^{\circ}C$ $V_D = 5V$, $R_{GK} = 1K$, $T = 125^{\circ}C$

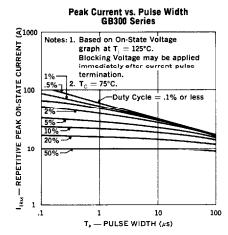


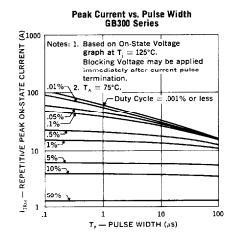


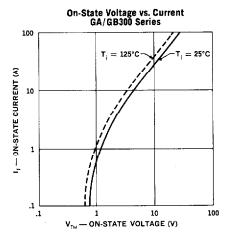
Notes: 1. $I_0 = 10 \text{mA}$, Pulse Test, Duty Cycle < 1%.

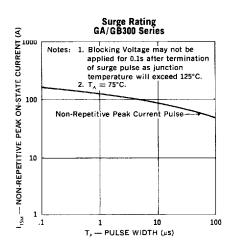
2. Pulse test intended to guarantee reverse anode voltage capability for pulse commutation. Device should not be operated in the reverse blocking mode on a continuous basis.











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