

# International Rectifier

PD - 9.1106

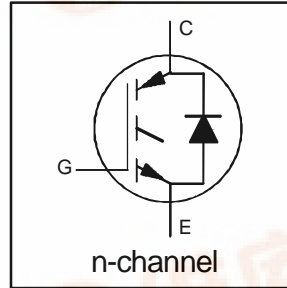
## IRGBC20MD2

INSULATED GATE BIPOLAR TRANSISTOR WITH ULTRAFAST SOFT RECOVERY DIODE

Short Circuit Rated Fast Copack IGBT

### Features

- Short circuit rated -10 $\mu$ s @125°C,  $V_{GE} = 15V$
- Switching-loss rating includes all "tail" losses
- HEXFRED™ soft ultrafast diodes
- Optimized for medium operating frequency (1 to 10kHz) See Fig. 1 for Current vs. Frequency curve

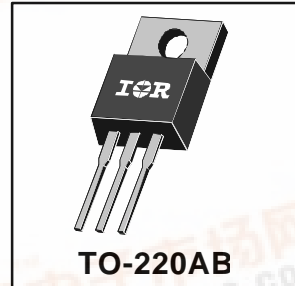


$V_{CES} = 600V$
$V_{CE(sat)} \leq 2.5V$
@ $V_{GE} = 15V, I_C = 8.0A$

### Description

Co-packaged IGBTs are a natural extension of International Rectifier's well known IGBT line. They provide the convenience of an IGBT and an ultrafast recovery diode in one package, resulting in substantial benefits to a host of high-voltage, high-current, applications.

These new short circuit rated devices are especially suited for motor control and other applications requiring short circuit withstand capability.



### Absolute Maximum Ratings

	Parameter	Max.	Units
$V_{CES}$	Collector-to-Emitter Voltage	600	V
$I_C @ T_C = 25^\circ C$	Continuous Collector Current	13	A
$I_C @ T_C = 100^\circ C$	Continuous Collector Current	8.0	
$I_{CM}$	Pulsed Collector Current $\text{\textcircled{D}}$	26	
$I_{LM}$	Clamped Inductive Load Current $\text{\textcircled{D}}$	26	
$I_F @ T_C = 100^\circ C$	Diode Continuous Forward Current	7.0	
$I_{FM}$	Diode Maximum Forward Current	60	$\mu s$
$t_{sc}$	Short Circuit Withstand Time	10	
$V_{GE}$	Gate-to-Emitter Voltage	$\pm 20$	V
$P_D @ T_C = 25^\circ C$	Maximum Power Dissipation	60	W
$P_D @ T_C = 100^\circ C$	Maximum Power Dissipation	24	
$T_J$	Operating Junction and	-55 to +150	$^\circ C$
$T_{STG}$	Storage Temperature Range		
	Soldering Temperature, for 10 sec.	300 (0.063 in. (1.6mm) from case)	
	Mounting Torque, 6-32 or M3 Screw.	10 lbf•in (1.1 N•m)	

### Thermal Resistance

	Parameter	Min.	Typ.	Max.	Units
$R_{\theta JC}$	Junction-to-Case - IGBT	—	—	2.1	$^\circ C/W$
$R_{\theta JC}$	Junction-to-Case - Diode	—	—	3.5	
$R_{\theta CS}$	Case-to-Sink, flat, greased surface	—	0.50	—	
$R_{\theta JA}$	Junction-to-Ambient, typical socket mount	—	—	80	
Wt	Weight	—	2 (0.07)	—	g (oz)



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## Electrical Characteristics @ T<sub>J</sub> = 25°C (unless otherwise specified)

	Parameter	Min.	Typ.	Max.	Units	Conditions
V <sub>(BR)CES</sub>	Collector-to-Emitter Breakdown Voltage ③	600	—	—	V	V <sub>GE</sub> = 0V, I <sub>C</sub> = 250μA
ΔV <sub>(BR)CES/ΔT<sub>J</sub></sub>	Temperature Coeff. of Breakdown Voltage	—	0.42	—	V/°C	V <sub>GE</sub> = 0V, I <sub>C</sub> = 1.0mA
V <sub>CE(on)</sub>	Collector-to-Emitter Saturation Voltage	—	2.0	2.5	V	I <sub>C</sub> = 8.0A V <sub>GE</sub> = 15V
		—	2.7	—		I <sub>C</sub> = 13A See Fig. 2, 5
		—	2.5	—		I <sub>C</sub> = 8.0A, T <sub>J</sub> = 150°C
V <sub>GE(th)</sub>	Gate Threshold Voltage	3.0	—	5.5		V <sub>CE</sub> = V <sub>GE</sub> , I <sub>C</sub> = 250μA
ΔV <sub>GE(th)/ΔT<sub>J</sub></sub>	Temperature Coeff. of Threshold Voltage	—	-11	—	mV/°C	V <sub>CE</sub> = V <sub>GE</sub> , I <sub>C</sub> = 250μA
g <sub>fe</sub>	Forward Transconductance ④	2.7	3.8	—	S	V <sub>CE</sub> = 100V, I <sub>C</sub> = 8.0A
I <sub>CES</sub>	Zero Gate Voltage Collector Current	—	—	250	μA	V <sub>GE</sub> = 0V, V <sub>CE</sub> = 600V
		—	—	1700		V <sub>GE</sub> = 0V, V <sub>CE</sub> = 600V, T <sub>J</sub> = 150°C
V <sub>FM</sub>	Diode Forward Voltage Drop	—	1.4	1.7	V	I <sub>C</sub> = 8.0A See Fig. 13
		—	1.4	1.7		I <sub>C</sub> = 8.0A, T <sub>J</sub> = 150°C
I <sub>GES</sub>	Gate-to-Emitter Leakage Current	—	—	±100	nA	V <sub>GE</sub> = ±20V

## Switching Characteristics @ T<sub>J</sub> = 25°C (unless otherwise specified)

	Parameter	Min.	Typ.	Max.	Units	Conditions	
Q <sub>g</sub>	Total Gate Charge (turn-on)	—	16	24	nC	I <sub>C</sub> = 8.0A	
Q <sub>ge</sub>	Gate - Emitter Charge (turn-on)	—	3.6	5.2		V <sub>CC</sub> = 400V	
Q <sub>gc</sub>	Gate - Collector Charge (turn-on)	—	6.0	9.0		See Fig. 8	
t <sub>d(on)</sub>	Turn-On Delay Time	—	66	—	ns	T <sub>J</sub> = 25°C	
t <sub>r</sub>	Rise Time	—	40	—		I <sub>C</sub> = 8.0A, V <sub>CC</sub> = 480V	
t <sub>d(off)</sub>	Turn-Off Delay Time	—	330	540		V <sub>GE</sub> = 15V, R <sub>G</sub> = 50Ω	
t <sub>f</sub>	Fall Time	—	260	480		Energy losses include "tail" and diode reverse recovery.	
E <sub>on</sub>	Turn-On Switching Loss	—	0.5	—		mJ	See Fig. 9, 10, 11, 18
E <sub>off</sub>	Turn-Off Switching Loss	—	1.0	—			
E <sub>ts</sub>	Total Switching Loss	—	1.5	2.5			
t <sub>sc</sub>	Short Circuit Withstand Time	10	—	—	μs	V <sub>CC</sub> = 360V, T <sub>J</sub> = 125°C V <sub>GE</sub> = 15V, R <sub>G</sub> = 50Ω, V <sub>CPK</sub> < 500V	
t <sub>d(on)</sub>	Turn-On Delay Time	—	65	—	ns	T <sub>J</sub> = 150°C, See Fig. 9, 10, 11, 18	
t <sub>r</sub>	Rise Time	—	46	—		I <sub>C</sub> = 8.0A, V <sub>CC</sub> = 480V	
t <sub>d(off)</sub>	Turn-Off Delay Time	—	520	—		V <sub>GE</sub> = 15V, R <sub>G</sub> = 50Ω	
t <sub>f</sub>	Fall Time	—	560	—		Energy losses include "tail" and diode reverse recovery.	
E <sub>ts</sub>	Total Switching Loss	—	2.3	—		mJ	
L <sub>E</sub>	Internal Emitter Inductance	—	7.5	—	nH	Measured 5mm from package	
C <sub>ies</sub>	Input Capacitance	—	365	—	pF	V <sub>GE</sub> = 0V	
C <sub>oes</sub>	Output Capacitance	—	47	—		V <sub>CC</sub> = 30V See Fig. 7	
C <sub>res</sub>	Reverse Transfer Capacitance	—	4.8	—		f = 1.0MHz	
t <sub>rr</sub>	Diode Reverse Recovery Time	—	37	55	ns	T <sub>J</sub> = 25°C See Fig. 14	
		—	55	90		T <sub>J</sub> = 125°C	
I <sub>rr</sub>	Diode Peak Reverse Recovery Current	—	3.5	5.0	A	T <sub>J</sub> = 25°C See Fig. 15	
		—	4.5	8.0		T <sub>J</sub> = 125°C	
Q <sub>rr</sub>	Diode Reverse Recovery Charge	—	65	138	nC	T <sub>J</sub> = 25°C See Fig. 16	
		—	124	360		T <sub>J</sub> = 125°C	
di <sub>(rec)M/dt</sub>	Diode Peak Rate of Fall of Recovery During t <sub>b</sub>	—	240	—	A/μs	T <sub>J</sub> = 25°C See Fig. 17	
		—	210	—		T <sub>J</sub> = 125°C	

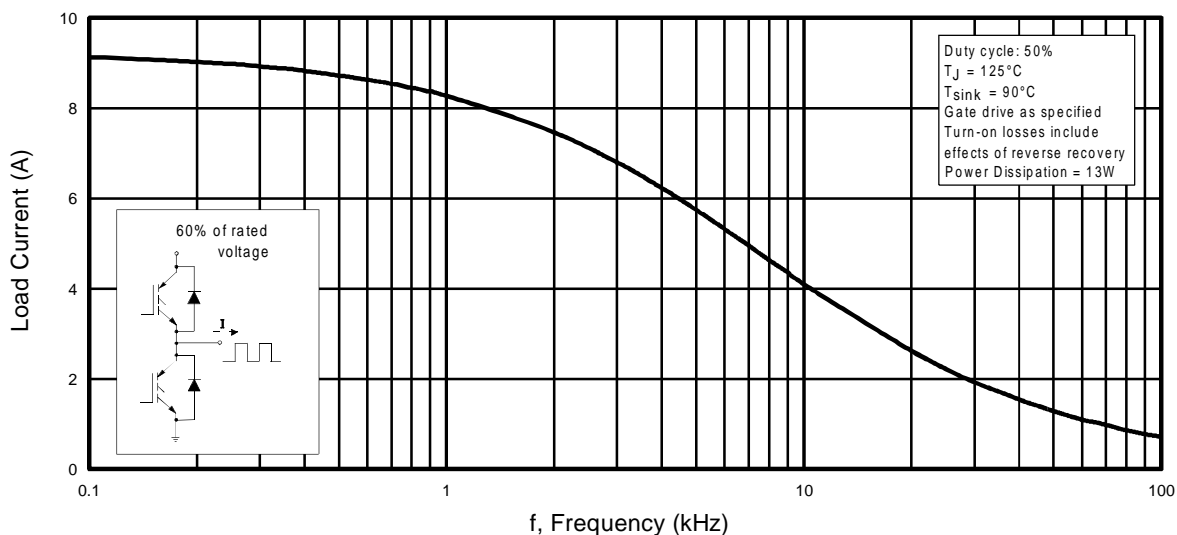
### Notes:

① Repetitive rating; V<sub>GE</sub>=20V, pulse width limited by max. junction temperature. ( See fig. 20 )

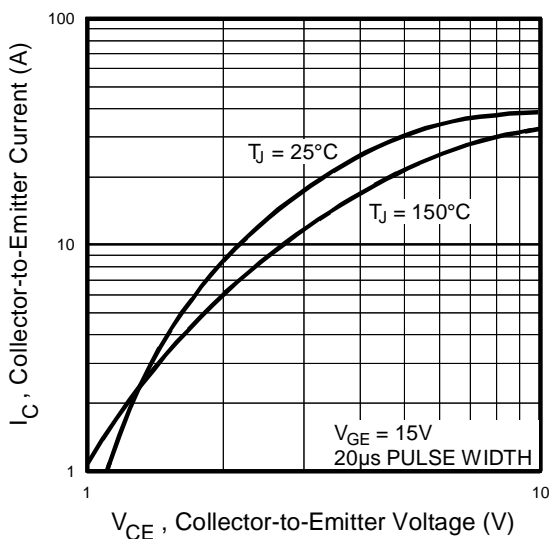
② V<sub>CC</sub>=80%(V<sub>CES</sub>), V<sub>GE</sub>=20V, L=10μH, R<sub>G</sub>= 50Ω, ( See fig. 19 )

③ Pulse width ≤ 80μs; duty factor ≤ 0.1%.

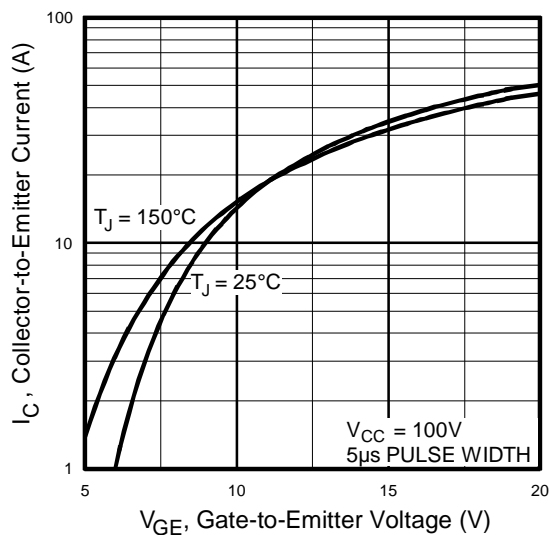
④ Pulse width 5.0μs, single shot.



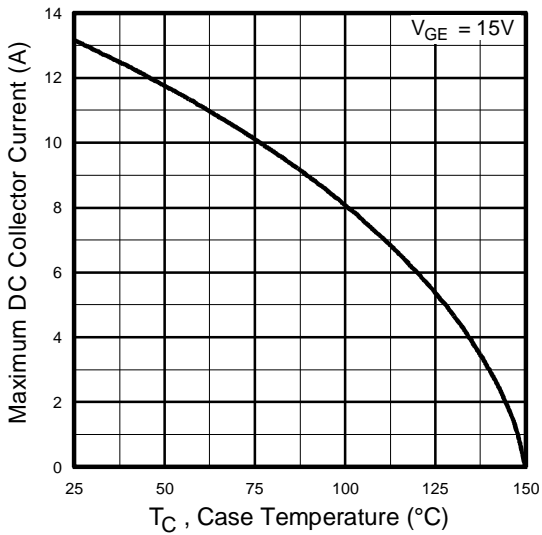
**Fig. 1** - Typical Load Current vs. Frequency  
(Load Current =  $I_{RMS}$  of fundamental)



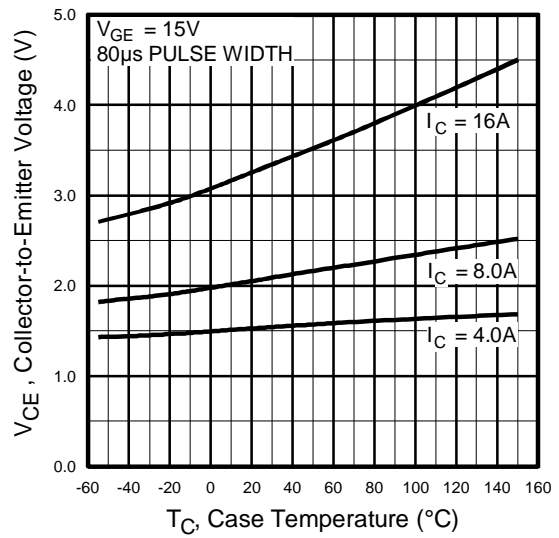
**Fig. 2** - Typical Output Characteristics



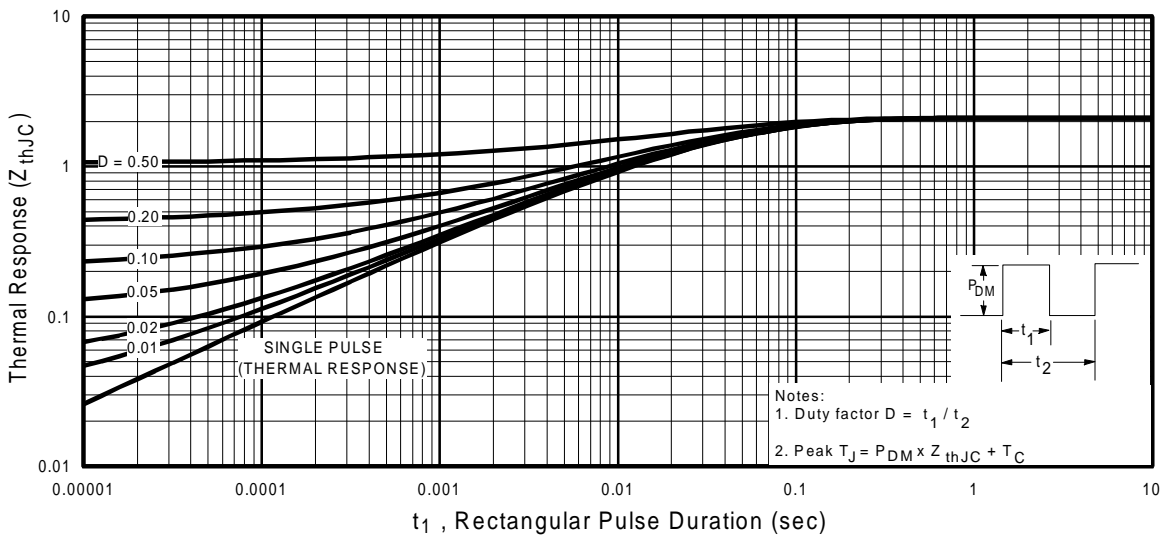
**Fig. 3** - Typical Transfer Characteristics



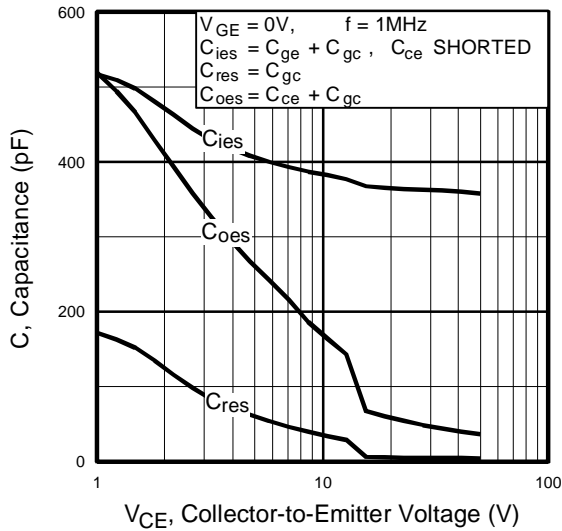
**Fig. 4** - Maximum Collector Current vs. Case Temperature



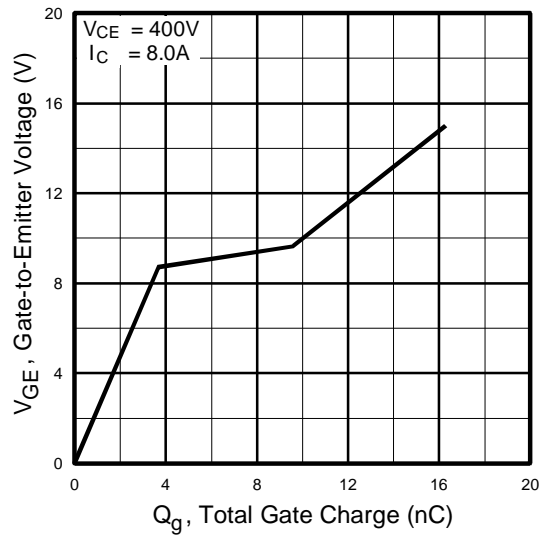
**Fig. 5** - Collector-to-Emitter Voltage vs. Case Temperature



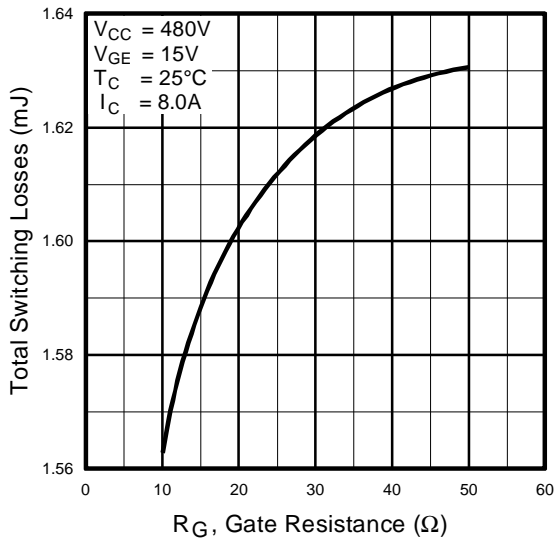
**Fig. 6** - Maximum IGBT Effective Transient Thermal Impedance, Junction-to-Case



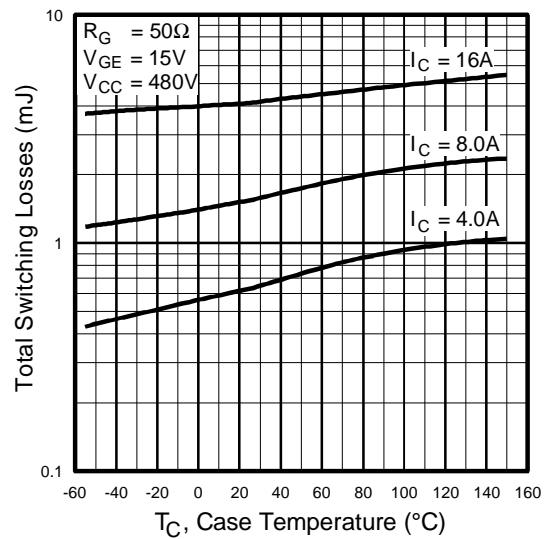
**Fig. 7** - Typical Capacitance vs. Collector-to-Emitter Voltage



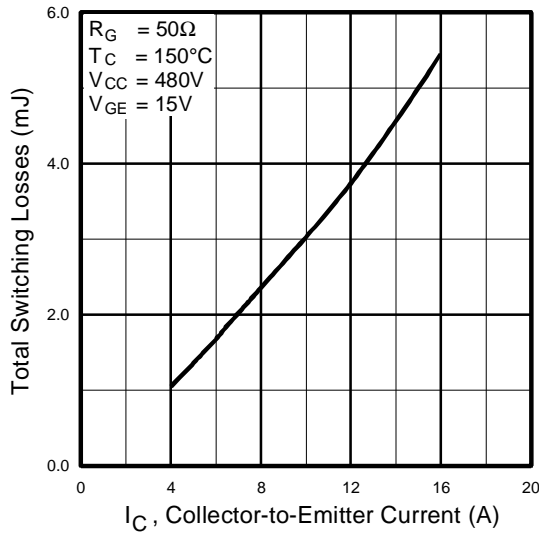
**Fig. 8** - Typical Gate Charge vs. Gate-to-Emitter Voltage



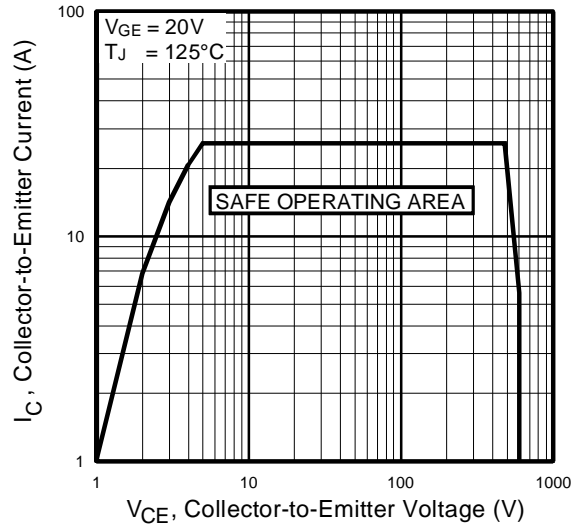
**Fig. 9** - Typical Switching Losses vs. Gate Resistance



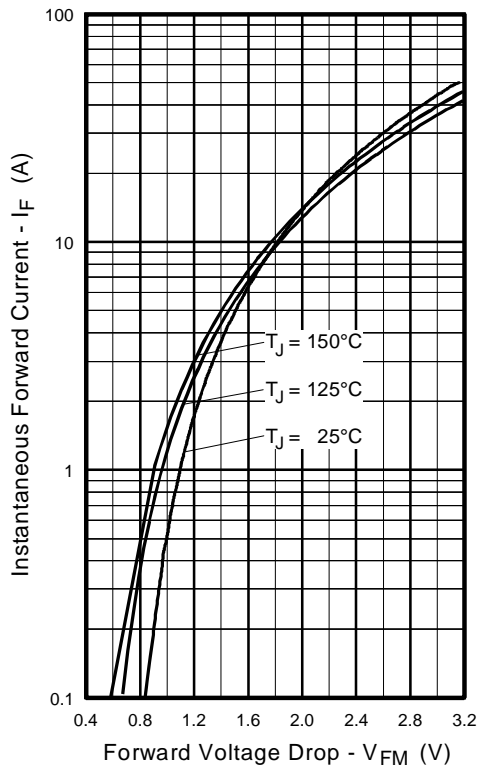
**Fig. 10** - Typical Switching Losses vs. Case Temperature



**Fig. 11** - Typical Switching Losses vs. Collector-to-Emitter Current



**Fig. 12** - Turn-Off SOA



**Fig. 13** - Maximum Forward Voltage Drop vs. Instantaneous Forward Current



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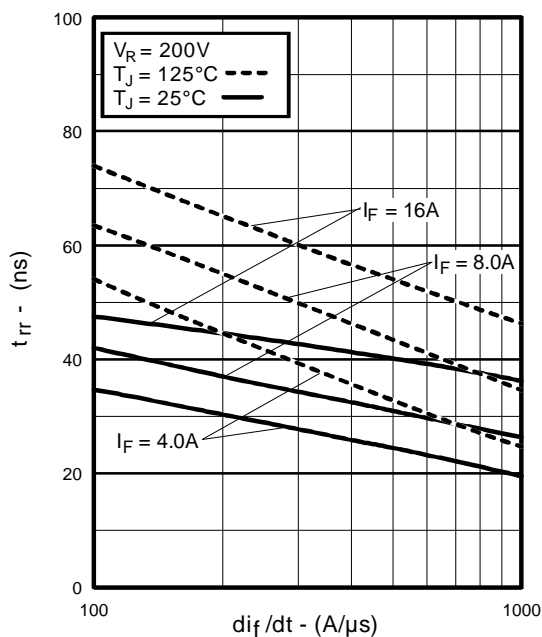


Fig. 14 - Typical Reverse Recovery vs.  $di_f/dt$

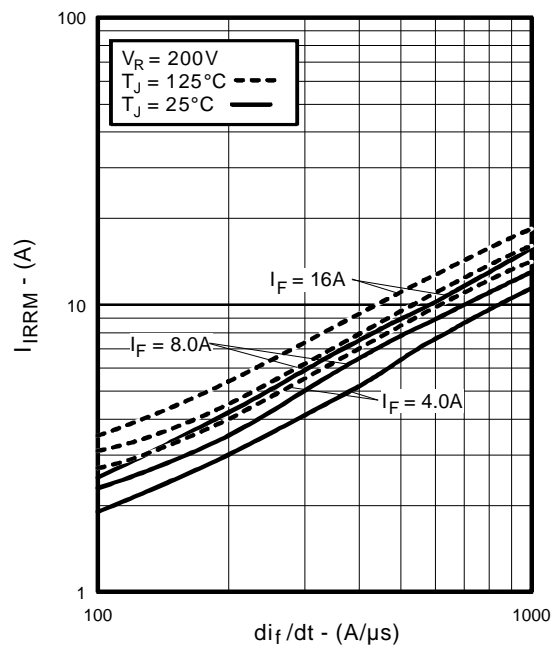


Fig. 15 - Typical Recovery Current vs.  $di_f/dt$

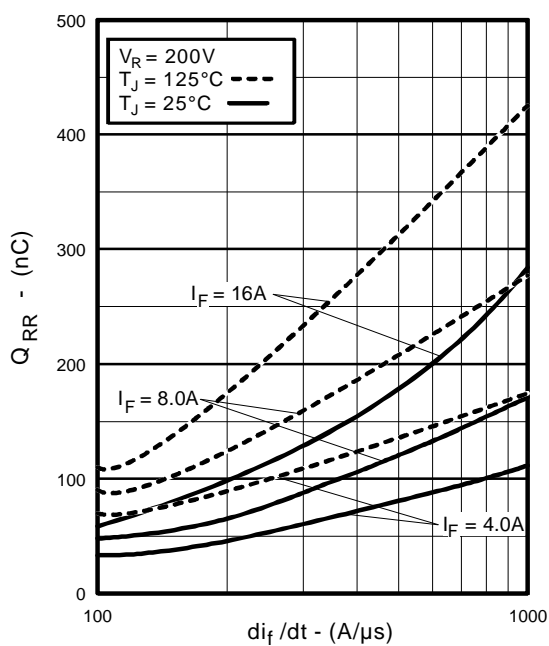


Fig. 16 - Typical Stored Charge vs.  $di_f/dt$

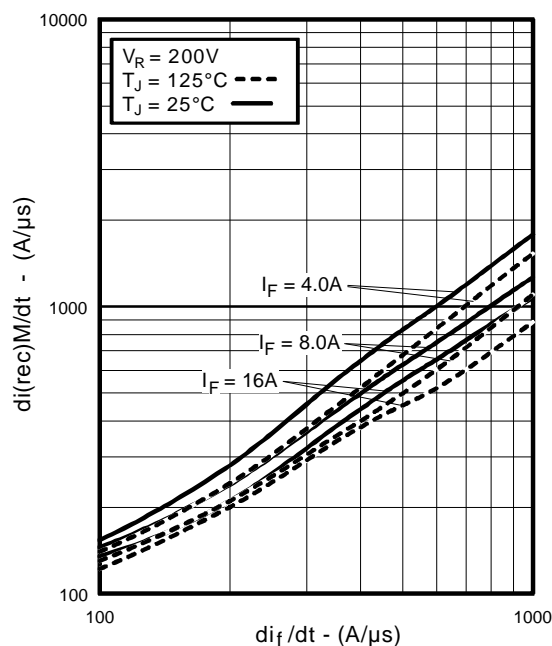
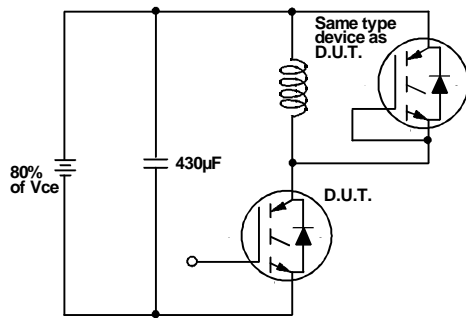
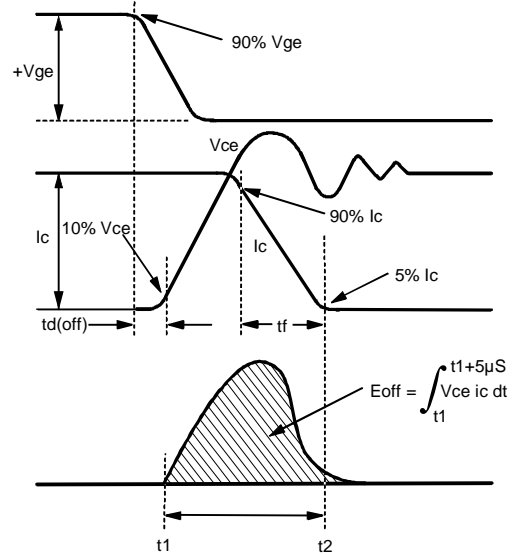


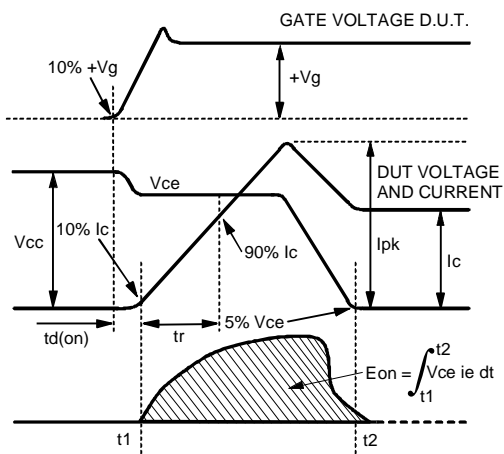
Fig. 17 - Typical  $di_{(rec)M}/dt$  vs.  $di_f/dt$



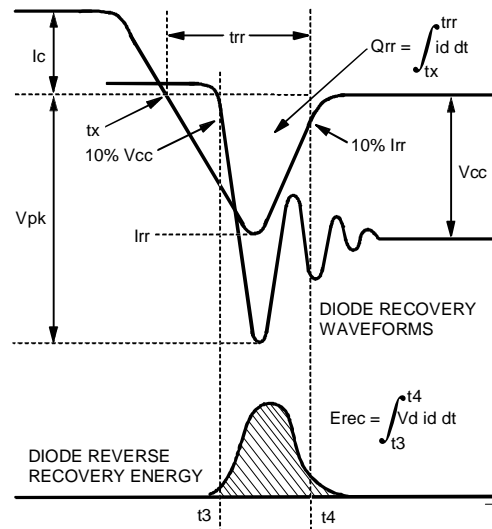
**Fig. 18a** - Test Circuit for Measurement of  $I_{LM}$ ,  $E_{on}$ ,  $E_{off(diode)}$ ,  $t_{rr}$ ,  $Q_{rr}$ ,  $I_{rr}$ ,  $t_{d(on)}$ ,  $t_r$ ,  $t_{d(off)}$ ,  $t_f$



**Fig. 18b** - Test Waveforms for Circuit of Fig. 18a, Defining  $E_{off}$ ,  $t_{d(off)}$ ,  $t_f$



**Fig. 18c** - Test Waveforms for Circuit of Fig. 18a, Defining  $E_{on}$ ,  $t_{d(on)}$ ,  $t_r$



**Fig. 18d** - Test Waveforms for Circuit of Fig. 18a, Defining  $E_{rec}$ ,  $t_{rr}$ ,  $Q_{rr}$ ,  $I_{rr}$

Refer to Section D for the following:

**Appendix D: Section D - page D-6**

Fig. 18e - Macro Waveforms for Test Circuit Fig. 18a

Fig. 19 - Clamped Inductive Load Test Circuit

Fig. 20 - Pulsed Collector Current Test Circuit