

GS-R51212

20W TRIPLE OUTPUT STEP-DOWN SWITCHING REGULATOR

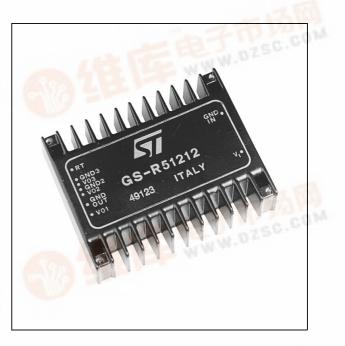
FEATURES

- MTBF in excess of 200,000 hours
- 4V max drop-out voltage
- Soft start
- Reset output
- Non-latching short circuit protection
- Crow-bar output overvoltage protection

DESCRIPTION

The GS-R51212 is a versatile triple output, high current, high voltage step-down switching regulator module that provides a +5V and two isolated 12V outputs. It is ideal for microprocessor based boards because it powers the logic and the communication ports and it has a Reset output for the correct system start-up.

The integral heatsink allows a large power handling capability and it provides also an effective shielding to minimize EMI.



MAIN CHARACTERISTICS

| Vi | Input Voltage | 9 to 40V |
|-----------------|----------------|----------|
| Vo1 | Output Voltage | 5.1V |
| Io1 | Output Current | 3.5A |
| V ₀₂ | Output Voltage | 12V |
| I ₀₂ | Output Current | 0.1A |
| Vo3 | Output Voltage | 12V |
| Io3 | Output Current | 0.1A |

ABSOLUTE MAXIMUM RATINGS

| Symbol | Parameter | Value | Unit | | |
|--------|--|-------|------|--|--|
| Vi | DC Input Voltage | 42 | V | | |
| Irt | Reset Output Sink Current 20 | | | | |
| Tstg | Storage Temperature Range -40 to +105 | | | | |
| Тсор | Operating Case Temperature Range 20 to +85 | | °C | | |



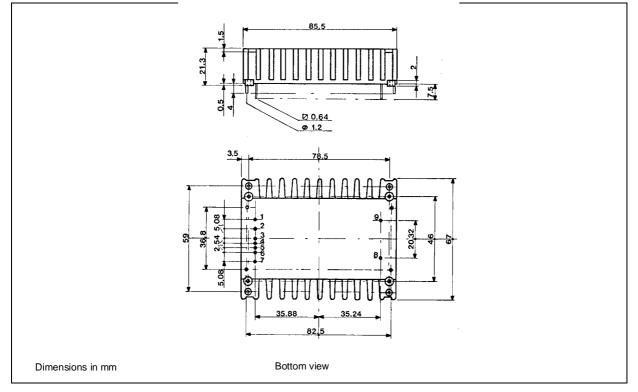
| Symbol | Parameter | Test Conditions | | Min | Тур | Max | Unit |
|-------------------------|--------------------------------|---|------------------------------|------|-----------|------|-------|
| V ₀ 1 | Output Voltage | Vi = 24V | l ₀ 1 = 2.5A | 4.95 | 5.1 | 5.2 | V |
| Vo2 | Output Voltage | V _i =24V I ₀ 2 = 0.1A | | 11.5 | | 12.5 | V |
| Vo3 | Output Voltage | $V_i = 24V$ $I_0 3 = 0.1A$ | | 11.5 | | 12.5 | V |
| $\Delta V_0 / \Delta T$ | Temperature Stability | All Outputs | | | 0.2 | | mV/°C |
| Vi | Input Voltage | | | 9 | | 40 | V |
| lo1 | Output Current | Vi = 24V | | 0.5 | | 3.5 | А |
| l ₀ 2 | Output Current | Vi = 24V | | | | 0.1 | А |
| l ₀ 3 | Output Current | Vi = 24V | | | | 0.1 | А |
| lisc | Average Input Current | Vi = 40V | $V_0 1 = 0 V$ | | 0.1 | 0.2 | А |
| lisc | Average Input Current | Vi = 40V | $V_0 1/2/3 = 0V$ | | 0.1 | 0.2 | А |
| lir | Reflected Input Current | Vi = 24V Io1 = 2.5A Io2,3 = 0.1A | | | 200 | | mApp |
| Vis | 5V to 12V Isolation Voltage | | | 200 | | | VDC |
| fs | Switching Frequency | | | | 100 | | kHz |
| η | Efficiency | $V_i = 24V$ $I_01 = 2.5A I_02,3 = 0.1A$ | | | 70 | | % |
| ΔV_{0} | Line Regulation | $I_01 = 2.5A V_i = 15 \text{ to } 25V I_02,3 = 0.1A$ | | | 2 | | mV/V |
| ΔV_{O} | Load Regulation | $ \begin{array}{ll} V_i = 24V & I_01 = 0.5 \text{ to } 2.5A \\ V_i = 24V & I_02, I_03 = 0.05 \text{ to } 0.1A \end{array} $ | | | 35 600 | | mV/A |
| SVR | Supply Voltage Rejection | 50/60Hz | | | 4 | | mV/V |
| Vor | Output Ripple Voltage | Vi = 24V | l ₀ 1 = 2.5A | | 30 | | mVpp |
| Von | Output Noise Voltage | Vi = 24V Io1 = 2.5A | | | 40 | | mVpp |
| lrh | Reset Leakage Current | | | | 100 | | μΑ |
| Vrl | Reset Low Level | Ireset = 5mA | | | 0.2 | | V |
| trd | Reset Delay Time | | | 100 | | ms | |
| tr1 | Line Transient Recovery Time | lo1 = 2.5A | Vi = 15 to 35V | | 500 | | μs |
| tr2 | Load Transient Recovery Time | Vi = 24V | l ₀ = 0.5 to 2.5A | | 200 | | μs |
| tss | Soft Start Time | | | | 10 | | ms |
| tcd | Crowbar Delay Time | | | | 5 | | μs |
| Vcth | Crowbar Intervention Threshold | | | | 6.37 | | V |
| Rth | Thermal Resistance | Case to ambient | | | 5 | | °C/W |

ELECTRICAL CHARACTERISTICS ($T_A = 25^{\circ}C$ unless otherwise specified)



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CONNECTION DIAGRAM AND MECHANICAL DATA



PIN DESCRIPTION

| Pin | Function | Description |
|-----|------------|---|
| 1 | Output 1 | Regulated 5.1V output. |
| 2 | Output GND | Return for output1 current path. Internally connected to pin 8. |
| 3 | Output 2 | Regulated 12V output. |
| 4 | Ground 2 | Return of output 3 current path. |
| 5 | Output 3 | Regulated 12V output. |
| 6 | Ground 3 | Return of output 3 current path. |
| 7 | Reset | Open collector Reset output. |
| 8 | Input GND | Return of input voltage source. Internally connected to pin 2. |
| 9 | + Input | DC input voltage. Recommended maximum voltage is 40V. |

USER NOTES

Input Voltage

The recommended operating maximum DC input voltage is 40V inclusive of the ripple voltage.

Case Grounding

The module case is internally connected to pin 2 and pin 8.

The PCB area below the module can be used as an effective sixth side shield against EMI.

Thermal Characteristics

The case-to-ambient thermal resistance of the GS-R51212 module is about 5° C/W. This produces a 50° C temperature increase of the module surface for a 10W of internal power dissipation.

Depending on the ambient temperature and/or on the power dissipation, an additional heatsink or forced ventilation may be required.

Input Impedance

The module has an internal capacitor connected between the input pins in order to assure PWM stability. This capacitor cannot handle large values of high frequency ripple current, and it can be permanently damaged if the primary energy source impedance is not adequate.

The use of an external low ESR, high ripple current capacitor located as close to the module as possible is recommended. Suitable capacitors should have a RMS current capability of 2,5 ARMS with a working voltage of 50 V_{DC} and an ESR of 0,1 Ω at 100 kHZ. When space is a limitation, a 22 μ F ceramic multilayer capacitor must be connected to the module input pins.

Module Protection

The module is protected against occasional and permanent short circuits of the output pins to ground, as well as against output current overload. When the output current at 5V output exceeds the maximum value, the output is automatically disabled. After a fixed time the module starts again in a soft mode. The cycle is repeated until the overload condition is removed.

A crow-bar output overvoltage protection is activated when the output voltage on V_{01} exceeds 6.37V.

Output Current

The output current of the main output is 3.5A. The max output current of the two 12V outputs is a function of the input voltage and of the main output current as shown in fig. 1.

If the main current is zero, no voltage will be available on the 12V outputs.

Figure 1. Output Current Capability vs. Operating Conditions

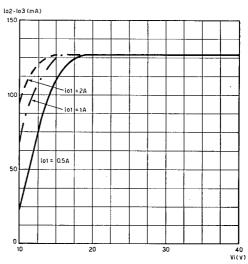


Figure 2. Reset Operation

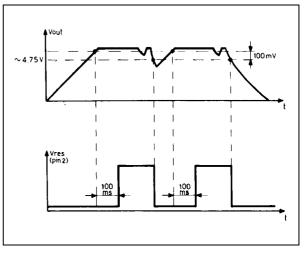
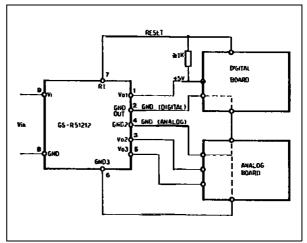


Figure 3. Typical application



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