

### **General Description**

The AAT9460 is a low threshold MOSFET designed for applications in DC-DC Converter, battery, cell phone, and PDA markets. Using AnalogicTech™'s ultra-high density proprietary TrenchDMOS™ technology, this product demonstrates high power handling and small size.

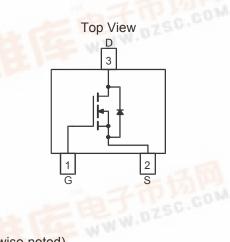
### **Features**

- V<sub>DS(MAX)</sub> = 30V
- $I_{D(MAX)}^{1} = 3.4A @ 25^{\circ}C$
- Low R<sub>DS(ON)</sub>:
  - 58 mΩ @ V<sub>GS</sub> = 4.5V
    - 84 mΩ @ V<sub>GS</sub> = 2.5V

# Applications

- DC-DC Converters
- Battery Packs
- Cellular & Cordless Telephones
- Battery-powered portable equipment

# SC59 Package



## Absolute Maximum Ratings (T<sub>A</sub>=25°C unless otherwise noted)

Symbol	Description		Value	Units	
V <sub>DS</sub>	Drain-Source Voltage		30	V	
V <sub>GS</sub>	Gate-Source Voltage		±12		
I <sub>D</sub>	Continuous Drain Current @ T <sub>J</sub> =150°C <sup>1</sup>	T <sub>A</sub> = 25°C	±3.4		
		T <sub>A</sub> = 70°C	±2.7	А	
I <sub>DM</sub>	Pulsed Drain Current <sup>2</sup>		±8.0	~	
۱ <sub>s</sub>	Continuous Source Current (Source-Drain Diode) 1		1.0	COM	
P <sub>D</sub>	Maximum Power Dissipation 1	$T_A = 25^{\circ}C$	1.1	W	
		T <sub>A</sub> = 70°C	0.7	vv	
T <sub>J</sub> , T <sub>STG</sub>	Operating Junction and Storage Temperature Range		-55 to 150	°C	

# Thermal Characteristics

Symbol	Description	Value	Units	
R <sub>0JA</sub>	Typical Junction-to-Ambient steady state <sup>1</sup> 140			
R <sub>0JA2</sub>	Maximum Junction-to-Ambient t<5 seconds 1	115	°C/W	
$R_{ ext{ heta}JF}$	Typical Junction-to-Foot 1	45		



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# AAT9460 30V N-Channel Power MOSFET

#### Electrical Characteristics (T<sub>J</sub>=25°C unless otherwise noted)

Symbol	Description	Conditions	Min	Тур	Мах	Units	
DC Charac	DC Characteristics						
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V, I <sub>D</sub> =-250µA	30			V	
R <sub>DS(ON)</sub>	Drain-Source ON-Resistance <sup>2</sup>	V <sub>GS</sub> =-4.5V, I <sub>D</sub> =3.4A		46	58	mΩ	
		V <sub>GS</sub> =-2.5V, I <sub>D</sub> =2.8A		65	84		
I <sub>D(ON)</sub>	On-State Drain Current <sup>2</sup>	$V_{GS}$ =4.5V, $V_{DS}$ =5V (Pulsed)	8			A	
V <sub>GS(th)</sub>	Gate Threshold Voltage	$V_{GS}=V_{DS}$ , $I_{D}=-250\mu A$	0.6			V	
I <sub>GSS</sub>	Gate-Body Leakage Current	V <sub>GS</sub> =±12V, V <sub>DS</sub> =0V			±100	nA	
I <sub>DSS</sub>	Drain Source Leakage Current	V <sub>GS</sub> =0V, V <sub>DS</sub> =30V			1	μA	
		V <sub>GS</sub> =0V, V <sub>DS</sub> =24V, T <sub>J</sub> =70°C <sup>3</sup>			5		
9 <sub>fs</sub>	Forward Transconductance <sup>2</sup>	V <sub>DS</sub> =-5V, I <sub>D</sub> =3.4A		9		S	
Dynamic C	Characteristics <sup>3</sup>						
$Q_{G}$	Total Gate Charge	V <sub>DS</sub> =15V, R <sub>D</sub> =4.2Ω, V <sub>GS</sub> =4.5V		5			
$Q_{GS}$	Gate-Source Charge	$V_{DS}$ =15V, $R_{D}$ =4.2 $\Omega$ , $V_{GS}$ =4.5V		0.9		nC	
$Q_{GD}$	Gate-Drain Charge	V <sub>DS</sub> =15V, R <sub>D</sub> =4.2Ω, V <sub>GS</sub> =4.5V		1			
t <sub>D(ON)</sub>	Turn-ON Delay	$V_{DS}$ =15V, $R_{D}$ =4.2 $\Omega$ , $V_{GS}$ =4.5V, $R_{G}$ =6 $\Omega$		6			
t <sub>R</sub>	Turn-ON Rise Time	$V_{DS}$ =15V, $R_{D}$ =4.2 $\Omega$ , $V_{GS}$ =4.5V, $R_{G}$ =6 $\Omega$		3		ne	
t <sub>D(OFF)</sub>	Turn-OFF Delay	$V_{DS}$ =15V, $R_{D}$ =4.2 $\Omega$ , $V_{GS}$ =4.5V, $R_{G}$ =6 $\Omega$		30		ns	
t <sub>F</sub>	Turn-OFF Fall Time	$V_{DS}$ =15V, $R_{D}$ =4.2 $\Omega$ , $V_{GS}$ =4.5V, $R_{G}$ =6 $\Omega$		8			
Source-Drain Diode Characteristics							
$V_{SD}$	Source-Drain Forward Voltage <sup>2</sup>	V <sub>GS</sub> =0, I <sub>S</sub> =3.4A			1.3	V	
۱ <sub>s</sub>	Continuous Diode Current <sup>1</sup>				1.0	A	

Note 1: Based on thermal dissipation from junction to ambient while mounted on a 1" x 1" PCB with optimized layout. A 5 second pulse on a 1" x 1" PCB approximates testing a device mounted on a large multi-layer PCB as in many applications. R<sub>θJF</sub> + R<sub>θFA</sub> = R<sub>θJA</sub> where the foot thermal reference is defined as the normal solder mounting surface of the device's leads. R<sub>θJF</sub> is guaranteed by design; however, R<sub>θFA</sub> is determined by PCB design. Actual maximum continuous current is limited by the application's design. Note 2: Pulse test: Pulse width = 300 µs.

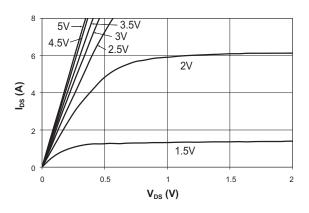
Note 3: Guaranteed by design. Not subject to production testing.



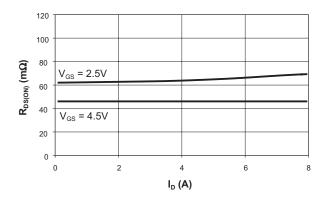
### **Typical Characteristics**

 $(T_1 = 25^{\circ}C \text{ unless otherwise noted})$ 

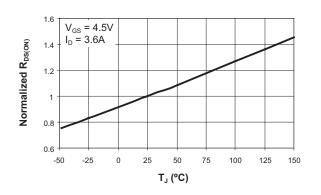
**Output Characteristics** 



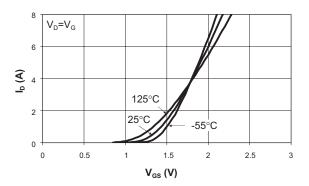
#### **On-Resistance vs. Drain Current**



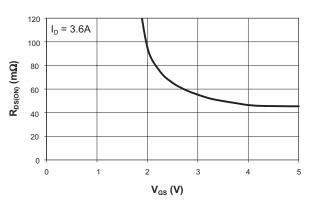
**On-Resistance vs. Junction Temperature** 

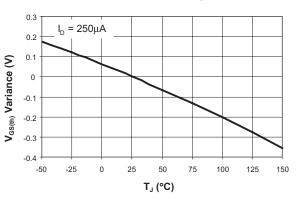


**Transfer Characteristics** 



**On-Resistance vs. Gate to Source Voltage** 





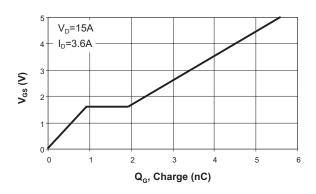
**Threshold Voltage** 



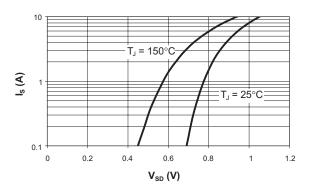
## **Typical Characteristics**

 $(T_J = 25^{\circ}C \text{ unless otherwise noted})$ 

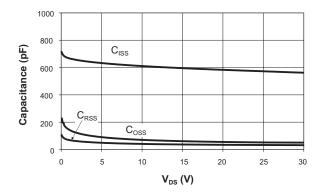
Gate Charge



#### Source-Drain Diode Forward Voltage



#### Capacitance



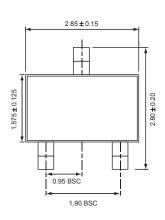


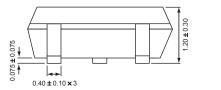
# **Ordering Information**

Package	Marking	Part Number (Tape and Reel)
SC59	HA	AAT9460IGY-T1

SC59

# Package Information







All dimensions in millimeters.

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