PECL* to TTL Translator (+5 Vdc Power Supply Only)

The MC10H350 is a member of Motorola's 10H family of high performance ECL logic. It consists of 4 translators with differential inputs and TTL outputs. The 3-state outputs can be disabled by applying a HIGH TTL logic level on the common OE input.

The MC10H350 is designed to be used primarily in systems incorporating both ECL and TTL logic operating off a common power supply. The separate V_{CC} power pins are not connected internally and thus isolate the noisy TTL V_{CC} runs from the relatively quiet ECL VCC runs on the printed circuit board. The differential inputs allow the H350 to be used as an inverting or noninverting translator, or a differential line receiver. The H350 can also drive CMOS with the addition of a pullup resistor.

- Propagation Delay, 3.5 ns Typical
- MECL 10K-Compatible

MAXIMUM RATINGS

Characteristic	Symbol	Rating	Unit
Power Supply (V _{EE} = Gnd)	V _{CC}	7.0	Vdc
Operating Temperature Range	Т _А	0 to +75	°C
Storage Temperature Range — Plastic — Ceramic	T _{stg}	-55 to +150 -55 to +165	° °

ELECTRICAL CHARACTERISTICS (V_{CC} = 5.0 V ±5%) (See Note 1)

- / 65.1				T _A = 0°C to 75°C	
Characteristic		Symbol	Min	Max	Unit
Power Supply Current	TTL ECL	ICC		20 12	mA
Input Current High	Pin 9 Others	I _{IH} IINH	— —	20 50	μΑ
Input Current Low	Pin 9 Others	I _{IL} I _{INL}	-	-0.6 50	mΑ μΑ
Input Voltage High	Pin 9	VIH	2.0	V/	Vdc
Input Voltage Low	Pin 9	VIL		0.8	Vdc
Differential Input Voltage (1) Pins	3–6, 11–14 (1)	VDIFF	350	—	mV
Voltage Common Mode	2 <mark>ins 3</mark> –6, 11–14	VCM	2.8	VCC	Vdc
Output Voltage High I _{OH} = 3.0 mA		VOH	2.7	—	Vdc
Output Voltage Low I _{OL} = 20 mA		V _{OL}	-	0.5	Vdc
Short Circuit Current V _{OUT} = 0 V		los	-60	-150	mA
Output Disable Current High $V_{OUT} = 2.7 V$		ЮZН	1	50	μA
Output Disable Current Low $V_{OUT} = 0.5 V$	"m=	lozl	COM.	<u>-50</u>	μA

(1) Common mode input voltage to pins 3-4, 5-6, 11-12, 13-14 must be between the values of 2.8

V and 5.0 V. This common mode input voltage range includes the differential input swing. For single ended use, apply 3.75 V (VBB) to either input depending on output polarity required. Signal level range to other input is 3.3 V to 4.2 V. (2)

(3) Any unused gates should have the inverting inputs tied to V_{CC} and the non-inverting inputs tied to ground to prevent output glitching.
(4) 1.0 V to 2.0 V w/50 pF into 500 ohms.

*Positive Emitter Coupled Logic

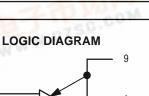


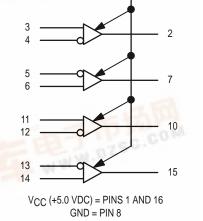


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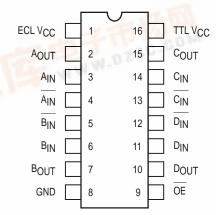


FN SUFFIX PI CC CASE 775-02





DIP **PIN ASSIGNMENT**



Pin assignment is for Dual-in-Line Package. For PLCC pin assignment, see the Pin Conversion Tables on page 6-11 of the Motorola MECL Data Book (DL122/D).

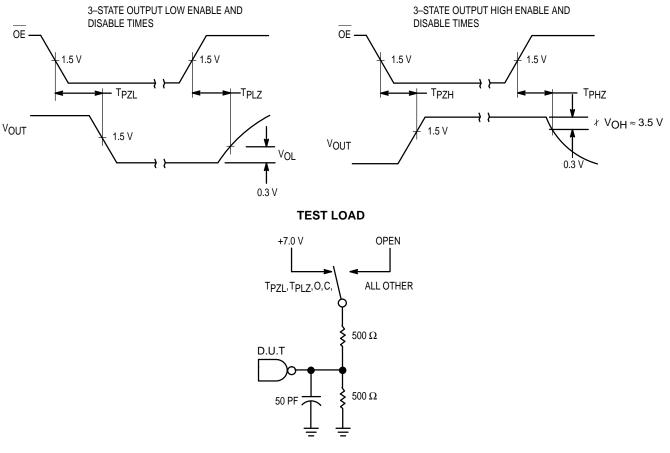


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		T _A = 0°C to 75°C					
Characteristic	Symbol	Min	Max	Unit			
AC PARAMETERS (C _L = 50 pF) (V _{CC} = 5.0 \pm 5%) (T _A = 0°C to 75°C)							
Propagation Delay Data	^t pd	1.5	5.0	ns			
Rise Time	tr	0.3	1.6	ns			
Fall Time	t _f	0.3	1.6	ns			
Output Disable Time	^t pdLZ ^t pdHZ	2.0 2.0	6.0 6.0	ns			
Output Enable Time	^t pdZL ^t pdZH	2.0 2.0	8.0 8.0	ns			

ELECTRICAL CHARACTERISTICS (V_{CC} = 5.0 V \pm 5%) (See Notes 1 & 4)

3-STATE SWITCHING WAVEFORMS

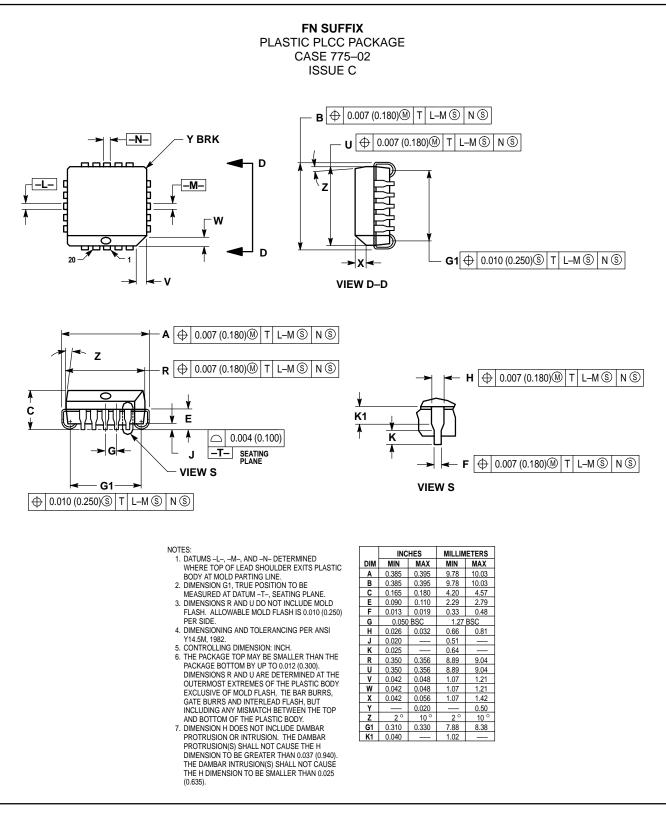


*INCLUDES JIG AND PROBE CAPACITANCE

Application Note: Pin 9 is an \overline{OE} and the 10H350 is disabled when \overline{OE} is at V_{IH} or higher.

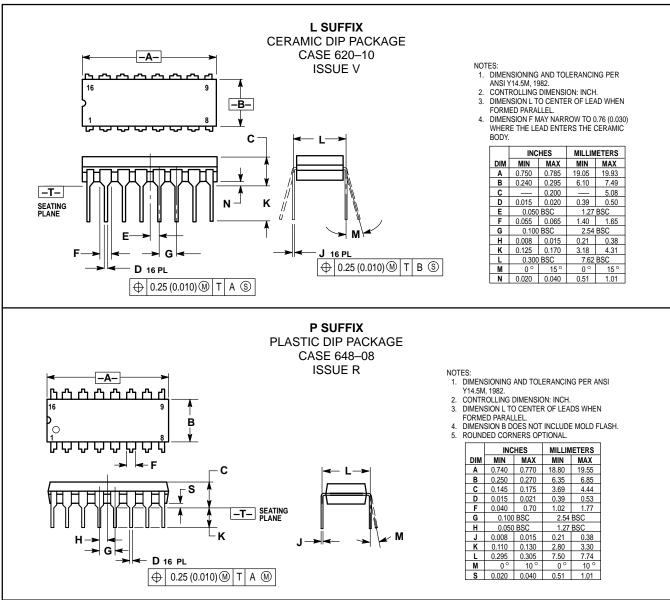
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OUTLINE DIMENSIONS



MC10H350

OUTLINE DIMENSIONS



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