

HA13490P, HA13490MP, HA13490FP

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Voice Coil Motor Driver

Description

HA13490/FP/MP are VCM drive IC for HDD and have following functions and features.

Functions

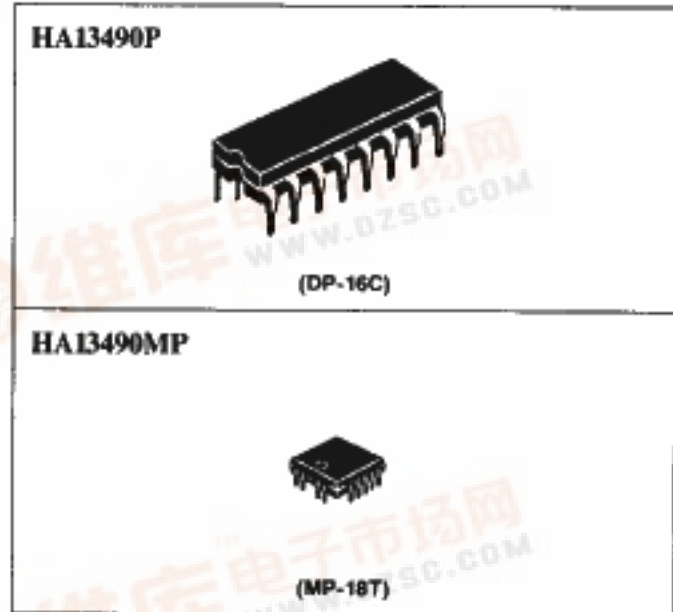
- Input buffer amp
- 1.2 A peak BTL output amp
- Retract input
- Chip enable input
- Independent OP amp
- OTSD (Over Temperature Shut Down)

Features

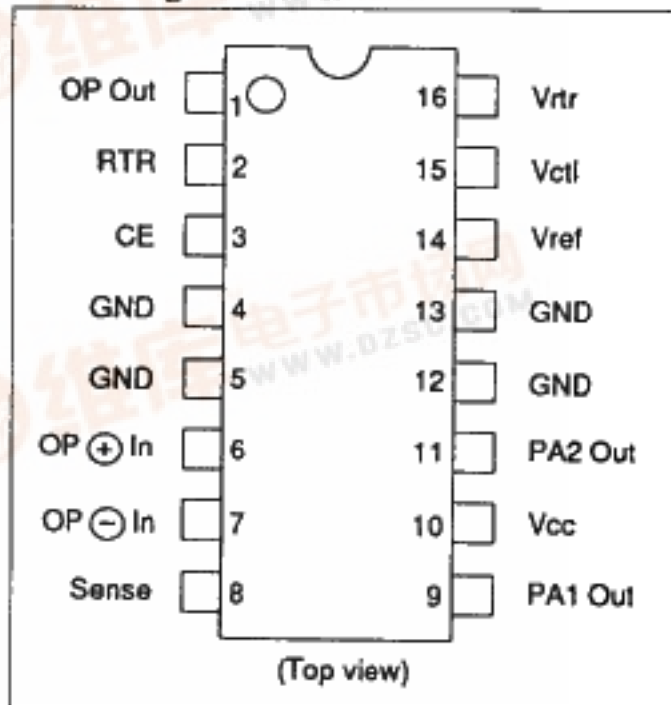
- Wide operating voltage range
- No cross-over distortion
- Small external components
- Low saturation voltage
- 3 types package line up

Ordering Information

Type No.	Package
HA13490P	300 mil 16 pin plastic DIP (DP-16C)
HA13490MP	18 pin plastic QFI (MP-18T)
HA13490FP	16 pin plastic SOP (FP-16T)



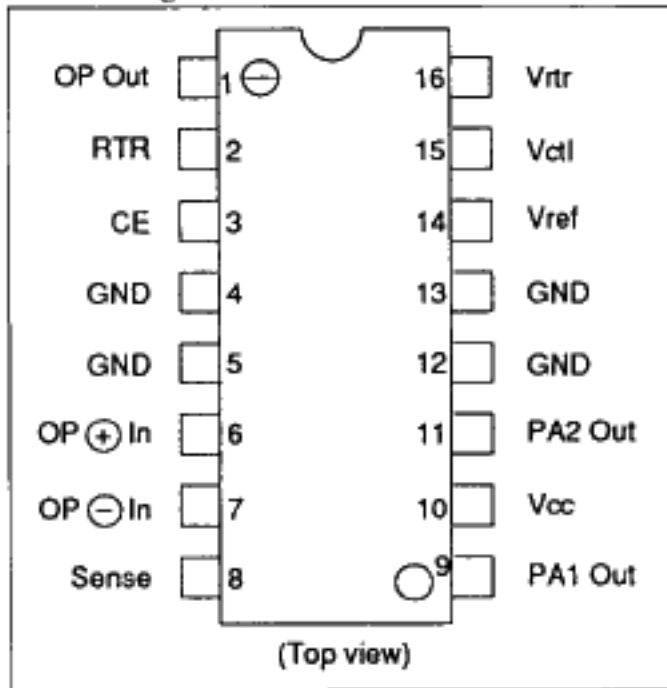
Pin Arrangement



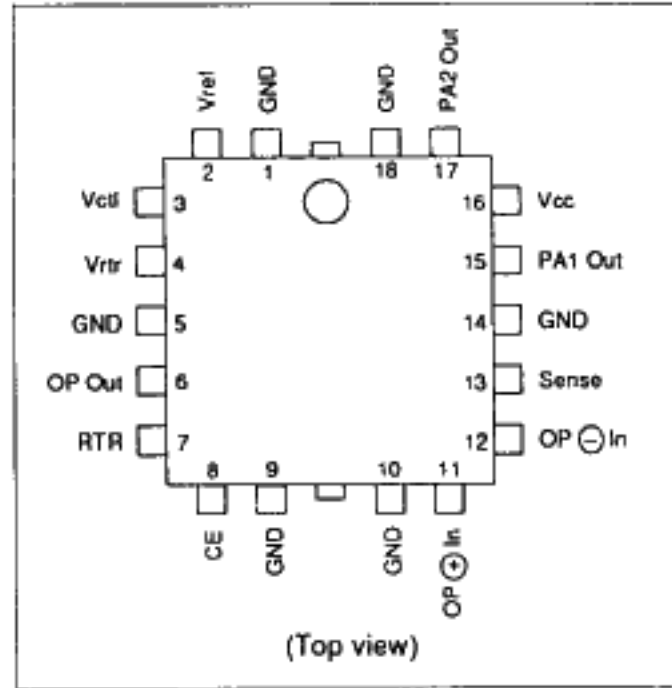
HA13490P

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Pin Arrangement

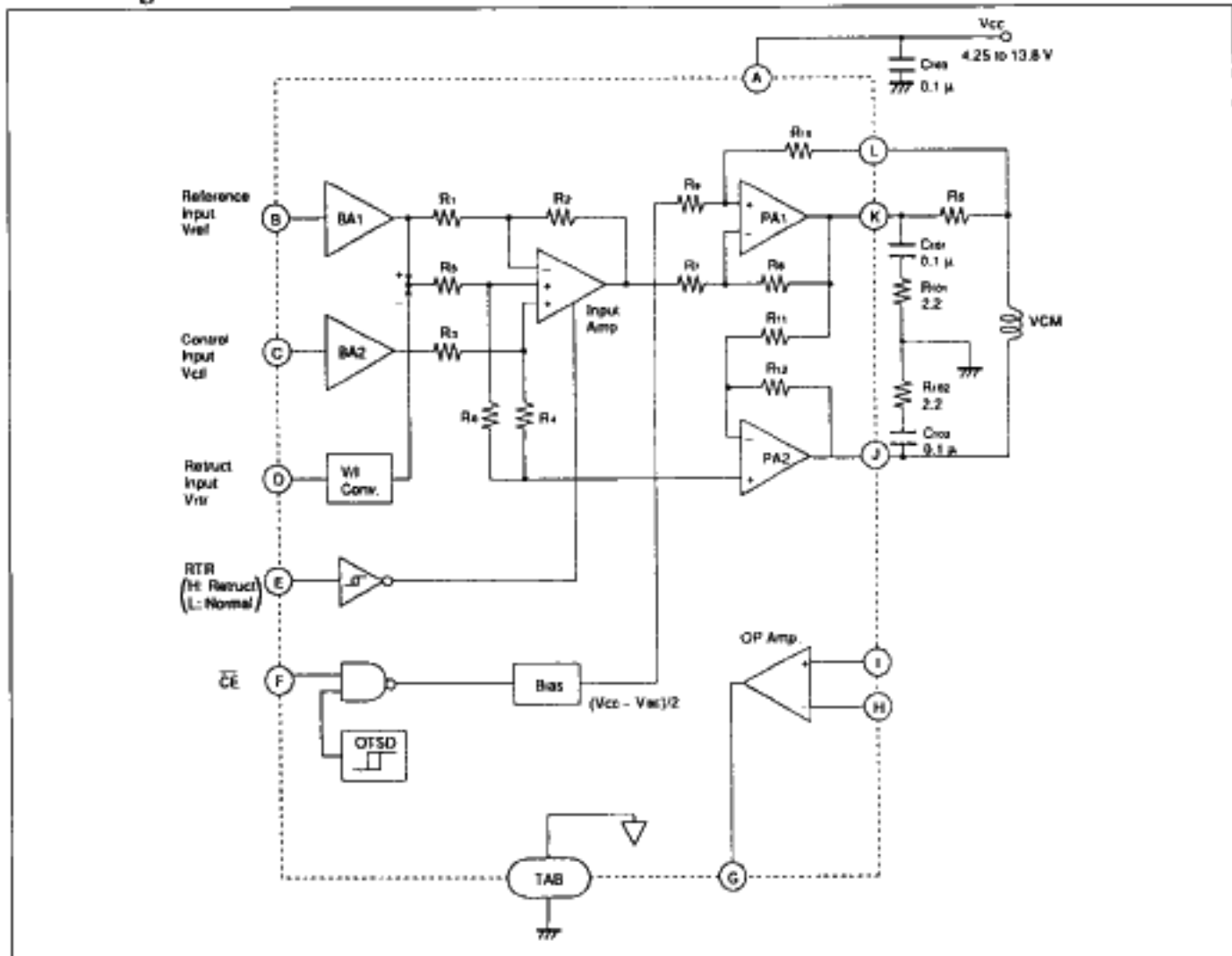


HA13490FP



HA13490MP

Block Diagram



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Table 1 External Components

Parts No.	Recommended Value	Purpose	Note
R101, R102	2.2 Ω	Stability	
Rs	2.0 Ω	Current sense	1
C101, C102	0.1 μ F	Stability	2
C103	$\geq 0.1 \mu$ F	Power supply bypass	

Note: Use a reactance free resistance.
 The relation between output current and input voltage can be described as follows.
 When RTR is L
 $i_o = -g_m(V_{ctl} - V_{ref})$
 When RTR is H
 $i_o = -g_{mr} V_{tr}$
 Where g_m and g_{mr} are internal constant (see electrical characteristics) and output current "+" means source current from PA1 and "-" means sink current.

Table 2 Absolute Maximum Ratings (Ta = 25 °C)

Item	Symbol	HA13490	HA13490MP	HA13490FP	Unit	Note
Supply voltage	Vcc	15	15	15	V	1
Input voltage	Vin	0 to Vcc	0 to Vcc	0 to Vcc	V	2
Peak output current	Iopeak	1.2	1.2	1.2	Ap	3
DC output current	Io	0.8	0.8	0.8	A	
Power dissipation	PT	2(Tpin=120 °C)	2(Tpin=100 °C)	2(Tpin=70 °C)	W	4
Junction temperature	Tj	150	150	150	°C	1
Storage temperature	Tstg	-55 to +125	-55 to +125	-55 to +125	°C	

The absolute maximum ratings are limiting values, to be applied individually, beyond which the device may be permanently damaged. Functional operation under any of these conditions is not guaranteed. Exposing a circuit to its absolute maximum rating for extended periods of time may affect the device's reliability.

- Notes: 1. Recommended operating range is as follows.
 Vcc = 4.25 to 13.8 V
 Tjopr = 0 to 125 °C
 2. Apply to CE, RTF, Vref, Vctl and Vtr input.
 3. t \leq 20 ms
 4. Package thermal data

Item	HA13490	HA13490MP	HA13490FP	Unit
θ_{j-pin}	15	25	40	°C/W
θ_{j-a}	60	80	100	°C/W

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Table 4 Electrical Characteristics (Ta = 25 °C, Vcc = 12 V)

Item	Symbol	Min	Typ	Max	Unit	Test Condition	Appli- cation Terminal	Note
Quiescent current	I_{CCQ}	—	0.15	0.4	mA	$V_{CC}=13.8\text{ V}$ $CE=H$	A	
	I_{CC}	—	7.5	15	mA	$I_o=0$ $CE=L$		
CE & RTR	Input low voltage	V_{IL}	—	—	0.8	V		E, F
	Input high voltage	V_{IH}	2.0	—	—	V		
	input current	I_i	—	—	±2.5	μA	$V_{in}=0\text{ to }V_{CC}$	
BA1 & BA2	Input current	I_{CTL}	—	—	±2.5	μA	$V_{ref}=6\text{ V}$, $V_{ctl}=0\text{ to }12\text{ V}$	B, C
	Input offset current	ΔI_{CTL}	—	—	±0.5	μA		
	Input offset voltage	V_{IO1}	—	—	±10	mV	$V_{ref}=6\text{ V}$	B, C 1
	Common mode voltage range	V_{CM1}	$\frac{1}{3}V_{CC}$	—	$\frac{2}{3}V_{CC}$	V		B, C
PA1 & PA2	Quiescent voltage	V_Q	5.3	5.65	6.0	V		K, J
	Leak current	I_{CER}	—	—	4.0	mA	$V_{CE}=15\text{ V}$	
	Output total Saturation voltage	V_{sat}	—	1.8	2.4	V	$I_o=0.8\text{ A}$	K, J
—			1.2	1.4	V	$I_o=0.1\text{ A}$		
BA to PA	V/I transfer gain	g_m	—	0.5	—	A/V	$R_s=2\ \Omega$	K, L 1
	Gain bandwidth	B	—	75	—	kHz	$g_m=-3\text{ dB}$	K, L 2
	Phase shift	$\Delta\phi$	—	8	—	deg.	$f=10\text{ kHz}$	
	Total harmonic distortion	D	—	—	2	%	$f=1\text{ kHz}$, $I_o=0.1\text{ Arms}$	
Vtr to PA	Offset voltage	V_{IC2}	—	—	±20	mV		D 3
	Retract gain	g_{mr}	—	0.1	—	A/V	$R_s=2\ \Omega$	L

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Electrical Characteristics (Ta = 25 °C) (cont)

OP amp	Input current	I _{op}	—	—	±1.0	μA	H, I
	Input offset current	ΔI _{op}	—	—	±0.1	μA	
	Input offset voltage	V _{ios}	—	—	±5	mV	
	Common mode voltage range	V _{cms}	2	—	10	V	
	Openloop voltage gain	G _{ol}	—	60	—	dB	f=1 kHz G
	Gain bandwidth	B _s	—	1000	—	kHz	G _{ol} =0 dB
	Output low voltage	V _{oL}	—	—	1.0	V	I _{oL} =1 mA
Output high voltage	V _{oH}	V _{cc} -1.0	—	—	V	I _{oH} =1 mA	
OTSD	Shutdown temperature	T _{sd}	—	150	—	°C	
	Hysteresis	T _{hys}	—	25	—	°C	

- Notes: 1. See figure 1.
The gm(V/I transfer gain) can be calculated as.
 $gm = \Delta I_o / \Delta V_{ctl}$ (A/V)
2. Test conditions are follows.
R_s = 2 Ω
R_L = 10.5 Ω (Non inductive)
3. See figure 2.
The gmr(Retract gain) can be calculated as

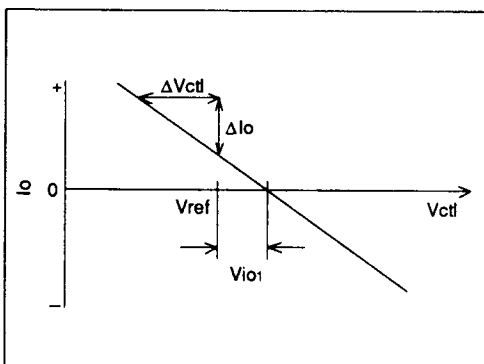


Figure 1 Io vs. Vctl

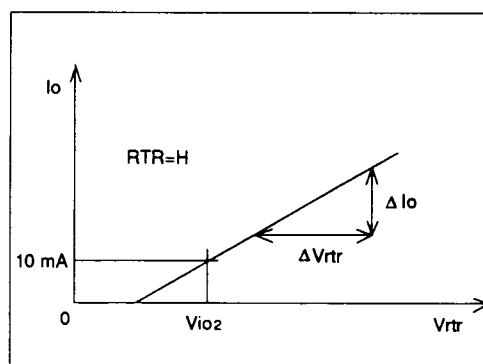


Figure 2 Io vs. Vrtr