Silicon P Channel MOS FET Series
Power Switching / Over Temperature Shut-down Capability

HITACHI

ADE-208-583 A (Z) 2nd Edition October 1997

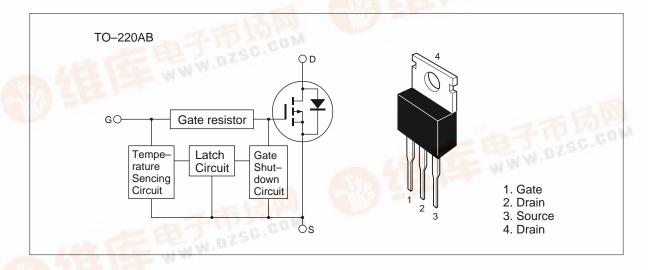
Features

This FET has the over temperature shut—down capability sensing to the junction temperature.

This FET has the built—in over temperature shut—down circuit in the gate area. And this circuit operation to shut—down the gate voltage in case of high junction temperature like applying over power consumption, over current etc.

- Logic level operation (-4 to -6 V Gate drive)
- High endurance capability against to the short circuit
- Built–in the over temperature shut–down circuit
- Latch type shut—down operation (Need 0 voltage recovery)

Outline





Absolute Maximum Ratings ($Ta = 25^{\circ}C$)

Item	Symbol	Ratings	Unit	
Drain to source voltage	$V_{\scriptscriptstyle DSS}$	-60	V	
Gate to source voltage	V_{GSS+}	-16	V	
Gate to source voltage	V_{GSS-}	3	V	
Drain current	I _D	–15	А	
Drain peak current	Note1 D(pulse)	-30	А	
Body-drain diode reverse drain current	I _{DR}	–15	А	
Channel dissipation	Pch Note2	50	W	
Channel temperature	Tch	150	°C	
Storage temperature	Tstg	-55 to +150	°C	

Note: 1. PW \leq 10 μ s, duty cycle \leq 1 %

2. Value at Tc = 25°C

Typical Operation Characteristics

Item	Symbol	Min	Тур	Max	Unit	Test Conditions
Input voltage	V _{IH}	-3.5	_	_	V	
	V _{IL}	_	_	-1.2	V	
Input current	I _{IH1}			-100	μΑ	$Vi = -8V$, $V_{DS} = 0$
(Gate non shut down)	I _{IH2}			-50	μΑ	$Vi = -3.5V, V_{DS} = 0$
	I _{IL}			-1	μΑ	$Vi = -1.2V, V_{DS} = 0$
Input current	I _{IH(sd)1}		-0.8	_	mA	$Vi = -8V, V_{DS} = 0$
(Gate shut down)	I _{IH(sd)2}		-0.35		mA	$Vi = -3.5V, V_{DS} = 0$
Shut down temperature	T _{sd}		175		°C	Channel temperature
Gate operation voltage	V _{OP}	-3.5	_	-13	V	

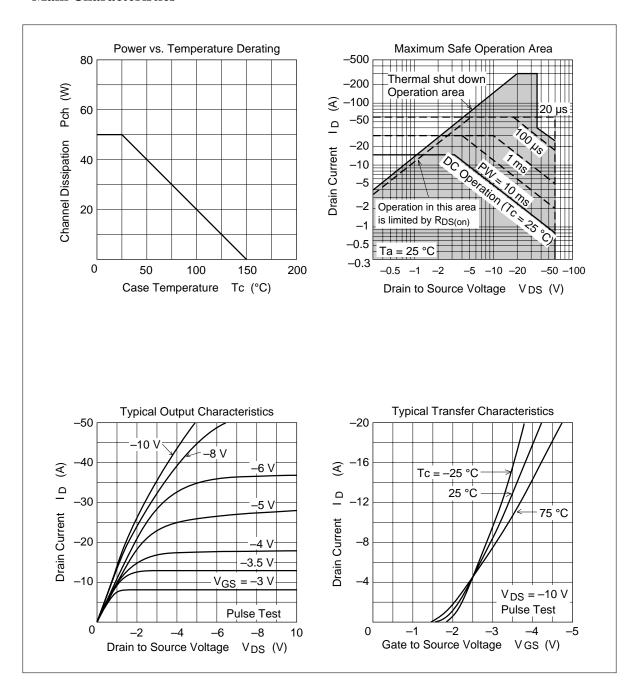
Electrical Characteristics (Ta = 25° C)

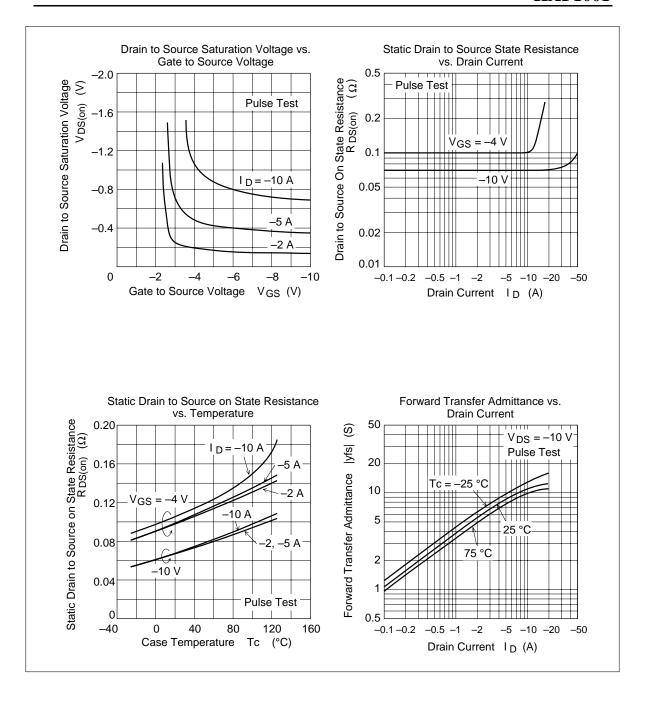
Item	Symbol	Min	Тур	Max	Unit	Test Conditions
Drain current	I _{D1}	-7	_	_	Α	$V_{GS} = -3.5V, V_{DS} = -2V$
Drain current	I _{D2}	_	_	-10	mA	$V_{GS} = -1.2V, V_{DS} = -2V$
Drain to source breakdown voltage	$V_{(BR)DSS}$	-60			V	$I_D = -10 \text{mA}, \ V_{GS} = 0$
Gate to source breakdown voltage	$V_{(BR)GSS+}$	-16	_	_	V	$I_{G} = -100\mu A, V_{DS} = 0$
Gate to source breakdown voltage	$V_{(BR)GSS-}$	3	_	_	V	$I_{G} = 100 \mu A, V_{DS} = 0$
Gate to source leak current	I _{GSS+1}	_		-100	μΑ	$V_{GS} = -8V$, $V_{DS} = 0$
	I _{GSS+2}	_		– 50	μΑ	$V_{GS} = -3.5V, V_{DS} = 0$
	I _{GSS+3}	_	_		μΑ	$V_{GS} = -1.2V, V_{DS} = 0$
	I _{GSS-}	_		100	μΑ	$V_{GS} = 2.4V, V_{DS} = 0$
Input current (shut down)	I _{GS(op)1}	_	-0.8		mA	$V_{GS} = -8V$, $V_{DS} = 0$
	I _{GS(op)1}	_	-0.35	_	mA	$V_{GS} = -3.5V, V_{DS} = 0$
Zero gate voltege drain curren	t I _{DSS}	_	_	-250	μΑ	$V_{DS} = -50 \text{ V}, V_{GS} = 0$
Gate to source cutoff voltage	$V_{GS(off)}$	-1.1	_	-2.25	V	$I_{D} = -1 \text{mA}, \ V_{DS} = -10 \text{V}$
Static drain to source on state resistance	R _{DS(on)}		100	130	mΩ	$I_D = -7.5A, V_{GS} = -4V^{Note3}$
Static drain to source on state resistance	R _{DS(on)}	<u> </u>	70	90	mΩ	$I_D = -7.5A$ $V_{GS} = -10V^{Note3}$
Forward transfer admittance	y _{fs}	5	10		S	$I_D = -7.5A, V_{DS} = -10V^{Note3}$
Output capacitance	Coss	_	610	_	pF	$V_{DS} = -10V , V_{GS} = 0$ f = 1 MHz
Turn-on delay time	t _{d(on)}	_	7.5		μs	$I_D = -7.5A, V_{GS} = -5V$
Rise time	t _r		36	_	μs	$R_L = 4\Omega$
Turn-off delay time	t _{d(off)}		32	_	μs	
Fall time	t _f	_	29	_	μs	_
Body-drain diode forward	V_{DF}		-1.0	_	V	$I_F = -15A, V_{GS} = 0$
voltage						
Body-drain diode reverse	t _{rr}	_	200	_	ns	$I_F = -15A, V_{GS} = 0$
recovery time						diF/ dt =50A/μs
Over load shut down	t _{os1}	_	3.7		ms	$V_{GS} = -5V, V_{DD} = -12V$
operation time Note4	t _{os2}	_	1	_	ms	$V_{GS} = -5V, V_{DD} = -24V$

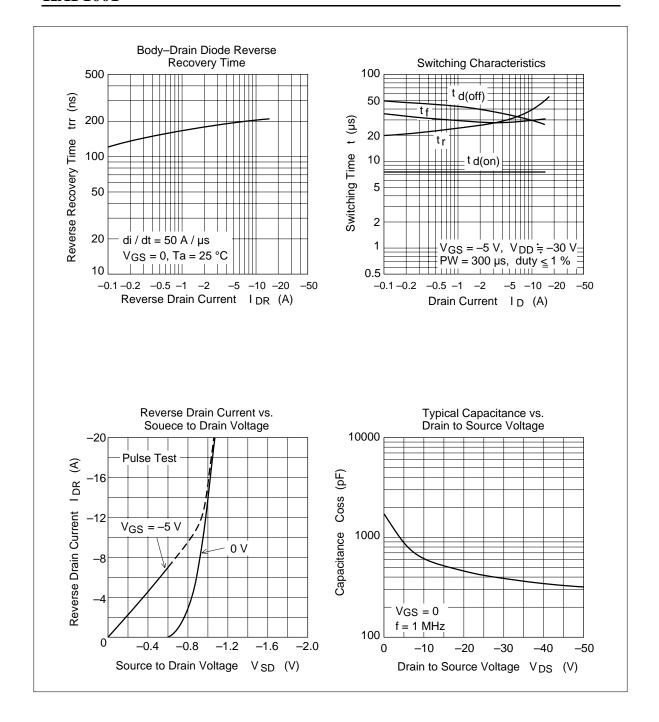
Note: 3. Pulse test

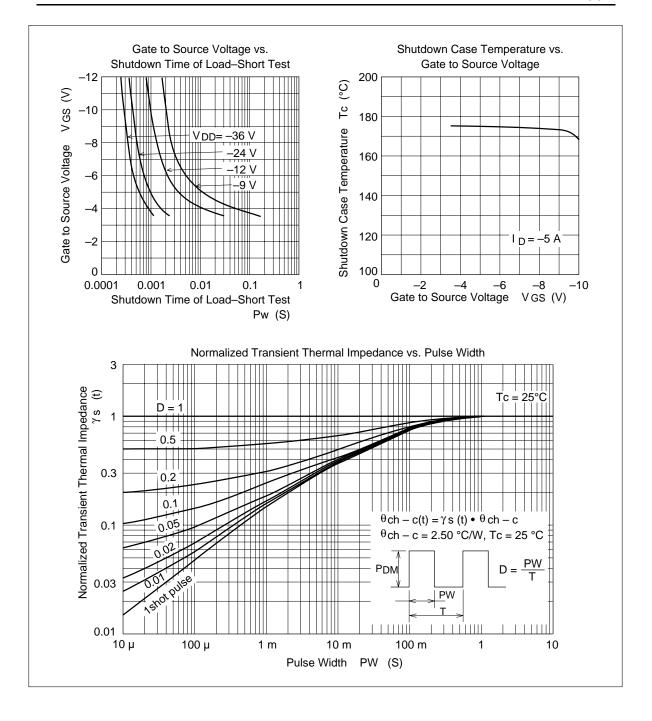
4. Including the junction temperature rise of the over loaded condition.

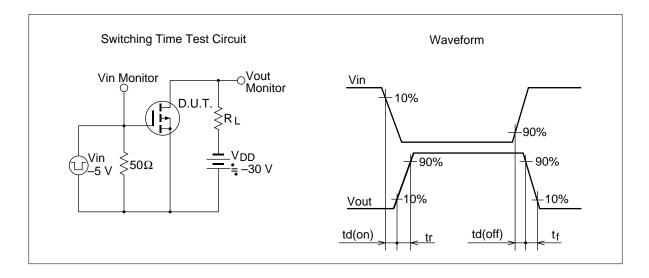
Main Characteristics





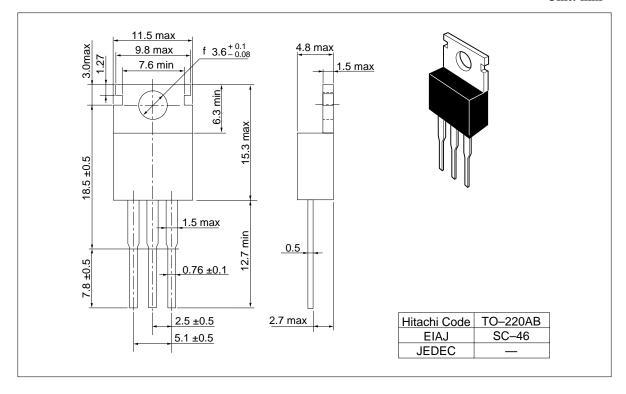






Package Dimensions

Unit: mm



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