HD74AC00

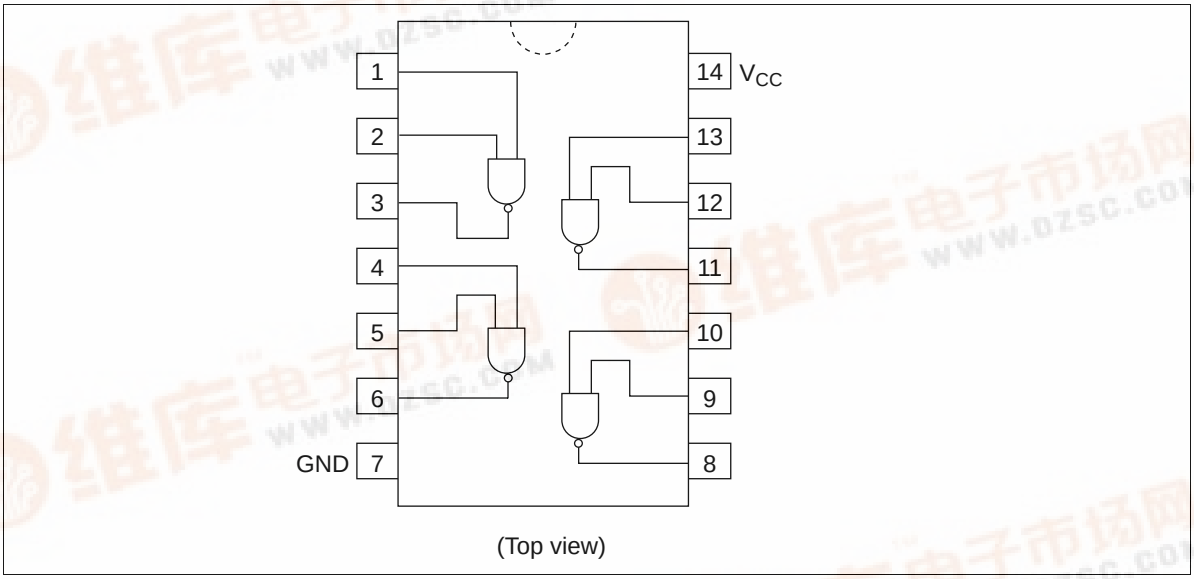
Quad 2-Input NAND Gate

HITACHI

Feature

- Outputs Source/Sink 24 mA

Pin Arrangement



(Top view)

DC Characteristics (unless otherwise specified)

Item	Symbol	Max	Unit	Condition
Maximum quiescent supply current	I_{CC}	40	µA	V_{IN} = V_{CC} or ground, V_{CC} = 5.5 V, Ta = Worst case
Maximum quiescent supply current	I_{CC}	4.0	µA	V_{IN} = V_{CC} or ground, V_{CC} = 5.5 V, Ta = 25°C

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AC Characteristics

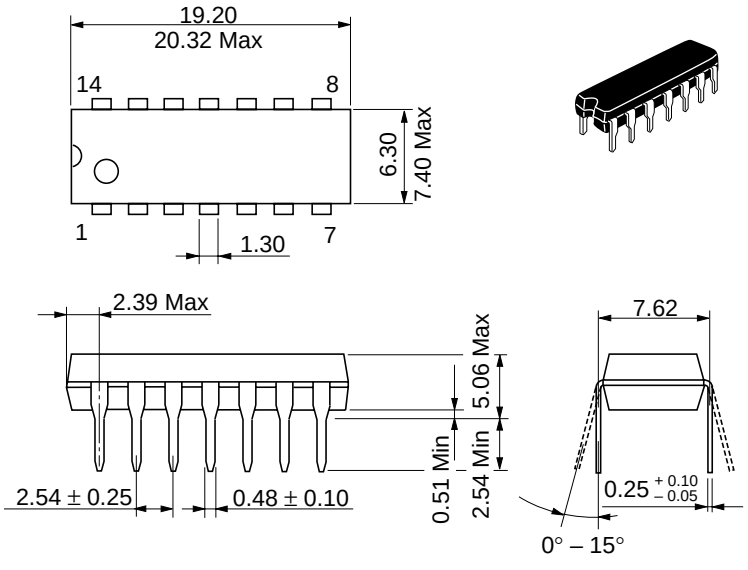
Item	Symbol	V_{CC} (V)*[1]	Ta = +25°C, C_L = 50 pF			Ta = –40°C to +85°C, C_L = 50 pF		Unit
			Min	Typ	Max	Min	Max	
Propagation delay	t_{PLH}	3.3	1.0	7.0	9.5	1.0	10.0	ns
		5.0	1.0	6.0	8.0	1.0	8.5	
Propagation delay	t_{PHL}	3.3	1.0	5.5	8.0	1.0	8.5	ns
		5.0	1.0	4.5	6.5	1.0	7.5	

Note: 1. Voltage Range 3.3 is 3.3 V ± 0.3 V
Voltage Range 5.0 is 5.0 V ± 0.5 V

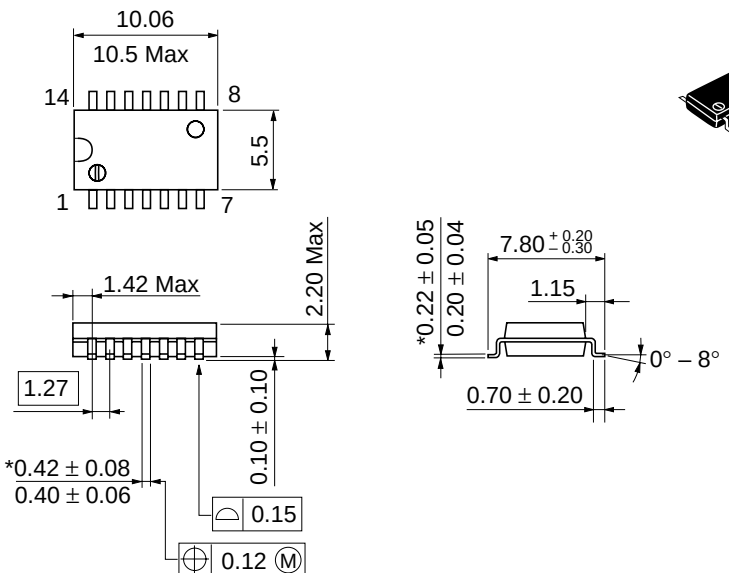
Capacitance

Item	Symbol	Typ	Unit	Condition
Input capacitance	C_{IN}	4.5	pF	V_{CC} = 5.5 V
Power dissipation capacitance	C_{PD}	30.0	pF	V_{CC} = 5.0 V

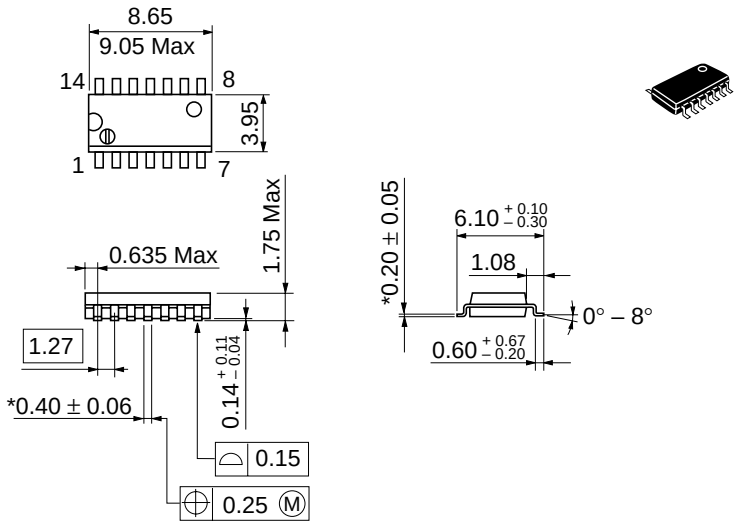
Unit: mm


19.20
20.32 Max
14
8
6.30
7.40 Max
1
1.30
7
2.39 Max
5.06 Max
0.51 Min
2.54 Min
2.54 ± 0.25
0.48 ± 0.10
7.62
0.25 + 0.10 − 0.05
0° – 15°

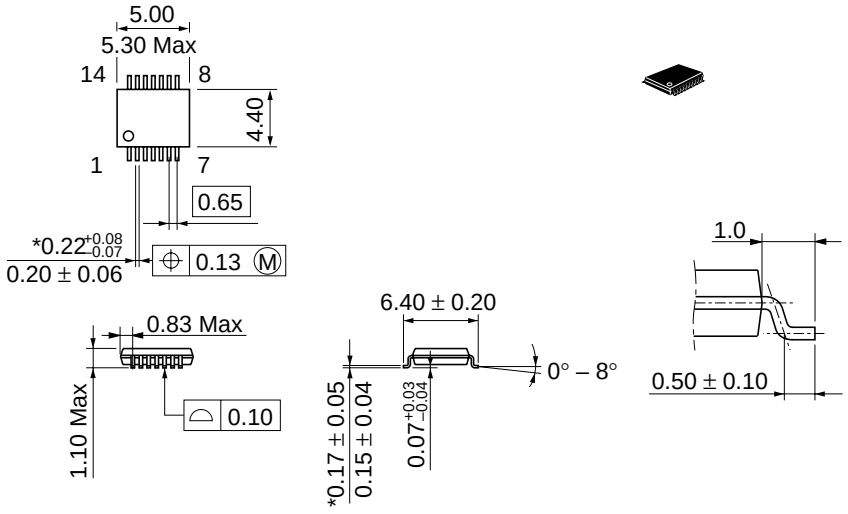
Unit: mm


10.06
10.5 Max
14
8
1
7
5.5
1.42 Max
2.20 Max
1.27
*0.42 ± 0.08
0.40 ± 0.06
0.10 ± 0.10
0.15
0.12 M
*0.22 ± 0.05
0.20 ± 0.04
7.80 +0.20 −0.30
1.15
0° – 8°
0.70 ± 0.20

Unit: mm


8.65
9.05 Max
14
8
3.95
1
7
1.75 Max
0.635 Max
1.27
*0.40 ± 0.06
0.14 +0.11 −0.04
0.15
0.25 M
*0.20 ± 0.05
6.10 +0.10 −0.30
1.08
0° – 8°
0.60 +0.67 −0.20

Unit: mm


5.00
5.30 Max
14
8
4.40
1
7
0.65
*0.22 +0.08 −0.07
0.20 ± 0.06
0.13 M
0.83 Max
1.10 Max
0.10
6.40 ± 0.20
0° – 8°
*0.17 ± 0.05
0.15 ± 0.04
0.07 +0.03 −0.04
1.0
0.50 ± 0.10

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