Dual 1-of-4 Decoder/Demultiplexer



#### Description

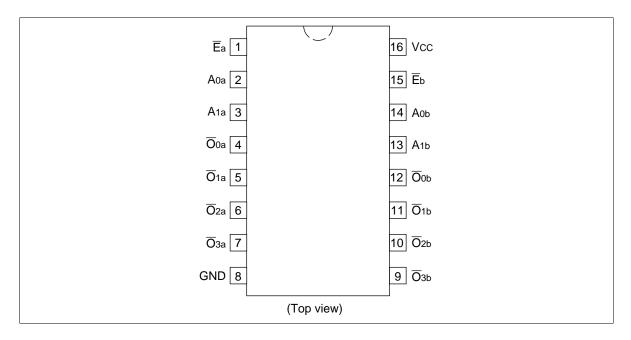
The HD74AC139/HD74ACT139 is a high-speed, dual 1-of-4 decoder/demultiplexer. The device has two independent decoders, each accepting two inputs and providing four mutually-exclusive active-Low outputs. Each decoder has an active-Low Enable input which can be used as a data input for a 4-output demultiplexer. Each half of the HD74AC139/HD74ACT139 can be used as a function generator providing all four minterms of two variables.

#### Features

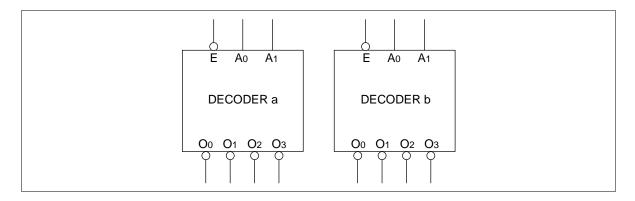
- Multifunction Capability
- Two Completely Independent 1-of-4 Decoders
- Active Low Mutually Exclusive Outputs
- Outputs Source/Sink 24 mA
- HD74ACT139 has TTL-Compatible Inputs



# **Pin Arrangement**



# Logic Symbol



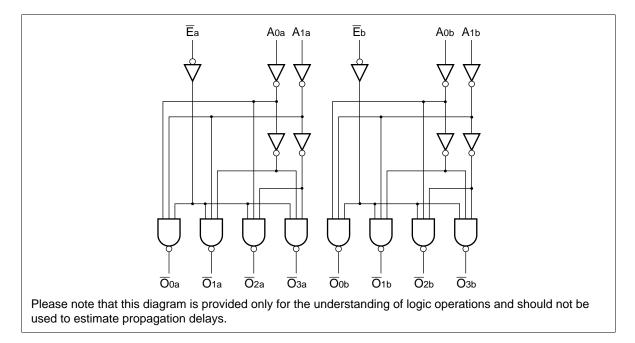
# **Pin Names**

A<sub>0</sub>, A<sub>1</sub> Address Inputs

 $\overline{E}$  Enable Inputs

 $\overline{O}_0$  to  $\overline{O}_3$  Outputs

#### Logic Diagram



#### **Functional Description**

The HD74AC139/HD74ACT139 is a high-speed dual 1-of-4 decoder/demultiplexer. The device has two independent decoders, each of which accepts two binary weighted inputs ( $A_0$  to  $A_1$ ) and provides four mutually exclusive active-Low outputs ( $\overline{O}_0$  to  $\overline{O}_3$ ). Each decoder has an active-Low enable ( $\overline{E}$ ). When  $\overline{E}$  is High all outputs are forced High. The enable can be used as the data input for a 4-output demultiplexer application. Each half of the HD74AC139/HD74ACT139 generates all four minterms of two variables. These four minterms are useful in some applications, replacing multiple gate functions as shown in Figure a, and thereby reducing the number of packages required in a logic network.

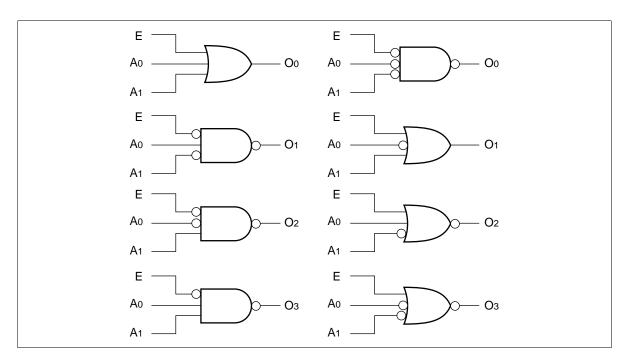
Input	s		Outputs				
Ē	A <sub>o</sub>	<b>A</b> <sub>1</sub>	$\overline{\mathbf{O}}_{0}$	$\overline{\mathbf{O}}_{1}$	$\overline{\mathbf{O}}_{2}$	$\overline{O}_{3}$	
Н	Х	Х	Н	Н	Н	Н	
L	L	L	L	Н	Н	Н	
L	Н	L	Н	L	Н	Н	
L	L	Н	Н	Н	L	Н	
L	Н	Н	Н	Н	Н	L	
Η :	High Voltage Level						
L :	Low Voltage Level						

#### **Truth Table**

X : Immaterial

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Figure a: Gate Functions (each half)



DC Characteristics (unless otherwise specified)

Item	Symbol	Max	Unit	Condition
Maximum quiescent supply current	I <sub>cc</sub>	80	μA	$V_{IN} = V_{CC}$ or ground, $V_{CC} = 5.5 V$ , Ta = Worst case
Maximum quiescent supply current	I <sub>cc</sub>	8.0	μΑ	$V_{IN} = V_{CC}$ or ground, $V_{CC} = 5.5 \text{ V}$ , Ta = 25°C
Maximum I <sub>cc</sub> /input (HD74ACT139)	I <sub>CCT</sub>	1.5	mA	$V_{IN} = V_{CC} - 2.1 \text{ V}, V_{CC} = 5.5 \text{ V}$ Ta = Worst case

## AC Characteristics: HD74AC139

	Symbol	V <sub>cc</sub> (V)* <sup>1</sup>	Ta = +25°C C <sub>∟</sub> = 50 pF			Ta = –40°C to +85°C C <sub>∟</sub> = 50 pF		
Item			Min	Тур	Max	Min	Max	Unit
Propagation delay	t <sub>PLH</sub>	3.3	1.0	8.0	11.5	1.0	13.0	ns
$A_n$ to $\overline{O}_n$		5.0	1.0	6.5	8.5	1.0	9.5	
Propagation delay	t <sub>PHL</sub>	3.3	1.0	7.0	10.0	1.0	11.0	ns
$A_n$ to $\overline{O}_n$		5.0	1.0	5.5	7.5	1.0	8.5	
Propagation delay	t <sub>PLH</sub>	3.3	1.0	9.5	12.0	1.0	13.0	ns
$\overline{E}_n$ to $\overline{O}_n$		5.0	1.0	7.0	8.5	1.0	10.0	
Propagation delay	t <sub>PHL</sub>	3.3	1.0	8.0	10.0	1.0	11.0	ns
$\overline{E}_n$ to $\overline{O}_n$		5.0	1.0	6.0	7.5	1.0	8.5	_

Note: 1. Voltage Range 3.3 is 3.3 V  $\pm$  0.3 V Voltage Range 5.0 is 5.0 V  $\pm$  0.5 V

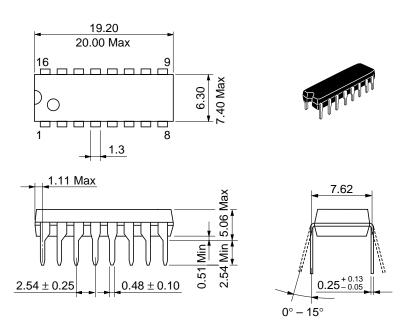
### AC Characteristics: HD74ACT139

				Ta = +25°C C <sub>∟</sub> = 50 pF		Ta = –40°C to +85°C C <sub>∟</sub> = 50 pF			
ltem	Symbol	V <sub>cc</sub> (V)* <sup>1</sup>	Min	Тур	Max	Min	Max	Unit	
Propagation delay $A_n$ to $\overline{O}_n$	t <sub>PLH</sub>	5.0	1.0	6.0	8.5	1.0	9.5	ns	
Propagation delay $A_n$ to $\overline{O}_n$	t <sub>PHL</sub>	5.0	1.0	6.0	9.5	1.0	10.5	ns	
Propagation delay $\overline{E}_n$ to $\overline{O}_n$	t <sub>PLH</sub>	5.0	1.0	7.0	10.0	1.0	11.0	ns	
Propagation delay $\overline{E}_n$ to $\overline{O}_n$	t <sub>PHL</sub>	5.0	1.0	7.0	9.5	1.0	10.5	ns	

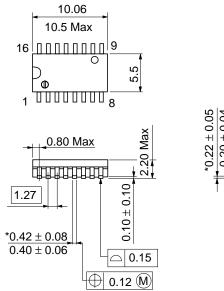
Note: 1. Voltage Range 5.0 is 5.0 V  $\pm$  0.5 V

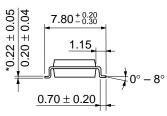
# Capacitance

Item	Symbol	Тур	Unit	Condition
Input capacitance	C <sub>IN</sub>	4.5	pF	$V_{cc} = 5.5 V$
Power dissipation capacitance	C <sub>PD</sub>	40.0	pF	$V_{cc} = 5.0 V$



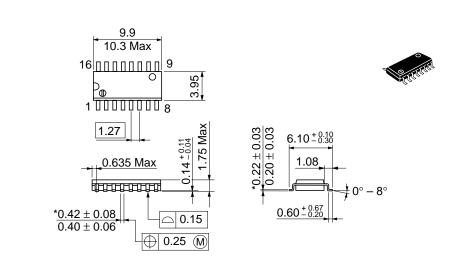
Unit: mm



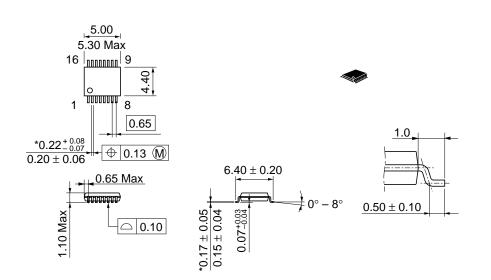




Unit: mm



Unit: mm





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