

# HD74AC298/HD74ACT298

Quad 2-Input Multiplexer with Storage

# HITACHI

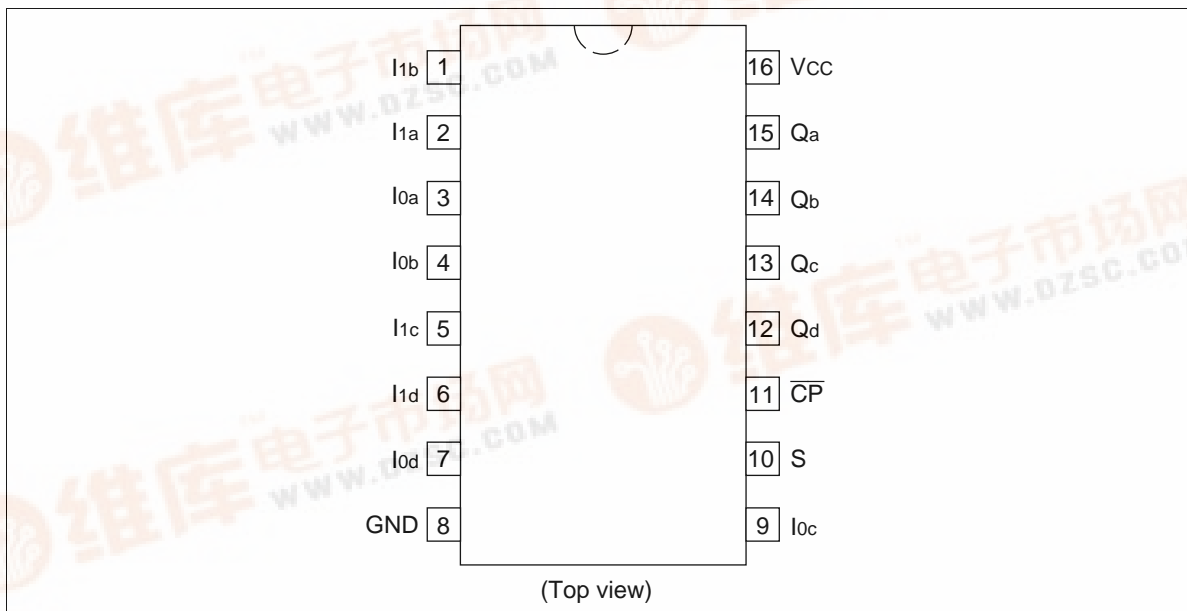
## Description

This device is a high-speed multiplexer with storage. It selects four bits of data from two sources (Ports) under the control of a common Select input (S). The selected data is transferred to the 4-bit output register synchronous with the HIGH-to-LOW transition of the Clock input (CP). The 4-bit register is fully edge triggered. The Data inputs ( $I_0$  and  $I_1$ ) and Select input (S) must be stable only one setup time prior to the HIGH-to-LOW transition of the clock for predictable operation.

## Features

- Outputs Source/Sink 24 mA
- HD74ACT298 has TTL-Compatible Inputs

## Pin Arrangement

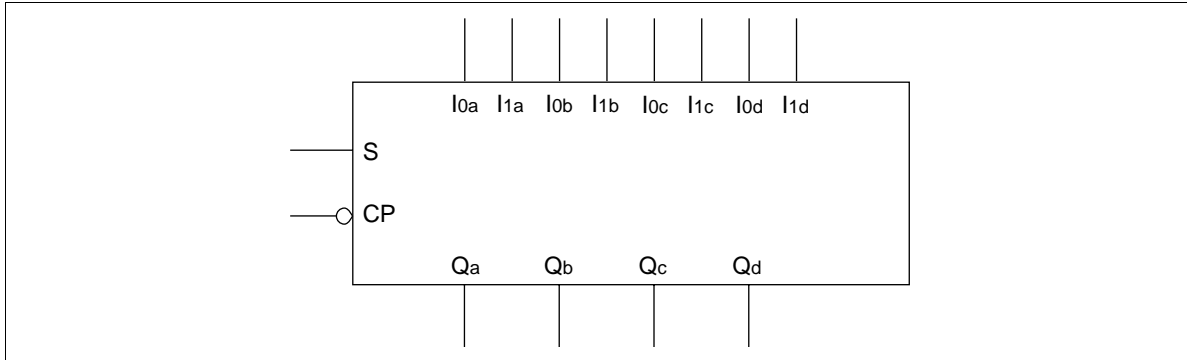


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## HD74AC298/HD74ACT298

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### Logic Symbol



### Pin Names

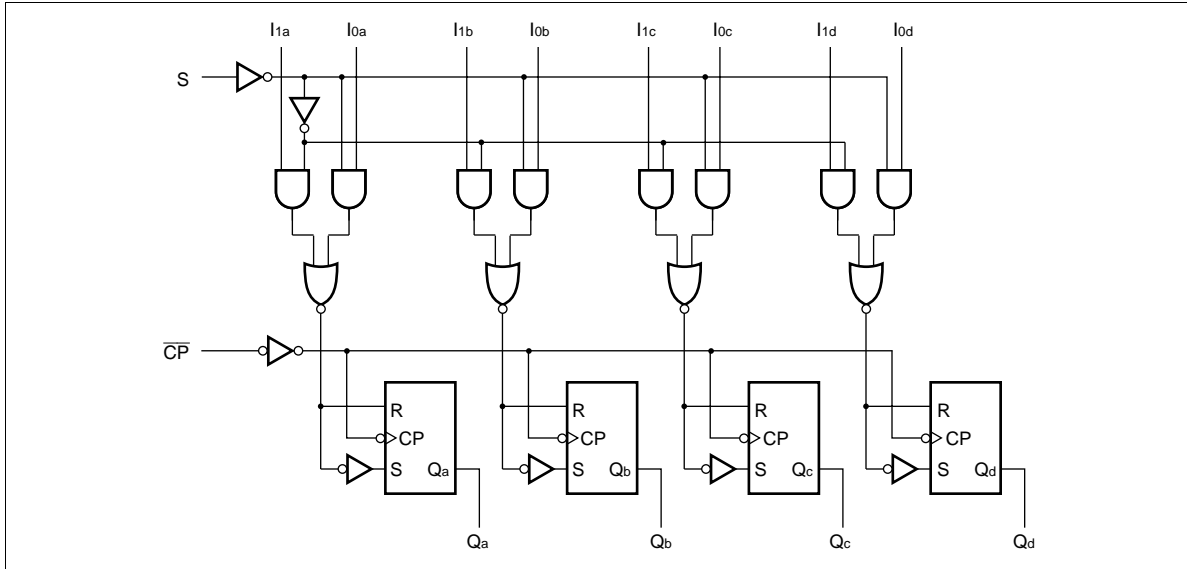
- $I_{1a} - I_{1d}$  Source 1 Data Inputs
- $I_{0a} - I_{0d}$  Source 0 Data Inputs
- S Select Input
- $\overline{CP}$  Clock Pulse Input (Active Falling Edge)
- $Q_a - Q_d$  Outputs

### Function Table

Inputs		Outputs			
S	Cp	$Q_a$	$Q_b$	$Q_c$	$Q_d$
L		$a_1$	$b_1$	$c_1$	$d_1$
H		$a_2$	$b_2$	$c_2$	$d_2$
X	H	$Q_{A0}$	$Q_{B0}$	$Q_{C0}$	$Q_{D0}$

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### Logic Diagram



### DC Characteristics (unless otherwise specified)

Item	Symbol	Max	Unit	Condition
Maximum quiescent supply current	$I_{CC}$	80	$\mu\text{A}$	$V_{IN} = V_{CC}$ or ground, $V_{CC} = 5.5\text{ V}$ , $T_a = \text{Worst case}$
Maximum quiescent supply current	$I_{CC}$	8.0	$\mu\text{A}$	$V_{IN} = V_{CC}$ or ground, $V_{CC} = 5.5\text{ V}$ , $T_a = 25^\circ\text{C}$
Maximum $I_{CC}/\text{input}$ (HD74ACT298)	$I_{CCT}$	1.5	mA	$V_{IN} = V_{CC} - 2.1\text{ V}$ , $V_{CC} = 5.5\text{ V}$ , $T_a = \text{Worst case}$

### AC Characteristics: HD74AC298

Item	Symbol	$V_{CC} (\text{V})^{*1}$	$T_a = +25^\circ\text{C}$ $C_L = 50\text{ pF}$			$T_a = -40^\circ\text{C to } +85^\circ\text{C}$ $C_L = 50\text{ pF}$		Unit
			Min	Typ	Max	Min	Max	
Maximum count frequency	$f_{\text{max}}$	3.3	90	—	—	70	—	MHz
		5.0	110	—	—	100	—	
Propagation delay CP to Q	$t_{\text{PLH}}$	3.3	1.0	7.0	10.0	1.0	11.5	ns
		5.0	1.0	5.0	7.5	1.0	8.5	
Propagation delay CP to Q	$t_{\text{PHL}}$	3.3	1.0	7.5	10.0	1.0	11.5	
		5.0	1.0	5.5	7.5	1.0	8.5	

Note: 1. Voltage Range 3.3 is  $3.3\text{ V} \pm 0.3\text{ V}$   
Voltage Range 5.0 is  $5.0\text{ V} \pm 0.5\text{ V}$

## HD74AC298/HD74ACT298

### AC Operating Requirements: HD74AC298

Item	Symbol	V <sub>CC</sub> (V)* <sup>1</sup>	Ta = +25°C	Ta = -40°C to +85°C		Unit
			C <sub>L</sub> = 50 pF	C <sub>L</sub> = 50 pF		
			Typ	Guaranteed Minimum		
Setup time, HIGH or LOW D or S to CP	t <sub>su</sub>	3.3	4.5	5.5	7.0	ns
		5.0	2.5	4.5	5.5	
Hold time, HIGH or LOW D <sub>n</sub> to CP <sub>n</sub>	t <sub>h</sub>	3.3	-2.0	0.0	0.0	ns
		5.0	-1.0	0.0	0.0	
Hold time, HIGH or LOW S to CP	t <sub>h</sub>	3.3	-3.0	0.5	0.5	ns
		5.0	-1.5	0.5	0.5	
Pulse width	t <sub>w</sub>	3.3	3.0	5.5	7.0	ns
		5.0	3.0	4.5	5.0	

Note: 1. Voltage Range 3.3 is 3.3 V ± 0.3 V  
Voltage Range 5.0 is 5.0 V ± 0.5 V

### AC Characteristics: HD74ACT298

Item	Symbol	V <sub>CC</sub> (V)* <sup>1</sup>	Ta = +25°C			Ta = -40°C to +85°C		Unit
			C <sub>L</sub> = 50 pF			C <sub>L</sub> = 50 pF		
			Min	Typ	Max	Min	Max	
Maximum count frequency	f <sub>max</sub>	5.0	70	—	—	60	—	MHz
Propagation delay CP to Q	t <sub>PLH</sub>	5.0	1.0	6.0	10.0	1.0	11.0	ns
Propagation delay CP to Q	t <sub>PHL</sub>	5.0	1.0	6.5	10.0	1.0	11.0	

Note: 1. Voltage Range 5.0 is 5.0 V ± 0.5 V

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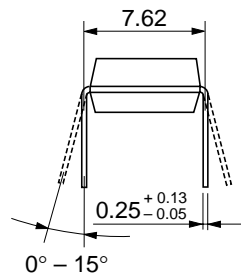
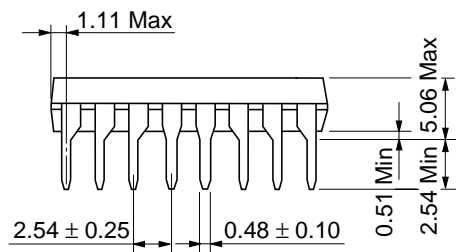
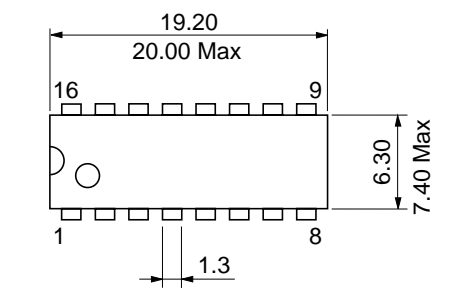
### AC Operating Requirements: HD74ACT298

Item	Symbol	$V_{CC}$ (V)*1	Ta = +25°C	Ta = -40°C		Unit
			Typ	to +85°C		
			$C_L = 50$ pF	$C_L = 50$ pF		
			Typ	Guaranteed Minimum		
Setup time, HIGH or LOW $\bar{D}_n$ or S to CP	$t_{su}$	5.0	3.5	5.5	6.5	ns
Hold time, HIGH or LOW $D_n$ to CP	$t_h$	5.0	-5.0	1.0	1.0	ns
Hold time, HIGH or LOW S to CP	$t_h$	5.0	-0.5	1.0	1.0	ns
Pulse width	$t_w$	5.0	3.0	7.0	8.0	ns

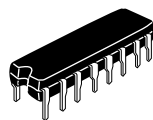
Note: 1. Voltage Range 5.0 is  $5.0\text{ V} \pm 0.5\text{ V}$

### Capacitance

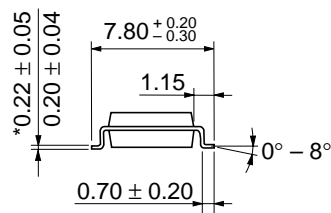
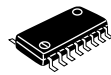
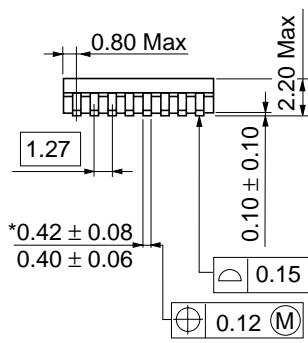
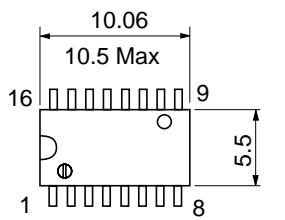
Item	Symbol	Typ	Unit	Condition
Input capacitance	$C_{IN}$	4.5	pF	$V_{CC} = 5.5\text{ V}$
Power dissipation capacitance	$C_{PD}$	30	pF	$V_{CC} = 5.0\text{ V}$



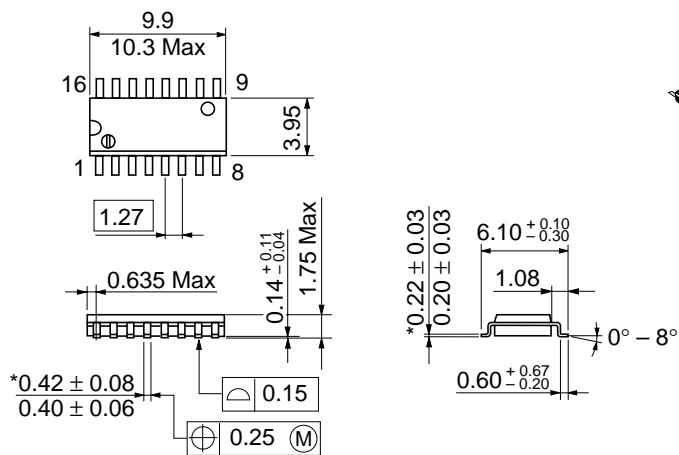
Unit: mm



Unit: mm

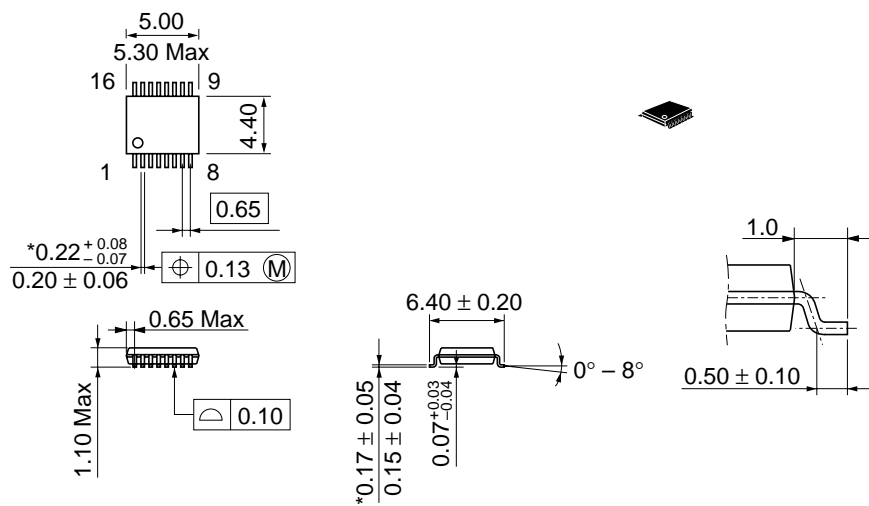


Unit: mm





Unit: mm



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