

HD74ALVC2G53

2-channel Analog Multiplexer Demultiplexer

HITACHI

ADE-205-636B (Z)

Rev. 2
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Description

The HD74ALVC2G53 has 2-channel analog multiplexer / demultiplexer in a 8 pin package. Applications include signal gating, chopping, modulation or demodulation (modem), and signal multiplexing for analog to digital and digital to analog conversion systems. Low voltage and high speed operation is suitable for the battery powered products (e.g., notebook computers), and the low power consumption extends the battery life.

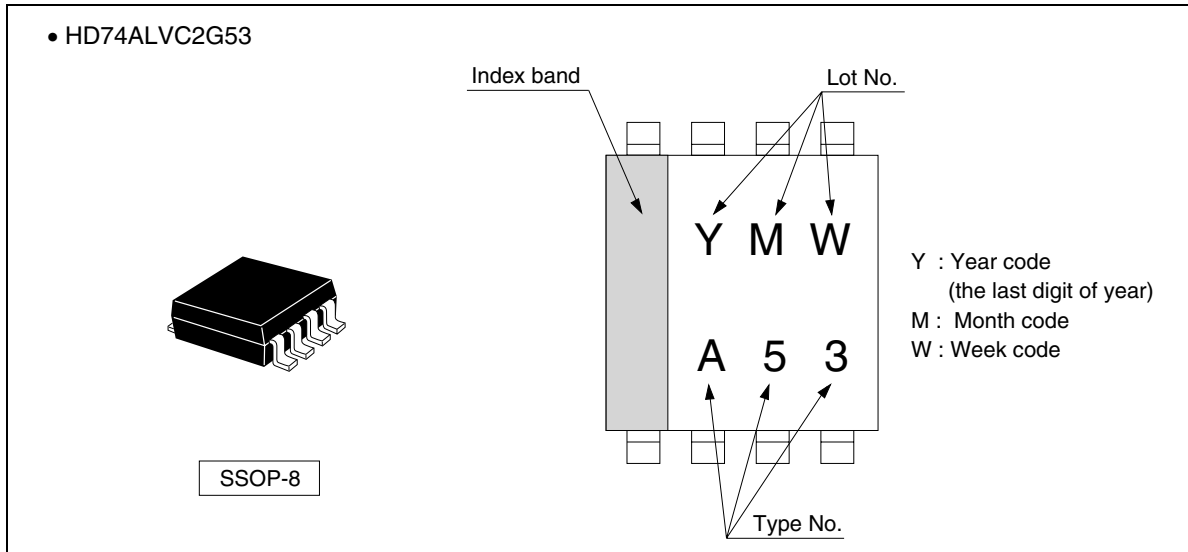
Features

- The basic gate function is lined up as hitachi uni logic series.
- Supplied on emboss taping for high speed automatic mounting.
- Supply voltage range : 1.2 to 3.6 V
Operating temperature range : -40 to +85°C
- All control input V_{IH} (Max.) = 3.6 V (@ V_{CC} = 0 V to 3.6 V)
- Package type

Package type	Package code	Package suffix	Taping code
SSOP-8 pin	TTP-8DB	US	E (3,000 pcs / Reel)

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Outline and Article Indication



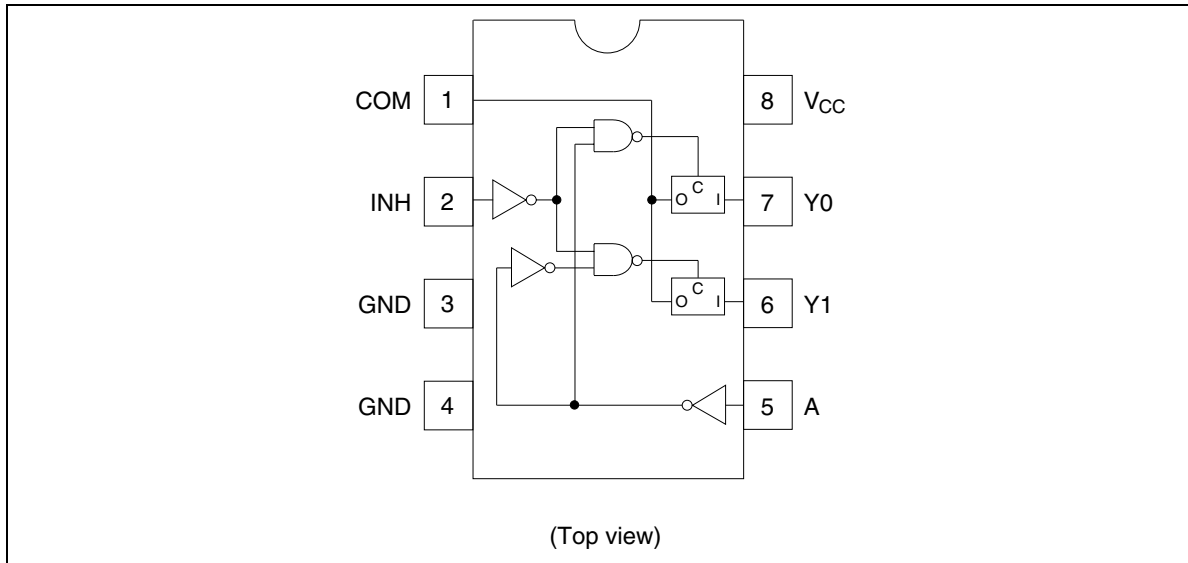
Function Table

Control inputs

INH	A	On channel
H	X	None
L	H	Y1
L	L	Y0

H: High level
L: Low level
X: Immaterial

Pin Arrangement



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Absolute Maximum Ratings

Item	Symbol	Ratings	Unit	Conditions
Supply voltage range	V_{CC}	−0.5 to 4.6	V	
Input voltage range ^{*1}	V_I	−0.5 to 4.6	V	
Output voltage range ^{*1,2}	V_O	−0.5 to $V_{CC}+0.5$	V	Output : H or L
Input clamp current	I_{IK}	−50	mA	$V_I < 0$
Output clamp current	I_{OK}	±50	mA	$V_O < 0$ or $V_O > V_{CC}$
Continuous output current	I_O	±50	mA	$V_O = 0$ to V_{CC}
Continuous current through V_{CC} or GND	I_{CC} or I_{GND}	±100	mA	
Maximum power dissipation at $T_a = 25^\circ\text{C}$ (in still air) ^{*3}	P_T	200	mW	
Storage temperature	T_{stg}	−65 to 150	°C	

Notes: The absolute maximum ratings are values which must not individually be exceeded, and furthermore, no two of which may be realized at the same time.

1. The input and output voltage ratings may be exceeded if the input and output clamp-current ratings are observed.
2. This value is limited to 4.6 V maximum.
3. The maximum package power dissipation was calculated using a junction temperature of 150°C.

Recommended Operating Conditions

Item	Symbol	Min	Max	Unit	Conditions
Supply voltage range	V_{CC}	1.2	3.6	V	
Input voltage range	V_I	0	3.6	V	
Input / output voltage range	V_{IO}	0	V_{CC}	V	
Input transition rise or fall rate	$\Delta t / \Delta v$	0	20	ns / V	$V_{CC} = 1.2$ to 2.7 V
		0	10		$V_{CC} = 3.3 \pm 0.3$ V
Operating free-air temperature	T_a	−40	85	°C	

Note: Unused or floating inputs must be held high or low.

Electrical Characteristics

Item	Symbol	V _{CC} (V)	Ta=25°C			Ta=-40 to 85°C			Unit	Test conditions
			Min	Typ	Max	Min	Typ	Max		
Input voltage	V _{IH}	1.2	—	—	—	V _{CC} ×0.75	—	—	V	Control input only
		1.4 to 1.6	—	—	—	V _{CC} ×0.7	—	—		
		1.65 to 1.95	—	—	—	V _{CC} ×0.7	—	—		
		2.3 to 2.7	—	—	—	1.7	—	—		
		3.0 to 3.6	—	—	—	2.0	—	—		
	V _{IL}	1.2	—	—	—	—	—	V _{CC} ×0.25		
		1.4 to 1.6	—	—	—	—	—	V _{CC} ×0.3		
		1.65 to 1.95	—	—	—	—	—	V _{CC} ×0.3		
		2.3 to 2.7	—	—	—	—	—	0.7		
		3.0 to 3.6	—	—	—	—	—	0.8		
On-state switch resistance	R _{ON}	1.2	—	14	—	—	—	—	Ω	V _I = 0 V, I _O = 1 mA
			—	29	—	—	—	—		V _I = 1.2 V, I _O = 1 mA
		1.4	—	12	25	—	—	30		V _I = 0 V, I _O = 2 mA
			—	21	35	—	—	40		V _I = 1.4 V, I _O = 2 mA
		1.65	—	10	17	—	—	20		V _I = 0 V, I _O = 4 mA
			—	17	27	—	—	30		V _I = 1.65 V, I _O = 4 mA
		2.3	—	8	11	—	—	13		V _I = 0 V, I _O = 8 mA
			—	13	18	—	—	20		V _I = 2.3 V, I _O = 8 mA
		3.0	—	7	9.5	—	—	10		V _I = 0 V, I _O = 24 mA
			—	11	14.5	—	—	15.5		V _I = 3.0 V, I _O = 24 mA
Peak on resistance	R _{ON(p)}	1.2	—	280	—	—	—	—	Ω	I _O = 1 mA
		1.4	—	125	250	—	—	350		I _O = 2 mA
		1.65	—	55	110	—	—	150		I _O = 4 mA
		2.3	—	20	30	—	—	35		I _O = 8 mA
		3.0	—	13	18	—	—	20		I _O = 24 mA
Difference of on-state	ΔR _{ON}	1.2	—	1.0	—	—	—	—	Ω	I _O = 1 mA
		1.4	—	0.8	12	—	—	15		I _O = 2 mA

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Electrical Characteristics (cont)

Item	Symbol	V_{CC} (V)	$T_a=25^{\circ}\text{C}$			$T_a=-40$ to 85°C			Unit	Test conditions
			Min	Typ	Max	Min	Typ	Max		
resistance		1.65	—	0.6	9	—	—	12		$I_o = 4\text{ mA}$
between		2.3	—	0.5	5	—	—	9		$I_o = 8\text{ mA}$
switches		3.0	—	0.2	3	—	—	4		$I_o = 24\text{ mA}$
Off-state switch leakage current	$I_{S(OFF)}$	3.6	—	—	± 0.1	—	—	± 1.0	μA	$V_{IN} = V_{CC}$, $V_{OUT} = \text{GND}$ or $V_{IN} = \text{GND}$, $V_O = V_{CC}$, $V_C = V_{IL}$
On-state switch leakage current	$I_{S(ON)}$	3.6	—	—	± 0.1	—	—	± 1.0	μA	$V_{IN} = V_{CC}$ or GND $V_C = V_{IH}$
Input current	I_{IN}	0 to 3.6	—	—	± 0.1	—	—	± 1.0	μA	$V_{IN} = 3.6\text{ V}$ or GND
Quiescent supply current	I_{CC}	3.6	—	—	—	—	—	10	μA	$V_{IN} = V_{CC}$ or GND
Control input capacitance	C_{IC}	—	—	4.5	—	—	—	—	pF	
Switch terminal capacitance	$C_{IN/OUT}$	—	—	5.5	—	—	—	—	pF	
Feedthrough capacitance	C_{IN-OUT}	—	—	0.2	—	—	—	—	pF	

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Switching Characteristics

(Ta = -40 to 85°C)

- $V_{CC} = 1.2 \text{ V}$

Item	Symbol	Min	Typ	Max	Unit	Test conditions	FROM (Input)	TO (Output)
Propagation delay time ^{*1}	t_{PLH} t_{PHL}	—	0.4	—	ns	$C_L = 15 \text{ pF}$	COM or Yn	Yn or COM
Enable time	t_{ZH} t_{ZL}	—	6.5	—	ns	$C_L = 15 \text{ pF}$	INH	COM or Yn
Disable time	t_{HZ} t_{LZ}	—	7.2	—	ns	$C_L = 15 \text{ pF}$	INH	COM or Yn

- $V_{CC} = 1.5 \pm 0.1 \text{ V}$

Item	Symbol	Min	Typ	Max	Unit	Test conditions	FROM (Input)	TO (Output)
Propagation delay time ^{*1}	t_{PLH} t_{PHL}	—	—	0.3	ns	$C_L = 15 \text{ pF}$	COM or Yn	Yn or COM
Enable time	t_{ZH} t_{ZL}	2.0	—	9.0	ns	$C_L = 15 \text{ pF}$	INH	COM or Yn
Disable time	t_{HZ} t_{LZ}	2.0	—	10.0	ns	$C_L = 15 \text{ pF}$	INH	COM or Yn

- $V_{CC} = 1.8 \pm 0.15 \text{ V}$

Item	Symbol	Min	Typ	Max	Unit	Test conditions	FROM (Input)	TO (Output)
Propagation delay time ^{*1}	t_{PLH} t_{PHL}	—	—	0.48	ns	$C_L = 30 \text{ pF}$	COM or Yn	Yn or COM
Enable time	t_{ZH} t_{ZL}	1.5	—	7.0	ns	$C_L = 30 \text{ pF}$	INH	COM or Yn
Disable time	t_{HZ} t_{LZ}	1.5	—	8.0	ns	$C_L = 30 \text{ pF}$	INH	COM or Yn

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Switching Characteristics (cont)

- $V_{CC} = 2.5 \pm 0.2$ V

Item	Symbol	Min	Typ	Max	Unit	Test conditions	FROM (Input)	TO (Output)
Propagation delay time ^{*1}	t_{PLH} t_{PHL}	—	—	0.35	ns	$C_L = 30$ pF	COM or Yn	Yn or COM
Enable time	t_{ZH} t_{ZL}	1.0	—	5.0	ns	$C_L = 30$ pF	INH	COM or Yn
Disable time	t_{HZ} t_{LZ}	1.0	—	6.0	ns	$C_L = 30$ pF	INH	COM or Yn

- $V_{CC} = 3.3 \pm 0.3$ V

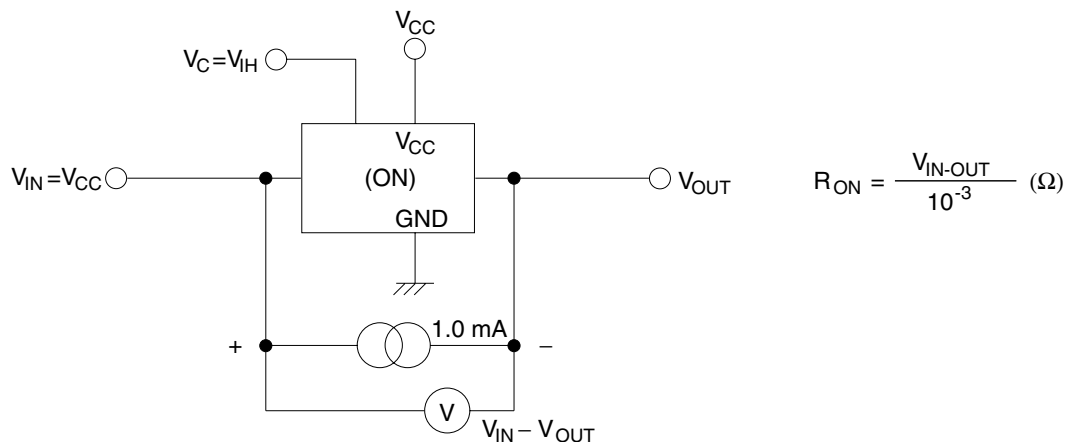
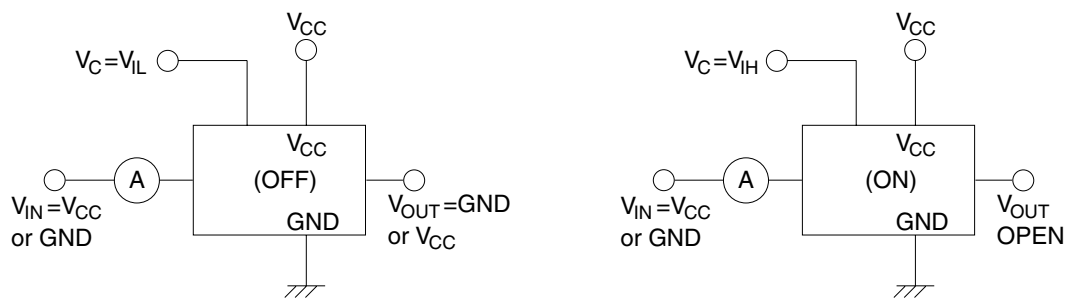
Item	Symbol	Min	Typ	Max	Unit	Test conditions	FROM (Input)	TO (Output)
Propagation delay time ^{*1}	t_{PLH} t_{PHL}	—	—	0.3	ns	$C_L = 30$ pF	COM or Yn	Yn or COM
Enable time	t_{ZH} t_{ZL}	1.0	—	4.0	ns	$C_L = 30$ pF	INH	COM or Yn
Disable time	t_{HZ} t_{LZ}	1.0	—	5.0	ns	$C_L = 30$ pF	INH	COM or Yn

Note: 1. The propagation delay time is calculated by the RC (on-resistance and load capacitance) time constant.

Operating Characteristics

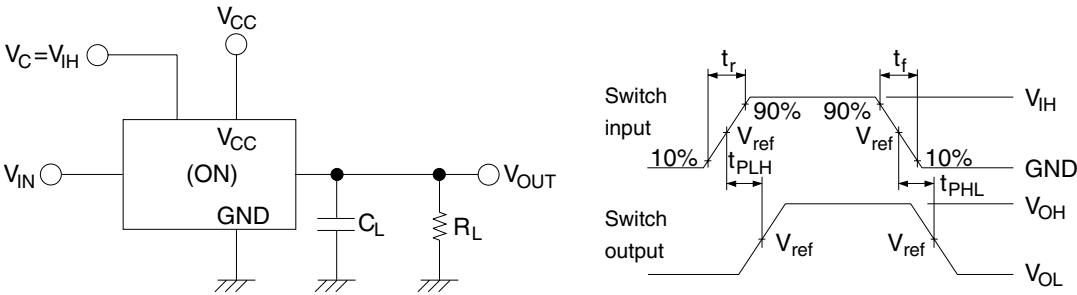
($T_a = 25^\circ\text{C}$)

Item	Symbol	V_{CC} (V)	Min	Typ	Max	Unit	Test conditions
Power dissipation capacitance	C_{PD}	1.5	—	11.5	—	pF	$f = 10$ MHz
		1.8	—	11.5	—		
		2.5	—	12.5	—		
		3.3	—	14.0	—		

Test Circuit
• R_{ON}

• I_S (off), I_S (on)


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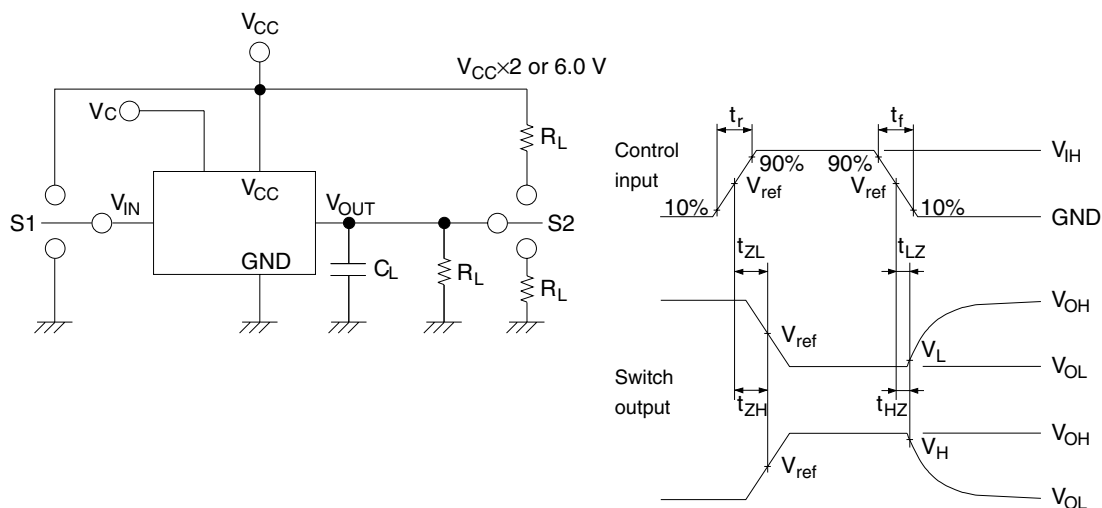
• t_{PLH} , t_{PHL}



Symbol	$V_{CC} = 1.2\text{ V},$ $1.5\pm0.1\text{ V}$	$V_{CC} = 1.8\pm0.15\text{ V}$	$V_{CC} = 2.5\pm0.2\text{ V},$ $3.3\pm0.3\text{ V}$
R_L	2.0 k Ω	1.0 k Ω	500 Ω
C_L	15 pF	30 pF	30 pF

Symbol	$V_{CC} = 1.2\text{ V},$ $1.5\pm0.1\text{ V},$ $1.8\pm0.15\text{ V}$	$V_{CC} = 2.5\pm0.2\text{ V}$	$V_{CC} = 3.3\pm0.3\text{ V}$
t_r / t_f	2.0 ns	2.5 ns	2.5 ns
V_{IH}	V_{CC}	V_{CC}	2.7 V
V_{ref}	50%	50%	1.5 V

- $t_{ZH}, t_{ZL} / t_{HZ}, t_{LZ}$



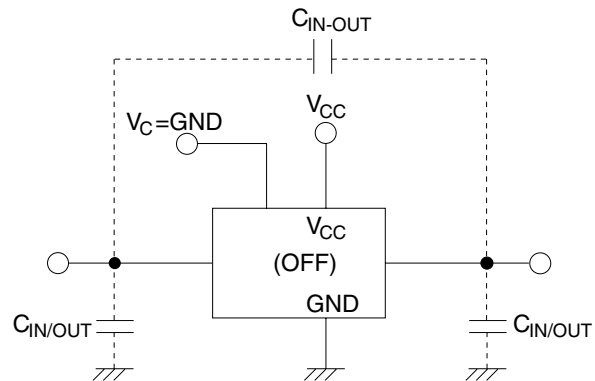
	S1		S2	
Symbol	V _{CC} = 1.2 V, 1.5±0.1 V, 1.8±0.15 V, 2.5±0.2 V	V _{CC} = 3.3±0.3 V	V _{CC} = 1.2 V, 1.5±0.1 V, 1.8±0.15 V, 2.5±0.2 V	V _{CC} = 3.3±0.3 V
t _{HZ} / t _{ZH}	V _{CC}	V _{CC}	GND	GND
t _{LZ} / t _{ZL}	GND	GND	V _{CC} ×2	6.0 V

Symbol	$V_{CC} = 1.2 \text{ V},$ $1.5 \pm 0.1 \text{ V}$	$V_{CC} = 1.8 \pm 0.15 \text{ V}$	$V_{CC} = 2.5 \pm 0.2 \text{ V},$ $3.3 \pm 0.3 \text{ V}$
R_L	2.0 k Ω	1.0 k Ω	500 Ω
C_L	15 pF	30 pF	30 pF

Symbol	$V_{CC} = 1.2 \text{ V},$ $1.5 \pm 0.1 \text{ V}$	$V_{CC} = 1.8 \pm 0.15 \text{ V}$	$V_{CC} = 2.5 \pm 0.2 \text{ V}$	$V_{CC} = 3.3 \pm 0.3 \text{ V}$
t_r / t_f	2.0 ns	2.0 ns	2.5 ns	2.5 ns
V_{IH}	V_{CC}	V_{CC}	V_{CC}	2.7 V
V_{ref}	50%	50%	50%	1.5 V
V_H / V_L	$V_H = V_{OH} - 0.1 \text{ V}$ $V_L = V_{OL} + 0.1 \text{ V}$	$V_H = V_{OH} - 0.15 \text{ V}$ $V_L = V_{OL} + 0.15 \text{ V}$	$V_H = V_{OH} - 0.15 \text{ V}$ $V_L = V_{OL} + 0.15 \text{ V}$	$V_H = V_{OH} - 0.3 \text{ V}$ $V_L = V_{OL} + 0.3 \text{ V}$

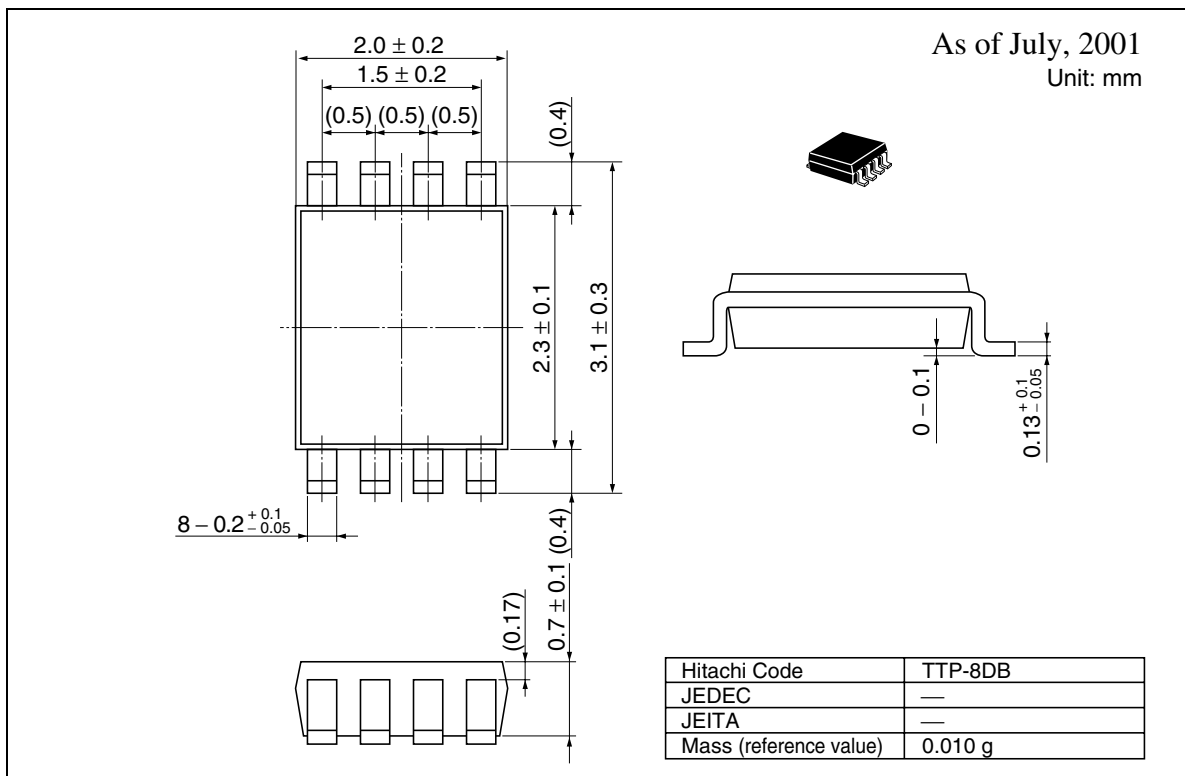
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- $C_{IN/OUT}$, C_{IN-OUT}



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Package Dimensions



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Hitachi, Ltd.

Semiconductor & Integrated Circuits
Nippon Bldg., 2-6-2, Ohte-machi, Chiyoda-ku, Tokyo 100-0004, Japan
Tel: (03) 3270-2111 Fax: (03) 3270-5109

URL <http://www.hitachisemiconductor.com/>

For further information write to:

Hitachi Semiconductor
(America) Inc.
179 East Tasman Drive
San Jose, CA 95134
Tel: <1> (408) 433-1990
Fax: <1> (408) 433-0223

Hitachi Europe Ltd.
Electronic Components Group
Whitebrook Park
Lower Cookham Road
Maidenhead
Berkshire SL6 8YA, United Kingdom
Tel: <44> (1628) 585000
Fax: <44> (1628) 585200

Hitachi Europe GmbH
Electronic Components Group
Dornacher Straße 3
D-85622 Feldkirchen
Postfach 201, D-85619 Feldkirchen
Germany
Tel: <49> (89) 9 9180-0
Fax: <49> (89) 9 29 30 00

Hitachi Asia Ltd.
Hitachi Tower
16 Collyer Quay #20-00
Singapore 049318
Tel: <65>-538-6533/538-8577
Fax: <65>-538-6933/538-3877
URL: <http://semiconductor.hitachi.com.sg>

Hitachi Asia Ltd.
(Taipei Branch Office)
4/F, No. 167, Tun Hwa North Road
Hung-Kuo Building
Taipei (105), Taiwan
Tel: <886>-(2)-2718-3666
Fax: <886>-(2)-2718-8180
Telex: 23222 HAS-TP
URL: <http://www.hitachi.com.tw>

Hitachi Asia (Hong Kong) Ltd.
Group III (Electronic Components)
7/F., North Tower
World Finance Centre,
Harbour City, Canton Road
Tsim Sha Tsui, Kowloon Hong Kong
Tel: <852>-(2)-735-9218
Fax: <852>-(2)-730-0281
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